

Model 750-IPC

Foreground/Background Interface

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282-T06-2L1-01

REVISION HISTORY		
ECO #	DATE	DESCRIPTION
0328	6/28/84	New ZETACO Cover

TABLE OF CONTENTS

SECTION 1	INTRODUCTION
SECTION 2	INSTALLATION
SECTION 3	CONFIGURATION (OPTIONS)
SECTION 4	PROGRAMMING NOTES
SECTION 5	SPECIFICATIONS
SECTION 6	DIAGNOSTICS

1.0 INTRODUCTION

The 750-IPC Controller is a single board controller whose function is to expedite the transition between RDOS Foreground and Background.

Features of the 750-IPC include:

- 1) Selection of any Even, Odd Device Code Pair (i.e. 2,3 or 40,41 or 76,77).
- 2) Selection of any Mask Bits (Data 0 thru Data 15).

The controller occupies one slot in the CPU chassis.

2.0 INSTALLATION INSTRUCTIONS

2.1 UNPACKING

Upon receiving the Interface Package, unpack the contents and inspect the board for visual damage. The package contains:

- 1) Controller Board
- 2) Manual
- 3) Diagnostic Tape

If any damage is apparent, do not attempt to install the controller but notify the Shipper and Custom Systems immediately.

2.2 BOARD INSTALLATION

The controller board may be installed in any General I/O, Memory I/O or I/O Only slot of the Data General Nova or Eclipse Minicomputer.

Verify the Device Code and Mask Bit selection are correct (these are Switch Selectable).

Install the controller in the desired slot, component side up and lock into position with the release levers.

If with the selection of the I/O slot, a vacant slot or slots exist between the controller and the board below it, the DCHP (Data Channel Priority) and the INTP (Interrupt Priority) signals must be physically jumpered on the computer backpanel to maintain priority interrupt continuity. Install one end of a wire-wrap jumper to the DCHP-OUT signal at Pin 93 at the "A" connector occupied by the device below the controller. Connect the remaining end to the DCHP-IN signal at Pin 94 of the "A" connector occupied by the controller, bridging the vacant slot or slots. Similarly, connect the INTP-OUT signal (Pin A-95) from the lower device to the INTP-IN signal at Pin A-96 of the controller. This will complete the priority interrupt continuity to the card. If vacant slots exist between the controller and the device above the controller, perform similar strapping of the DCHP and INTP signals to maintain interrupt priority.

CAUTION: Be sure NO existing cabling or devices are connected to the backplane of the slot the 750-IPC is to be installed in.

3.0 CONFIGURATION (OPTIONS)

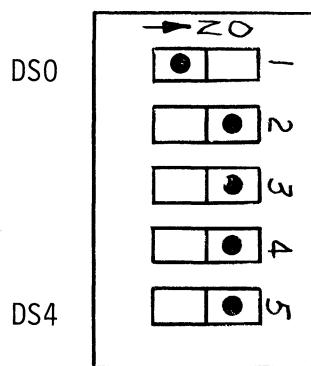
3.1 DEVICE CODE SELECTION

The five position switch at location D2 selects the Even Device Code of the required Even-Odd, Device Code Pair. Use the Table to set the desired Device Code.

DEVICE CODE TABLE

DEVICE CODE	(DS0) S1	(DS1) S2	(DS2) S3	(DS3) S4	(DS4) S5
0X	ON	ON	ON	-	-
1X	ON	ON	OFF	-	-
2X	ON	OFF	ON	-	-
3X	ON	OFF	OFF	-	-
4X	OFF	ON	ON	-	-
5X	OFF	ON	OFF	-	-
6X	OFF	OFF	ON	-	-
7X	OFF	OFF	OFF	-	-
X(0 & 1)	-	-	-	ON	ON
X(2 & 3)	-	-	-	ON	OFF
X(4 & 5)	-	-	-	OFF	ON
X(6 & 7)	-	-	-	OFF	OFF

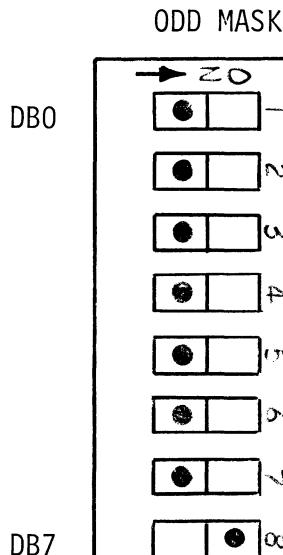
- = Don't Care



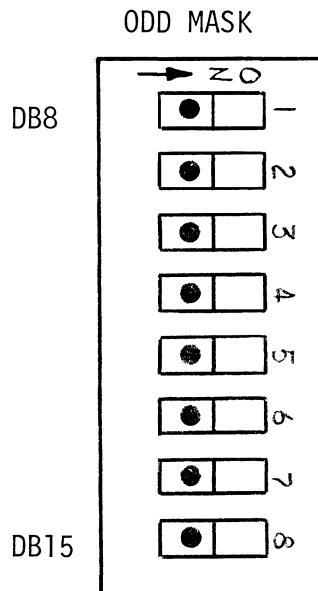
Switch is shown with Position 2, 3, 4 and 5 ON which equals Device Code Selection of 40, 41.

3.2 MASK BIT SELECTION

Switches at Location J3 and K3 select 1 of the 16 possible Mask Bits associated with the Even Device Code and switches at Locations G3 and H3 select 1 of the 16 possible Mask Bits associated with the Odd Device Code. Only one switch position can be ON for the J3, K3 Mask Bit Select Switches, and only one switch position can be ON for the G3, H3 Mask Bit Select Switches. See drawing for further definition.



LOC G3



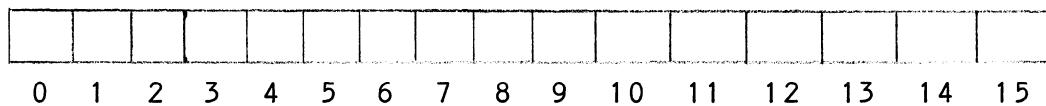
LOC H3

DB0 = Data Bit 0, DB15 = Data Bit 15
Mask Bit 7 shown selected

The Model 750 Inter-Program Communications Device is used with Data General RDOS to provide a software controlled signal between the foreground and background partitions for the purpose of indicating without delay the completion of any task.

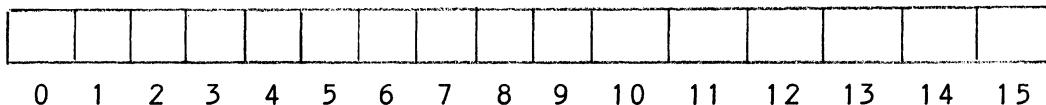
The Device is an I/O Interrupt Generating Device. The Start Command initiates the interrupt sequence, and the Clear Command clears the interrupt. The following Instruction Formats explain the Commands and what results are achieved.

.DOA XX Load Register (Even or Odd Device Code)



Load Bits 0 to 15 into Load Register

.DIA XX Read Register (Even or Odd Device Code)



Read Bits 0 to 15 from Register. DIA reads what DOA Command stored in the Register.

.START (ODD DEVICE CODE)

- a) Sets Odd Done F/F
- b) Sets Odd INTR F/F
- c) Sets INTR F/F if not Masked

An INTA Command returns Bits 10 thru 15 (Device Code) of the Interrupting Device, initiated by the Start Command.

.START (EVEN DEVICE CODE)

- a) Sets the Even Busy F/F
- b) Sets the Even INTR F/F
- c) Sets the INTR F/F if not Masked

An INTA Command returns Bits 10 thru 15 (Device Code) of the Interrupting Device, initiated by the Start Command.

.CLEAR (ODD DEVICE CODE)

- a) Clears Odd Done F/F
- b) Clears Odd INTR F/F

.CLEAR (EVEN DEVICE CODE)

- a) Clears Even Busy F/F
- b) Clears Even INTR F/F

.PULSE (ODD DEVICE CODE)

- a) Clears Odd Done F/F

.PULSE (EVEN DEVICE CODE)

- a) Clears Even Busy F/F

.DOC (ODD DEVICE CODE)

- a) Clears Odd INTR F/F

.DOC (EVEN DEVICE CODE)

- a) Clears Even INTR F/F

.IIRST

- a) Clears Odd, Even Mask F/F
- b) Clears Odd Done F/F, Even Busy F/F
- c) Clears Odd INTR F/F, Even INTR F/F

5.0 SPECIFICATIONS

Power Requirements -

Power is supplied by the minicomputer power supplies.

The board requires only +5 Volt DC.

+5 VDC Current = 1 Amp

Environmental -

Operating Temperature	10 C to 40 C degrees
Operating Humidity	10% to 90% Non-condensing
Non-Operating Temperature	-40 C to 55 C degrees
Non-Operating Humidity	10% to 90%

6.0 DIAGNOSTICS

A diagnostic tape is provided with the 750-IPC which contains Stand-Alone Diagnostics to test the controller board.



Please give us your comments.

Please use this form to send us your comments regarding this Technical Manual. Your input is greatly appreciated! Problems will be promptly addressed and action taken as necessary. If you wish a written reply, please furnish your name and mailing address. Thank you.

Date _____

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ERRORS IN MANUAL:

SUGGESTIONS FOR IMPROVING EITHER THE MANUAL OR THE PRODUCT:



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0001 FEDIG ROS ASSEMBLER REV 94.20

18:02:49 01/10/84

01
02
03
04
05
06
07) DESCRIPTION: DIAGNOSTIC PROGRAM FOR RDOS FOREGROUND/BACKGROUND
08) COMMUNICATION BOARD
09
10)
11) CUSTOM SYSTEMS INC. 1984
12) *****
13) TITLE: FEDIG
14) DSGR: X-1
15)
16) 2. REVISION HISTORY
17)
18) REV. DATE
19) 00 01/10/84
20)
21) 3. MACHINE REQUIREMENTS:
22) 3.1 NOVA/ECLIPSE FAMILY PROCESSOR
23) 3.2 MODEL 750 FOREGROUND/BACKGROUND BOARD
24) 3.3 CONSOLE DEVICE
25)
26) 4. SUMMARY
27) THE FOREGROUND/BACKGROUND BOARD DIAGNOSTIC PERFORMS A GATE BY
28) GATE TEST OF THE BOARD. THE DIAGNOSTIC TESTS THE SETTING AND
29) CLEARING OF THE LANE, BUSY, ODD INTR, EVEN INTR, AND INTR FF'S.
30) IT ALSO TESTS BOTH DATA REGISTERS FOR ALL DATA PATTERNS.
31)

0062 F8D1H
 01 000000 NOMAC 0
 02 ,5 DEBUGGER MAY BE ENTERED VIA JO
 03 , OR PROGRAM TERMINATION
 04
 05 ,6 SWITCHES:
 06 SWPD 6.1
 07
 08
 09 ,7 CONTROL CHARACTERS:
 10 , CTRL R - RESTART PROGRAM, SWITCHES MAINTAINED.
 11 , CTRL D - RESTART PROGRAM, SWITCHES RE-INITIALIZED.
 12 , CTRL O - SEND PROGRAM TO UOTRL DEBUGGER.
 13
 14 . MACRO MMESS
 15 LDA \$015WREG
 16 LDI 1,048
 17 AND# 0,1,52R ;SWR R ON
 18 JMP +4 ;YES, ALWAYS PRINT
 19 LDR 0,FRS7S
 20 MWRI 0,0,52R ;FIRST MSG
 21 JMP +3 ;NO, DON'T PRIN
 22 JSR @IMES?
 23 ?1
 24 X
 25 000012 1=10.
 26
 27 000000 LOC 0
 28 000000 000002 LOC0: 0
 29 00001 000000 LOC1: 2 ;100MM
 30 00002 000200 P1025B
 31 00003 002002 JMP 0,-1
 32 00004 000000 2
 33 00005 000000 0
 34 00006 002000 110MM: JMP 00
 35
 36 000043 LOC 40
 37 000045 LOC 45
 38 000045 001760 E055
 39 000050 LOC 50
 40 000040 000010 BLK 10
 41 000060 000004 BLK 4
 42
 43 000064 001180 XD0AS D0RS 0,0 ;LOADS ADC INTO DATA REG AND SETS DONE OR BUSY AND INTR FF'S
 44 000065 001000 XD0AB: D0R 0,0 ;LOADS ADC INTO DATA REG
 45 000066 004600 XD1AC: D1HC 1,0 ;LOADS DATA REG INTO H1 AND CLEARS DONE OR BUSY AND INTR FF'S
 46 000067 004400 XD1A1: D1R 1,0 ;LOADS DATA REG INTO H1
 47 000070 003900 XDUC0: D0C 0,0 ;CLEAR INTR FF DOES NOT AFFECT DONE OR BUSY FF'S
 48 000071 003500 XSBZ: SKP#2 0 ;SKP#2 INSTR. FOR TESTING EVEN CODE
 49 000072 003400 XSPN: SKP#1 0 ;SKP#1 INSTR. FOR TESTING EVEN CODE
 50 000073 003700 XSDZ: SKPD#2 0 ;SKPD#2 INSTR. FOR TESTING ODD CODE
 51 000074 001600 XSLN: SKPD#1 0 ;SKPD#1 INSTR. FOR TESTING ODD CODE
 52 000075 000100 XN105: N105 0 ;N105 INSTR. START SETS DONE OR BUSY AND INTR FF'S
 53 000075 000100 XN10P: N10P 0 ;N10P INTR. CLEARS DONE OR BUSY FF DOES NOT AFFECT INTR FF'S
 54 000077 000200 XN10C: N10C 0 ;N10C INTR. CLEARS DONE OR BUSY AND INTR FF'S
 55 00100 000000 DC1: 0 ;DEVICE CODE OF BOARD
 56 00101 000000 DC2: 0 ;DEVICE CODE OF OTHER BOARD
 57 00102 000000 1STDC: 0 ;DEVICE CODE CURRENTLY BEING TESTED
 58 00103 000000 MASKE: 0 ;MASK BIT FOR EVEN ADDRESS
 59 00104 000000 MASKO: 0 ;MASK BIT FOR ODD ADDRESS
 60 00105 000000 1DUMM: 1DUMM

0003 FBDIA
01
02 00166 00X031 MULI: MULI
03 00197 000011 DIV7
04 00110 002019 OUTLN: POP : LOCATION OF 4 SPACES AFTER NUMBER OUTPUT
05
06 00111 000000 DATA0: 0 , WRITE DATA
07 00112 000000 DATA0: 0
08 00113 000001 DATA1: 1
09 00114 000000 CHNG: 0
10 00115 000000 TEMP: 0
11 , ADDRESSES
12
13 PGM00 BEB1,K,J,1,200,70000,1
14
15 00233 000167 10D1? 00100
16 00234 000200 RESET: JMP 200
17
18
19 00235 000000 10M20: 0
20 00236 002175 IT1D: 1110
21 00237 002140 IT11: 1111
22 00240 002645 IT1H: T1N2H
23 006231 LOOP%: JSR# 1CYC : , DELAYED LOOP
24 006230 EHAlt=JSR# IERR?
25 000401 NOP=401
26 00241 000000 C0: 3
27 00242 000004 C4: 4
28 00243 000005 C5: 5
29 00244 000011 C9: 9
30 00245 000040 C40: 40
31 00246 000076 C76: 76
32 00247 000144 C144: 144
33 00250 076000 C76K: 76000
34 00251 100000 C100K: 100000
35 00252 177774 CM4: -4
36 00253 177767 CM9: -9
37 00254 177400 CM256: -256
38 00255 177000 CM312: -312
39 000500 , LOC 500
40
41

```

10004 FED18
01
02 ;=====
03 ;MAIN PROGRAM CODE
04 ;=====
05
06 000000 D0MAC 0 ;ALWAYS SHOW OUR STUFF
07
08 BEG1:
09 DMESS D1R1 ;TITLE MESSAGE
10 00000 006215 JSR @1MES? ;DISPLAY A MESSAGE
11 00501 004773 D1RT ;ADDR. OF MESSAGE
12 DMESS MSLCR ;LF, CR
13 00502 006215 JSR @1MES? ;DISPLAY A MESSAGE
14 00503 003766 MSLCR ;ADDR. OF MESSAGE
15 DEV1:
16 DMESS MS0002 ;DISPLAY A MESSAGE
17 00504 006215 JSR @1MES? ;DISPLAY A MESSAGE
18 00505 004632 MS0002 ;ADDR. OF MESSAGE
19 00506 006227 JSR @1TT11 ;GET DEVICE CODE FOR BOARD
20 00507 000775 JMP DEV1 ;TRY AGAIN IF END
21 00510 125220 MOVZR 1,1
22 00511 125120 MOVZL 1,1 ;MAKE IT AN EVEN DEVICE CODE
23 00512 044100 STA 1,0C1 ;OTHER DEVICE CODE IS ONE GREATER
24 00513 131400 INC 1,2
25 00514 050101 STH 2,0C2
26 DMESS MS0004
27 00515 006215 JSR @1MES? ;DISPLAY A MESSAGE
28 00516 004130 MS0004 ;ADDR. OF MESSAGE
29 00517 024100 LDR 1,0C1 ;DISPLAY DEVICE CODE 1
30 00520 006222 JSR @1Z0C71 ;TEST DEVICE CODE 1 FIRST
31 00521 044102 STA 1,7S1DC
32 DMESS MS0005
33 00522 006215 JSR @1MES? ;DISPLAY A MESSAGE
34 00523 004154 MS0005 ;ADDR. OF MESSAGE
35 00524 024101 LDR 1,0C2 ;DISPLAY DEVICE CODE 2
36 00525 006223 JSR @1Z0C71
37 DMESS MSLCR
38 00526 006215 JSR @1MES? ;DISPLAY A MESSAGE
39 00527 003766 MSLCR ;ADDR. OF MESSAGE
40 GMSKE: DMESS MS0006
41 00530 006215 JSR @1MES? ;DISPLAY A MESSAGE
42 00531 003776 MS0006 ;ADDR. OF MESSAGE
43 00532 006236 JSR @1TT0 ;GET EVEN MASK BIT
44 00533 000775 JMP GMSKE
45 00534 124000 COM 1,1
46 00535 102621 SUBZR 0,0,SKP
47 00536 101220 EMMB: MOVZR 0,0
48 00537 125404 INC 1,1,SKR ;MAKE MASK FROM OCTAL NUMBER
49 00540 000776 JMP EMMB
50 00541 040103 STR 0,MASKE ;EVEN MASK
51 GMASK0: DMESS MS0001
52 00542 006215 JSR @1MES? ;DISPLAY A MESSAGE
53 00543 004621 MS0001 ;ADDR. OF MESSAGE
54 00544 006236 JSR @1TTD ;GET ODD MASK BIT
55 00545 000775 JMP GMASK0
56 00546 124000 COM 1,1
57 00547 102621 SUBZR 0,0,SKP
58 00550 101220 OHMB: MOVZR 0,0
59 00551 125404 INC 1,1,SKR ;MAKE MASK
60 00552 000776 JMP OHMB

```

0000 PBDIA
 01 00032 040104 STB 0, MASK0
 02 00054 102400 D0: SUB 0, R
 03 00055 040202 STA 0, PH2S ;ZERO PASS COUNT
 04 00056 020105 D0Z: LDA 0, 10UM
 05 00057 040001 STA 0, LOC1 ;SETUP DUMMY VECTOR
 06 00058 020200 LDA 0, C70K ;DELAY FOR INPUT
 07 00061 040211 STA 0, ERR24
 08 00062 024100 LDA 1, 001
 09 00063 030071 LDA 2, XSB2 ;MASKB2 CONTAINS INSTR. SKPBZ
 10 00064 130000 ADD 1, 2 ;PUT DEVICE CODE INTO INSTRUCTION
 11 00065 050405 STA 2, B0B2 ;STORE SKPBZ IN NEXT LOC.
 12 00066 000226 R0: JSR# IENT?
 13 00067 000012 I
 14 00070 062677 I0R31 ;RESET 170
 15 00071 030401 LDA 2, B0B2 ;FOR SHOW
 16 00072 063500 B0B2: SKPBZ 0 ;EXECUTE IT
 17 00073 000230 EHRLT
 18 00074 000231 LOOPX
 19
 20 00075 024101 LDA 1, 002
 21 00076 020003 LDA 2, XED2 ;MASKD2 CONTAINS INSTR. SKRDZ
 22 00077 130000 ADD 1, 2 ;PUT DEVICE CODE IN INSTRUCTION
 23 00080 050405 STA 2, D0D2 ;STORE SKRDZ
 24 00081 000226 D0: JSR# IENT?
 25 00082 000012 I
 26 00083 062677 I0R31 ;FOR SHOW
 27 00084 030401 LDA 2, D0D2 ;FOR SHOW
 28 00085 063700 D0D2: SKRDZ 0 ;EXECUTE IT
 29 00086 000230 EHRLT
 30 00087 000231 LOOPX
 31
 32
 33 TMESS MF-000
 34 00010 022213 LDA 0, 01SWR7E0
 35 00011 024245 LDA 1, C48
 36 00012 107414 HNOT 0, 1, S2R ;SWT A ON
 37 00013 000404 JMP , +4 ;YES, ALWAYS PRINT
 38 00014 220203 LDA 0, PH52S
 39 00015 101014 HMOV# 0, 0, S2R ;FIRST PASS
 40 00016 000403 JMP , +3 ;NO, DON'T PRINT
 41 00017 000215 JSR #01MES2
 42 00020 004284 MF-000
 43
 44 00021 024100 LDA 1, 001 ;EVEN DEVICE CODE
 45 00022 030075 LDA 2, XN105 ;N105 TO PROPER DEVICE CODE
 46 00023 130000 ADD 1, 2
 47 00024 050407 STA 2, R15
 48 00025 030072 LDA 2, XSEN
 49 00026 130000 ADD 1, 2
 50 00027 000407 STA 2, R1BN
 51 00030 000226 R1: JSR# IENT?
 52 00031 000012 I
 53 00032 062677 I0R31 ;N105 TO EVEN DEVICE CODE HERE
 54 00033 000100 R15: N105 0 ;N105 TO EVEN DEVICE CODE HERE
 55 00034 024777 LDH 1, R15 ;FOR SHOW
 56 00035 030401 LDA 2, R1BN
 57 00036 063490 R1BN: SKFBN 0 ;SKBN TO EVEN DEVICE CODE
 58 00037 000230 EHRLT
 59 00040 000231 LOOPX
 60

0006 FBD1A
 01 00641 024101 LDR 1, DC2
 02 00642 030075 LDR 2, XN105
 03 00643 133000 ADD 1, 2
 04 00644 050407 STA 2, R25
 05 00645 030074 LDR 2, XSDN
 06 00646 133000 ADD 1, 2
 07 00647 050407 STA 2, RCDN
 08 00650 006226 R2: JSR# IENT?
 09 00651 000012 I
 10 00652 062677 T0RS1
 11 00653 060100 R25: N105 0 ;N105 TO ODD DEVICE CODE GOES HERE
 12 00654 024777 LDR 1, R25 ;FOR SHOW
 13 00655 030401 LDR 2, R2DN
 14 00656 063606 R2DN: SKPBN 0 ;SKPBN TO ODD DEVICE CODE GOES HERE
 15 00657 006230 EHAlt1
 16 00660 006231 LOOPX
 17
 18 TMESS MF001
 19 00661 022213 LDR 0, 01SHR7EG
 20 00662 024240 LDR 1, C40
 21 00663 107414 AND# 0, 1, SZR ;SWT R ON
 22 00664 000404 JMP .+4 ;YES, ALWAYS PRINT
 23 00665 020203 LDR 0, PHSYS
 24 00666 101014 MOV# 0, 0, SZR ;FIRST PASS
 25 00667 000403 JMP .+3 ;NO, DON'T PRINT
 26 00670 006215 JSR #TIMES?
 27 00671 004322 MF001
 28
 29 00672 024100 LDR 1, DC1 ;EVEN DEVICE CODE
 30 00673 030075 LDR 2, XN105 ;N105 TO PROPER DEVICE CODE
 31 00674 133000 ADD 1, 2
 32 00675 050412 STA 2, R25
 33 00676 0200072 LDR 2, XSBN
 34 00677 133000 ADD 1, 2
 35 00706 050410 STA 2, R3BN
 36 00701 0200071 LDR 2, XSBZ
 37 00702 133000 ADD 1, 2
 38 00703 050411 STA 2, R3BZ
 39 00704 006226 R3: JSR# IENT?
 40 00705 000012 I
 41 00706 062677 T0RS1
 42 00707 060100 R35: N105 0 ;N105 TO EVEN DEVICE CODE HERE
 43 00710 063400 R3BN: SKPBN 0 ;SKPBN TO EVEN DEVICE CODE HERE
 44 00711 000777 JMP .-1 ;WAIT FOR BUSY TO SET
 45 00712 062677 R31R: T0RS1
 46 00713 0200774 LDR 0, R25 ;FOR SHOW
 47 00714 024776 LDR 1, R31R
 48 00715 030401 LDR 2, R3BZ
 49 00716 063500 R3BZ: SKPBZ 0 ;SKPBZ TO EVEN DEVICE HERE
 50 00717 006230 EHAlt1
 51 00720 006231 LOOPX
 52
 53 00721 024101 LDR 1, DC2
 54 00722 030075 LDR 2, XN105
 55 00723 133000 ADD 1, 2
 56 00724 050412 STA 2, R45
 57 00725 030074 LDR 2, XSDN
 58 00726 133000 ADD 1, 2
 59 00727 050410 STA 2, R4DN
 60 00730 030073 LDR 2, XSDY

0007 FEDIA
 01 00731 133000 ADD 1,2
 02 00732 050413 STA 2,R4D2
 03
 04 00733 006226 R4: JSR# IENT?
 05 00734 000012 1
 06 00735 062677 10RST
 07 00736 060100 R4S: NIOS 0 ;N10S TO ODD DEVICE CODE GOES HERE
 08 00737 063600 R4DN: SKPON 0 ;SKPON TO ODD DEVICE CODE GOES HERE
 09 00740 000077 JMP -1 ;WAIT FOR DONE SET
 10 00741 062677 R4IR: 10RST
 11 00742 020774 LDA 0,R4S ;FOR SHOW
 12 00743 024776 LDA 1,R4IR
 13 00744 030401 LDA 2,R4D2
 14 00745 063700 R4DZ: SKPDZ 0 ;SKPDZ TO ODD DEVICE CODE GOES HERE
 15 00746 006236 EHALT
 16 00747 006231 LOOPX
 17
 18 TMESS MF002
 19 00750 022213 LDA 0,01SWR7EG
 20 00751 024245 LDA 1,040
 21 00752 107414 AND# 0,1,\$2R ;SWT R ON
 22 00753 000404 JMP ,+4 ;YES, ALWAYS PRINT
 23 00754 020206 LDA 0,PAS0S
 24 00755 101014 MOV# 0,0,\$2R ;FIRST PASS
 25 00756 000003 JMP ,+3 ;NO, DON'T PRINT
 26 00757 006210 JSR 0IMES?
 27 00760 004245 MF002
 28 00761 024106 LDA 1,0C1 ;EVEN DEVICE CODE
 29 00762 020075 LDA 2,XN10S ;N10S TO PROPER DEVICE CODE
 30 00763 133000 ADD 1,2
 31 00764 050413 STA 2,R5S
 32 00765 020072 LDA 2,XSDN
 33 00766 133000 ADD 1,2
 34 00767 050413 STA 2,H5BN
 35 00770 030077 LDA 2,XN10C
 36 00771 133000 ADD 1,2
 37 00772 050412 STA 2,R5C
 38 00773 020071 LDA 2,XSBZ
 39 00774 133000 ADD 1,2
 40 00775 050412 STA 2,R5BZ
 41 00776 006226 R5: JSR# IENT?
 42 00777 000012 1
 43 01000 062677 10RST
 44 01001 060100 R5S: NIOS 0 ;N10S TO EVEN DEVICE CODE HERE
 45 01002 063400 R5BN: SKPBN 0 ;SKPBN TO EVEN DEVICE CODE HERE
 46 01003 000077 JMP -1 ;WAIT FOR BUSY TO SET
 47 01004 060200 R5C: N10C 0 ;N10C TO EVEN DEVICE CODE HERE
 48 01005 020774 LDA 0,R5S ;FOR SHOW
 49 01006 024776 LDA 1,R5C
 50 01007 030401 LDA 2,R5BZ
 51 01010 063500 R5BZ: SKPBZ 0 ;SKPBZ TO EVEN DEVICE HERE
 52 01011 006236 EHALT
 53 01012 006231 LOOPX
 54
 55 01013 024101 LDA 1,0C2
 56 01014 020075 LDA 2,XN10S
 57 01015 133000 ADD 1,2
 58 01016 050415 STA 2,R6S
 59 01017 030074 LDA 2,XSDN
 60 01018 133000 ADD 1,2

0006 FB018
 01 01021 050413 STA 2, R6DN
 02 01022 030077 LDA 2, XN10C
 03 01023 133000 ADD 1, 2
 04 01024 050412 STA 2, R6C
 05 01025 030073 LDA 2, X502
 06 01026 133000 ADD 1, 2
 07 01027 050413 STA 2, R6D2
 08
 09 01030 006226 R6: JSR# IENT?
 10 01031 000012 I
 11 01032 062677 10R\$1
 12 01033 060100 R6S: NIOS 0 ;NIOS TO ODD DEVICE CODE GOES HERE
 13 01034 063600 R6DN: SKPEN 0 ;SKPEN TO ODD DEVICE CODE GOES HERE
 14 01035 000777 JMP .-1 ;WAIT FOR DONE SET
 15 01036 060200 R6C: NI0C 0 ;NI0C TO ODD DEVICE CODE GOES HERE
 16 01037 020774 LDA 0, R66 ;FOR SHOW
 17 01040 024776 LDA 1, R6C
 18 01041 030401 LDA 2, R6D2
 19 01042 063700 R6D2: SKP02 0 ;SKP02 TO ODD DEVICE CODE GOES HERE
 20 01043 006230 EHAlt
 21 01044 006231 LOOPX
 22
 23 1MESS NF603
 24 01045 022213 LDA 0, 01SWR7EG
 25 01046 024245 LDH 1, C4B
 26 01047 107414 AND# 0, 1, S2R ;SWT R ON
 27 01050 000404 JMP .+4 ;YES, ALWAYS PRINT
 28 01051 020203 LDA 0, PASPS
 29 01052 101014 MOV# 0, 0, S2R ;FIRST PASS
 30 01053 000403 JMP .+3 ;NO, DON'T PRINT
 31 01054 006215 JSR @IMES?
 32 01055 004373 MF003
 33
 34 01056 024100 LDA 1, D01 ;EVEN DEVICE CODE
 35 01057 000073 LDA 2, XN10S ;NIOS TO PROPER DEVICE CODE
 36 01060 133000 ADD 1, 2
 37 01061 050415 STA 2, R7S
 38 01062 030072 LDA 2, X5BN
 39 01063 133000 ADD 1, 2
 40 01064 050413 STA 2, R7BN
 41 01065 030076 LDA 2, XN10P
 42 01066 133000 ADD 1, 2
 43 01067 050412 STA 2, R7P
 44 01070 030071 LDR 2, X5BZ
 45 01071 133000 ADD 1, 2
 46 01072 050413 STA 2, R7BZ
 47 01073 006226 R7: JSR# IENT?
 48 01074 000012 I
 49 01075 062677 10R\$1
 50 01076 060100 R7S: NIOS 0 ;NIOS TO EVEN DEVICE CODE HERE
 51 01077 063400 R7BN: SKPEN 0 ;SKPEN TO EVEN DEVICE CODE HERE
 52 01080 000777 JMP .-1 ;WAIT FOR BUSY TO SET
 53 01081 060300 R7P: NIOP 0 ;NIOP TO EVEN DEVICE CODE HERE
 54 01082 020774 LDA 0, R7S ;FOR SHOW
 55 01083 024776 LDA 1, R7P
 56 01084 030401 LDA 2, R7BZ
 57 01105 063500 R7BZ: SKPBZ 0 ;SKPBZ TO EVEN DEVICE HERE
 58 01106 006230 EHAlt
 59 01107 006231 LOOPX

0009 FED1A
 01 01110 024101 LDA 1, DC2
 02 01111 030075 LDA 2, XN10S
 03 01112 133000 ADD 1, 2
 04 01113 050415 STA 2, A86
 05 01114 030074 LDA 2, XSDW
 06 01115 133000 ADD 1, 2
 07 01116 050413 STA 2, A8DN
 08 01117 030076 LDR 2, XN10P
 09 01120 133000 ADD 1, 2
 10 01121 050412 STA 2, A8P
 11 01122 030072 LDR 2, XSDZ
 12 01123 133000 ADD 1, 2
 13 01124 050413 STA 2, A8DZ
 14
 15 01125 006226 A8: JSR# IEN? ?
 16 01126 000012 I
 17 01127 062677 10RS1
 18 01128 060100 A85 N10S 0 ; NIOS TO ODD DEVICE CODE GOES HERE
 19 01131 063680 A8DN: SKPDN 0 ; SKPDN TO ODD DEVICE CODE GOES HERE
 20 01132 000777 JMP , -1 ; WAIT FOR DONE SET
 21 01133 060300 A8P: N10P 0 ; N10P TO ODD DEVICE CODE GOES HERE
 22 01134 020774 LDA 0, A85 ; FOR SHOW
 23 01135 024776 LDA 1, A8P
 24 01136 030401 LDA 2, A8DZ
 25 01137 063700 A8DZ: SKPDZ 0 ; SKPDZ TO ODD DEVICE CODE GOES HERE
 26 01140 006230 EHAI1
 27 01141 006231 LOOPX
 28
 29 TMESS MF004
 30 01142 022213 LDA 0, 015WR7E6
 31 01143 024245 LDA 1, C48
 32 01144 107414 AND# 0, 1, S2R ; SWT R ON
 33 01145 000404 JMP +4 ; YES, ALWAYS PRINT
 34 01146 020203 LDA 0, PA575
 35 01147 101014 MOV# 0, 0, S2R ; FIRST PAGE
 36 01150 000403 JMP , +3 ; NO, DON'T PRINT
 37 01151 006215 JSR 01MES?
 38 01152 004426 MF004
 39
 40 01153 006226 A9: JSR# IEN? ?
 41 01154 000012 I
 42 01155 062677 A91R: 10RS1
 43 01156 024777 LDA 1, A91R ; FOR SHOW
 44 01157 030401 LDR 2, A91H
 45 01159 061477 A91A: INTA 0 ; GET ANY DEVICE'S INT CODE
 46 01161 101014 MOV# 0, 0, S2R ; NONE SHOULD BE THERE
 47 01162 006230 EHAI1
 48 01163 006231 LOOPX
 49
 50 01164 024100 LDA 1, DC1
 51 01165 030075 LDA 2, XN10S
 52 01166 133000 ADD 1, 2
 53 01167 050404 STA 2, A10S
 54 01170 006226 A10: JSR# IEN? ?
 55 01171 000012 I
 56 01172 062677 10RS1
 57 01173 060300 A10S: N10P 0 ; NIOS TO EVEN DEVICE CODE GOES HERE
 58 01174 000401 NOP
 59 01175 000401 NOP
 60 01176 000401 NOP

0010 FDIR
 01 0117 024100 LDA 1, DC1 ; EVEN DEVICE CODE
 02 01200 030401 LDA 2, R1018 ; FOR SHOW
 03 01201 061477 R1018: INTA 0 ; GET DEVICE CODE
 04 01202 106414 SUB# 0, 1, \$2R ; SAME?
 05 01203 006236 EHALT ; NO
 06 01204 006231 LOOPX
 07
 08 01205 024101 LDA 1, DC2
 09 01206 030401 LDA 2, XN105
 10 01207 133000 ADD 1, 2
 11 01210 050404 STA 2, R115
 12 01211 006226 R11: JSR# IEN?
 13 01212 000012 I
 14 01213 062677 10RS1
 15 01214 060100 R115 NIOS 0 ; NIOS TO GDD DEVICE CODE GOES HERE
 16 01215 000401 NOP
 17 01216 000401 NOP
 18 01217 000401 NOP
 19 01220 024101 LDA 1, DC2 ; GDD DEVICE CODE
 20 01221 030401 LDA 2, R111A ; FOR SHOW
 21 01222 061477 R111A: INTA 0 ; GET DEVICE CODE
 22 01223 106414 SUB# 0, 1, \$2R ; SAME?
 23 01224 006236 EHALT ; NO
 24 01225 006231 LOOPX
 25
 26 TMESS MF005
 27 01226 022213 LDA 0, #1SWR7E0
 28 01227 024245 LDA 1, C48
 29 01230 107414 AND# 0, 1, \$2R ; SWT A ON
 30 01231 000404 JMP , +4 ; YES, ALWAYS PRINT
 31 01232 020203 LDA 0, PAS05
 32 01233 101014 MOVE# 0, 0, \$2R ; FIRST PASS
 33 01234 000403 JMP , +3 ; NO, DON'T PRINT
 34 01235 006215 JSR @IMES?
 35 01236 004401 MF005
 36 01237 024100 LDA 1, DC1
 37 01240 030401 LDA 2, XN105
 38 01241 133000 ADD 1, 2
 39 01242 050406 STA 2, R125
 40 01243 006226 R12: JSR# IEN?
 41 01244 000012 I
 42 01245 062677 10RS1
 43 01246 102400 SUB 0, 0
 44 01247 040000 STA 0, LOC0 ; CLEAR LOC 0
 45 01250 060100 R125: NIOS 0 ; NIOS TO EVEN DEVICE CODE GOES HERE
 46 01251 060177 INTEN
 47 01252 000401 NOP
 48 01253 000401 NOP
 49 01254 000401 NOP
 50 01255 030401 LDA 2, R125 ; FOR SHOW
 51 01256 020000 LDA 0, LOC0 ; LOC 0 INT RETURN
 52 01257 101015 MOVE# 0, 0, \$NR ; WHERE WE THERE?
 53 01260 006236 EHALT ; NO
 54 01261 006231 LOOPX
 55
 56 01262 024101 LDA 1, DC2
 57 01263 030401 LDA 2, XN105
 58 01264 133000 ADD 1, 2
 59 01265 050406 STA 2, R125
 60 01266 006226 R13: JSR# IEN?

0011 FCBIA
 01 01267 000012 I
 02 01270 062677 IORST
 03 01271 102400 SUB 0, 0
 04 01272 040000 STH 0, L000 ;CLEAR LOC 0
 05 01273 060100 R135: N10S 0 ;N10S TO ODD DEVICE CODE GOES HERE
 06 01274 060177 INTEN
 07 01275 000401 NOP
 08 01276 000401 NOP
 09 01277 000401 NOP
 10 01300 030775 LDA 2, R125 ;FOR SHOW
 11 01301 020000 LDA 0, L000 ;LOC 0
 12 01302 101015 MOV# 0, 0, SBR ;WERE WE THERE?
 13 01303 006230 EHALT ;NO
 14 01304 006231 LOOPX
 15
 16 TMESS MF006
 17 01305 022213 LDA 0, 015WR7EG
 18 01306 024245 LDA 1, C48
 19 01307 107414 AND# 0, 1, S2R ;SWT R ON
 20 01310 000404 JNP +4 ;YES, ALWAYS PRINT
 21 01311 020203 LDA 0, PMS25
 22 01312 101014 MOV# 0, 0, S2R ;FIRST PASS
 23 01313 000405 JNP +3 ;NO, DON'T PRINT
 24 01314 006215 JSR 0IMES?
 25 01315 004477 MF006
 26 01316 024100 LDA 1, 0C1
 27 01317 030075 LDA 2, XN10S
 28 01320 133000 ADD 1, 2
 29 01321 050411 STA 2, R145
 30 01322 030077 LDA 2, XN10C
 31 01323 133000 ADD 1, 2
 32 01324 050407 STA 2, R140
 33 01325 006226 R14: JSR# 1ENT?
 34 01326 000012 I
 35 01227 062677 IORST
 36 01328 102400 SUB 0, 0
 37 01330 040000 STA 0, L000 ;CLEAR LOC 0
 38 01332 060100 R145: N10S 0 ;N10S TO EVEN DEVICE CODE GOES HERE
 39 01333 060200 R14C: N10C 0 ;N10C TO EVEN DEVICE CODE GOES HERE
 40 01334 060177 INTEN
 41 01335 000401 NOP
 42 01336 000401 NOP
 43 01337 000401 NOP
 44 01340 024772 LDA 1, R145 ;FOR SHOW
 45 01341 030772 LDA 2, R140
 46 01342 020000 LDA 0, L000 ;LOC 0 INT RETURN
 47 01343 101014 MOV# 0, 0, S2R ;WERE WE THERE?
 48 01344 006230 EHALT ;NO
 49 01345 006231 LOOPX
 50
 51 01346 024101 LDA 1, DC2
 52 01347 030075 LDA 2, XN10S
 53 01350 133000 ADD 1, 2
 54 01351 050411 STA 2, R155
 55 01352 030077 LDA 2, XN10C
 56 01353 133000 ADD 1, 2
 57 01354 050407 STA 2, R150
 58 01355 006226 R15: JSR# 1ENT?
 59 01356 000012 I
 60 01357 062677 IORST

0012 F8018
 01 01360 102400 SUB 0,0
 02 01361 0400000 STA 0,LOC0 ;CLEAR LOC 0
 03 01362 060100 R15S: N10S 0 ;N10S TO 000 DEVICE CODE GOES HERE
 04 01363 060200 R15C: N10C 0 ;N10C TO 000 DEVICE CODE GOES HERE
 05 01364 060177 INTEN
 06 01365 000401 NOP
 07 01366 000401 NOP
 08 01367 000401 NOP
 09 01370 024772 LDA 1,R15S ;FOR SHOW
 10 01371 036772 LDA 2,R15C
 11 01372 0200000 LDA 0,LOC0 ;LOC 0
 12 01373 101014 MOV# 0,0,\$2R ;WERE WE THERE?
 13 01374 006230 EHALT
 14 01375 006231 LOOPX
 15
 16 TMESS MF007
 17 01376 022213 LDA 0,015WR?EG
 18 01377 024245 LDA 1,048
 19 01400 107414 AND# 0,1,\$2R ;SWT R ON
 20 01401 000404 JMP ,+4 ;YES, ALWAYS PRINT
 21 01402 026263 LDA 0,PAS05
 22 01403 101014 MOV% 0,0,\$2R ;FIRST PASS
 23 01404 000403 JMP ,+3 ;NO, DON'T PRINT
 24 01405 006215 JSR 0IMES?
 25 01406 004026 MF007
 26 01407 024180 LDA 1,DC1
 27 01410 030075 LDA 2,XN10S
 28 01411 1030000 ADD 1,2
 29 01412 050411 STA 2,R16S
 30 01413 030070 LDA 2,XD000
 31 01414 1030000 ADD 1,2
 32 01415 050497 STA 2,R16DC
 33 01416 006226 R16: JSR# IENT?
 34 01417 000012 I
 35 01426 062677 10RST
 36 01421 102400 SUB 0,0
 37 01422 0400000 STA 0,0 ;CLEAR LOC 0
 38 01423 060100 R16S: N10S 0 ;N10S TO EVEN DEVICE CODE GOES HERE
 39 01424 063000 R16DC: DOC 0,0 ;DOC TO EVEN DEVICE CODE GOES HERE
 40 01425 060177 INTEN
 41 01426 000401 NOP
 42 01427 000401 NOP
 43 01428 000401 NOP
 44 01431 024772 LDA 1,R1ES ;FOR SHOW
 45 01432 030772 LDA 2,R16DC
 46 01433 0200000 LDA 0,LOC0 ;LOC 0 INT RETURN
 47 01434 101014 MOV# 0,0,\$2R ;WERE WE THERE?
 48 01435 006230 EHALT
 49 01436 006231 LOOPX
 50
 51 01437 024161 LDA 1,DC2
 52 01440 030075 LDA 2,XN10S
 53 01441 1030000 ADD 1,2
 54 01442 050411 STA 2,R17S
 55 01443 030070 LDA 2,XD000
 56 01444 1030000 ADD 1,2
 57 01445 050497 STA 2,R17DC
 58 01446 006226 R17: JSR# IENT?
 59 01447 000012 I
 60 01450 062677 10RST

0012 PDIR
 01 01451 102400 SUB 0,0
 02 01452 040000 STA 0,LOC0 ;CLEAR LOC 0
 03 01453 060100 R175, N105 0 ;N105 TO ODD DEVICE CODE GOES HERE
 04 01454 063000 R17DC, D0C 0,0 ;D0C TO ODD DEVICE CODE GOES HERE
 05 01455 060177 INTEN
 06 01456 000401 NOP
 07 01457 000401 NOP
 08 01458 000401 NOP
 09 01461 024772 LDR 1,R175 ;FOR SHOW
 10 01462 030772 LDR 2,R17DC
 11 01463 020000 LDA 0,LOC0 ;LOC 0
 12 01464 101014 MOV# 0,0,S2R ;WERE WE THERE?
 13 01465 000230 EH0L1 ;NO
 14 01466 000231 LOOPX
 15
 16 TMESS MF000
 17 01467 022213 LDH 0, #TSWR?ES
 18 01470 024245 LDA 1,048
 19 01471 107414 AND# 0,1,S2R ;SW1 A ON
 20 01472 000404 JMP .+4 ;YES, ALWAYS PRINT
 21 01473 020203 LDA 0,PAS?S
 22 01474 101014 MOV# 0,0,S2R ;FIRST PASS
 23 01475 000403 JMP .+3 ;NO, DON'T PRINT
 24 01476 000215 JSR @IMES?
 25 01477 004501 MF008
 26 01500 024100 LDA 1,DC1
 27 01501 030075 LDA 2,XN105
 28 01502 103000 ADD 1,2
 29 01503 000415 STA 2,R185
 30 01504 030072 LDA 2,XBN
 31 01505 103000 ADD 1,2
 32 01506 000413 STA 2,R18BN
 33 01507 000424 STA 2,RA18BN
 34 01510 030070 LDA 2,AD000
 35 01511 103000 ADD 1,2
 36 01512 000411 STA 2,R18DC
 37 01513 000226 R18, JSR# TENT?
 38 01514 000012 1
 39 01515 002677 J0RS1
 40 01516 102400 SUB 0,0
 41 01517 040000 STA 0,LOC0 ;CLEAR LOC 0
 42 01520 000100 R165, N105 0 ;N105 TO EVEN DEVICE CODE GOES HERE
 43 01521 063400 R18BN, SKPBN 0 ;SKPBN TO EVEN DEVICE CODE GOES HERE
 44 01522 000777 JMP .-1 ;WAIT FOR BUSY TO SET
 45 01523 063000 R18DC, D0C 0,0 ;D0C TO EVEN DEVICE CODE GOES HERE
 46 01524 000177 INTEN
 47 01525 000401 NOP
 48 01526 000401 NOP
 49 01527 000401 NOP
 50 01530 020770 LDR 0,R185 ;FOR SHOW
 51 01531 024772 LDR 1,R18DC
 52 01532 030401 LDA 2,RA18BN
 53 01533 063400 RA18BN, SKPBN 0 ;SKPBN TO EVEN DEVICE CODE GOES HERE
 54 01534 000230 EH0L1
 55 01535 000231 LOOPX
 56
 57 01536 024101 LDA 1,DC2
 58 01537 030075 LDA 2,XN105
 59 01540 103000 ADD 1,2
 60 01541 000415 STA 2,R195

0014 FBD1A
 01 01542 030074 LDA 2,XSDN
 02 01543 133000 ADD 1,2
 03 01544 050413 STA 2,A19DN
 04 01545 050424 STA 2,AR19DN
 05 01546 030070 LDA 2,XD000
 06 01547 133000 ADD 1,2
 07 01550 050411 STA 2,A19DC
 08 01551 006226 A19: JSR@ IENT?
 09 01552 000012 I
 10 01553 062677 10RST
 11 01554 102400 SUB 0,0
 12 01555 040000 STA 0,LOC0 ;CLEAR LOC 0
 13 01556 060100 A19S: NIOS 0 ;NIOS TO ODD DEVICE CODE GOES HERE
 14 01557 063600 A19DN: SKPDN 0 ;SKPDN TO ODD DEVICE CODE GOES HERE
 15 01559 000777 JMP -1 ;WAIT FOR DONE TO SET
 16 01561 063000 A19DC: DOC 0,0 ;DOC TO ODD DEVICE CODE GOES HERE
 17 01562 060177 INTEN
 18 01563 000401 NOP
 19 01564 000401 NOP
 20 01565 000401 NOP
 21 01566 020770 LDA 0,A19S ;FOR SHOW
 22 01567 024772 LDR 1,A19DC
 23 01570 030401 LDR 2,AR19DN
 24 01571 063600 AR19DN: SKPDN 0 ;SKPDN TO ODD DEVICE CODE GOES HERE
 25 01572 006230 EHAlt
 26 01573 006231 LOOPX
 27
 28 TMESS MF009
 29 01574 022212 LDA 0,0ISWR?EG
 30 01575 024245 LDA 1,C48
 31 01576 107414 PND# 0,1,52R ;SW1 R ON
 32 01577 000404 JMP +4 ;YES, ALWAYS PRINT
 33 01600 020203 LDA 0,PAS?S
 34 01601 101014 MOVW 0,0,52R ;FIRST PASS
 35 01602 000403 JMP +3 ;NO, DON'T PRINT
 36 01603 006215 JSR 01MES?
 37 01604 004577 MF009
 38 01605 024100 LDR 1,D01
 39 01606 030075 LDA 2,XNIOS
 40 01607 133000 ADD 1,2
 41 01610 000410 STA 2,A20S
 42 01611 000402 JMP A20
 43 01612 001630 A20L: A20L
 44
 45 01613 006226 A20: JSR@ IENT?
 46 01614 000012 I
 47 01615 062677 10RST
 48 01616 020774 LDA 0,A20L
 49 01617 040001 STA 0,L001 ;SET INT VECTOR TO ADDRESS OF A20L
 50 01620 060100 A20S: NIOS 0 ;NIOS TO EVEN DEVICE CODE GOES HERE
 51 01621 030777 LDA 2,A20S ;FOR SHOW
 52 01622 060177 INTEN
 53 01623 000401 NOP
 54 01624 000401 NOP ;WAIT FOR INT TO SEND US TO A20L
 55 01625 000401 NOP
 56 01626 000401 NOP
 57 01627 006230 EHAlt ;NO INTERRUPT TO SEND US TO A20L
 58 01630 006231 A20L: LOOPX
 59
 60 01631 024101 LDR 1,D02

0015 FBDIA

01 01632 030075 LDR 2,XN105
 02 01632 133006 ADD 1,2
 03 01634 050410 STA 2,R215
 04 01635 060402 JMP R21
 05 01636 001654 R21L: R21L
 06
 07 01637 006226 R21 JSR# IENT?
 08 01640 000012 I
 09 01641 062677 IORST
 10 01642 020774 LDA 0, R21L
 11 01643 040001 STA 0,LOC1 ;SET INT VECTOR TO R21L ADDRESS
 12 01644 060100 R215: NIOS 0 ;NIOS TO ODD DEVICE CODE GOES HERE
 13 01645 030777 LDA 2,R215 ;FOR SHOW
 14 01646 060177 INTEN
 15 01647 000401 NOP ;WAIT FOR INT TO SEND US TO R21L
 16 01650 000401 NOP
 17 01651 000401 NOP
 18 01652 000401 NOP
 19 01653 006230 EHRL1 ;NO INTERRUPT TO SEND US TO R21L
 20 01654 006231 R21L: LOOPX
 21
 22 01655 024100 LDA 1,,IDUMM
 23 01656 044001 STA 1,LOC1 ;RESTORE DUMMY VECTOR
 24 01657 024100 LDA 1,DC1
 25 01660 030075 LDA 2,XN105
 26 01661 133006 ADD 1,2
 27 01662 050410 STA 2,R225
 28 01663 006226 R22: JSR# IENT?
 29 01664 000012 I
 30 01665 062677 IORST
 31 01666 102400 SUB 0,0
 32 01667 040000 STA 0,LOC0 ;CLEAR LOC 0
 33 01670 020103 LDA 0,MASK0
 34 01671 062077 MSKO 0 ;SET MASK ON EVEN DEVICE CODE
 35 01672 060100 R225: NIOS 0 ;NIOS TO EVEN DEVICE CODE GOES HERE
 36 01673 060177 INTEN
 37 01674 000401 NOP
 38 01675 000401 NOP
 39 01676 000401 NOP
 40 01677 030772 LDA 2,R225 ;DEVICE CODE FOR SHOW
 41 01700 024000 LDA 1,LOC0 ;LOC 0 INT RETURN
 42 01701 125014 MOW# 1,1,5ZR ;WERE WE THERE?
 43 01702 006230 EHRL1 ;YES
 44 01703 006231 LOOPX
 45
 46 01704 024101 LDA 1,DC2
 47 01705 030075 LDA 2,XN105
 48 01706 133006 ADD 1,2
 49 01707 050410 STA 2,R235
 50 01710 006226 R23: JSR# IENT?
 51 01711 000012 I
 52 01712 062677 IORST
 53 01713 102400 SUB 0,0
 54 01714 040000 STA 0,LOC0 ;CLEAR LOC 0
 55 01715 020104 LDA 0,MASK0
 56 01716 062077 MSKO 0 ;SET ODD DEVICE CODE MASK
 57 01717 060100 R235: NIOS 0 ;NIOS TO ODD DEVICE CODE GOES HERE
 58 01720 060177 INTEN
 59 01721 000401 NOP
 60 01722 000401 NOP

0016 FBD1R
 01 01723 000401 NOP
 02 01724 020773 LDR 2,A235 ;FOR SHOW
 03 01725 024000 LDA 1,LOC0 ;LOC 0
 04 01726 125014 MOV# 1,1,52R ;WERE WE THERE?
 05 01727 006230 EHAlt ;YES
 06 01730 006231 LOOP%
 07
 08 01731 062677 IORST
 09 01732 020247 LDR 0,C144 ;DELAY SPEED UP
 10 01733 040211 STA 0,ERR?4
 11 DOLoop:
 12 DMESS MS006 ;STARTING WRITE/READ LOOP
 13 01734 006215 JSR @1MES? ;DISPLAY A MESSAGE
 14 01735 004266 MS006 ;ADDR. OF MESSAGE
 15 01736 024102 LDA 1,TSTDC ;ON THIS DEVICE CODE
 16 01737 006223 JSR @1Z0C?1
 17 DMESS MSLCR
 18 01740 006215 JSR @1MES? ;DISPLAY A MESSAGE
 19 01741 003766 MSLCR ;ADDR. OF MESSAGE
 20 01742 162400 SUB 0,0 ;W/R PATTERN
 21 01742 040111 STA 0,DATA ;START AT ZERO
 22 01744 024102 LDA 1,TSTDC
 23 01745 030065 LDR 2,XD0H0 ;XD0H0 HOLDS INSTR. DOA 0,0
 24 01746 133000 ADD 1,2 ;PUT TEST DEV. CODE IN INSTR.
 25 01747 050410 STA 2,A00A ;PUT DOA 0, TESTDC IN WRITE/READ LOOP
 26 01750 030067 LDA 2,XD1H1 ;XD1H1 HOLDS INSTR. DIA 1,0
 27 01751 133000 ADD 1,2 ;PUT TEST DEV. CODE IN INSTR.
 28 01752 050406 STA 2,A01A ;PUT DIA 1, TESTDC IN WRITE/READ LOOP
 29 01753 006226 R24: JSR@ TENT?
 30 01754 000001 1
 31 01755 020111 WRLoop: LDA 0,DATA ;GET DATA TO WRITE
 32 01756 126400 SUB 1,1 ;CLEAR IN REG
 33 01757 061000 A00A: DOA 0,0 ;WRITE PATTERN TO DEVICE (DOA)
 34 01758 064400 A01A: DIA 1,0 ;READ IT BACK INTO A00 (DIA)
 35 01761 020777 LDR 2,A01A ;GET PRESENT DEVICE CODE FOR SHOW
 36 01762 106414 SUB# 0,1,52R ;SKIP IF THEY MATCH
 37 01763 006230 EHAlt
 38 01764 006231 LOOP%
 39 01765 010111 ISZ DATA ;INCREMENT PATTERN. IF ZERO = DONE
 40 01766 000765 JMP R24 ;NEXT PATTERN
 41 DMESS MS007 ;TEST SUCCESSFUL
 42 01767 006215 JSR @1MES? ;DISPLAY A MESSAGE
 43 01770 004232 MS007 ;ADDR. OF MESSAGE
 44 DMESS MSLCR
 45 01771 006215 JSR @1MES? ;DISPLAY A MESSAGE
 46 01772 003766 MSLCR ;ADDR. OF MESSAGE
 47 NEXTDC:
 48 01773 024102 LDA 1,TSTDC
 49 01774 020101 LDA 0,DC2 ;READY DEVICE CODE 2 TO BE NEXT TEST
 50 01775 040102 STA 0,TSTDC ;DONE IF ALREADY TESTED DEVICE CODE 2
 51 01776 106414 SUB# 0,1,52R ;OTHERWISE, RUN PATTERNS ON DEVICE 2
 52 01777 000735 JMP DOLoop
 53 02000 020100 LDA 0,DC1
 54 02001 040102 STA 0,TSTDC ;SAY WE'RE DONE
 55 DMESS MS003 ;DISPLAY A MESSAGE
 56 02002 006215 JSR @1MES? ;ADDR. OF MESSAGE
 57 02003 004077 MS003
 58
 59
 60 02004 062677 R25: IORST

0017 FBDIR

01 DMESS MSLCR
02 02005 006215 JSR @IMES? ;DISPLAY A MESSAGE
03 02006 003766 MSLCR ;ADDR. OF MESSAGE
04 DMESS MF010
05 02007 006215 JSR @IMES? ;DISPLAY A MESSAGE
06 02010 004630 MF010 ;ADDR. OF MESSAGE
07 DMESS MSLCR
08 02011 006215 JSR @IMES? ;DISPLAY A MESSAGE
09 02012 003766 MSLCR ;ADDR. OF MESSAGE
10 02013 020437 LDA 0..INTM
11 02014 040001 STA 0..LOC1 ;SET UP INT VECTOR
12 02015 020100 LDA 0..DC1
13 02016 024064 LDA 1..XDOHS
14 02017 107000 ADD 0..1
15 02020 044403 STA 1..A25D
16 02021 102400 SUB 0..0
17 02022 040111 STA 0..DATA
18 02023 061100 A26D: DORS 0..0 ;DORS @ HERE
19 02024 060177 INTEN ;LET THEM GO
20 02025 000400 JMP . ;WAIT HERE
21 02026 000777 JMP .-1 ;DON'T LET THEM GET AWAY
22
23 02027 062677 A26: 10RS1
24 DMESS MSLCR
25 02030 006215 JSR @IMES? ;DISPLAY A MESSAGE
26 02031 003766 MSLCR ;ADDR. OF MESSAGE
27 DMESS MF011
28 02032 006215 JSR @IMES? ;DISPLAY A MESSAGE
29 02033 004657 MF011 ;ADDR. OF MESSAGE
30 DMESS MSLCR
31 02034 006215 JSR @IMES? ;DISPLAY A MESSAGE
32 02035 003766 MSLCR ;ADDR. OF MESSAGE
33 02036 020414 LDA 0..INTM
34 02037 040001 STA 0..LOC1 ;SET UP INT VECTOR
35 02040 020101 LDA 0..DC2
36 02041 024064 LDA 1..XDOHS
37 02042 107000 ADD 0..1
38 02043 044403 STA 1..A26D
39 02044 102400 SUB 0..0
40 02045 040111 STA 0..DATA
41 02046 061100 A26D: DORS 0..0 ;DORS @ HERE
42 02047 060177 INTEN ;LET THEM GO
43 02050 000400 JMP . ;WAIT HERE
44 02051 000777 JMP .-1 ;DON'T LET THEM GET AWAY
45 02052 002053 .INTM: INTM
46 02053 061477 INTM: INTB 0
47 02054 024100 LDA 1..DC1
48 02055 106415 SUB# 0..1,SNR ;EVEN UNIT
49 02056 000412 JMP INTQ ;YES
50 02057 024101 LDA 1..DC2 ;ODD UNIT
51 02060 106415 SUB# 0..1,SNR ;
52 02061 000415 JMP INT1 ;YES
53 02062 024077 LDA 1..XN10C ;
54 02063 107000 ADD 0..1
55 02064 044401 STA 1..IN10C ;NIOC FOR ANY OTHER DEVICE
56 02065 060200 IN10C: NIQC 0 ;HERE
57 02066 060177 INTR: INTEN
58 02067 002000 JMP #0 ;RETURN FROM INT
59
60 02070 030111 INTQ: LDA 2..DATA ;GET OLD DATA SENT

0018 F8D1A
 01 02071 010111 ISZ DATA
 02 02072 000412 JMP REND ;NOT DONE, READ AND SEND MORE
 03 DMESS MF012
 04 02073 006215 JSR @IMES? ;DISPLAY A MESSAGE
 05 02074 004780 MF012 ;ADDR. OF MESSAGE
 06 02075 000732 JMP R26
 07 02076 030111 INT1: LDA 2, DATA ;GET OLD DATA SENT
 08 02077 010111 ISZ DATA
 09 02100 000404 JMP REND ;NOT DONE, READ AND SEND MORE
 10 DMESS MF013
 11 02101 006215 JSR @IMES? ;DISPLAY A MESSAGE
 12 02102 004724 MF013 ;ADDR. OF MESSAGE
 13 02103 000421 JMP END
 14
 15 02104 024066 REND: LDA 1, XD1AC
 16 02105 107000 ADD 0,1
 17 02106 044401 STA 1, DIA ;MAKE DIA
 18 02107 064600 ,DIA: DIAC 1,0 ;DIAC 1 HERE
 19 02110 062677 IORST
 20 02111 132415 SUB# 1,2, SNR
 21 02112 000404 JMP INTO
 22 DMESS MSERR ;DATA ERROR UNDER INT'S
 23 02113 006215 JSR @IMES? ;DISPLAY A MESSAGE
 24 02114 004742 MSERR ;ADDR. OF MESSAGE
 25 02115 002233 JMP @I0DT? ;BAD NEWS
 26
 27 02116 024064 INTO: LDA 1, XD0AS
 28 02117 107000 ADD 0,1
 29 02120 044402 STA 1, D0AS
 30 02121 020111 LDA 0, DATA ;DATA
 31 02122 061100 ,D0AS: D0AS 0,0 ;D0AS @ HERE
 32 02123 000743 JMP INTR
 33
 34 02124 062677 END: IORST
 35 02125 020105 LDA 0, 1DUM
 36 02126 040001 STA 0, LOC1 ;RESTORE DUMMY VECTOR
 37 DMESS MPAS
 38 02127 006215 JSR @IMES? ;DISPLAY A MESSAGE
 39 02128 004767 MPAS ;ADDR. OF MESSAGE
 40 02131 024203 LDA 1, PAS?S
 41 02132 006226 JSR @1PDE?C
 42 DMESS MSLCR
 43 02133 006215 JSR @IMES? ;DISPLAY A MESSAGE
 44 02134 003766 MSLCR ;ADDR. OF MESSAGE
 45 02135 010203 ISZ PAS?S
 46 02136 000401 NOP
 47 02137 002401 JMP @ +1
 48 02140 000556 G02

10019 FBD1A
 01 000001 . NOMAC X
 02
 03 ;=====
 04 ; OCTAL AND DECIMAL INPUT ROUTINES AND MESSAGES
 05 ;=====
 06
 07 02141 000000 XXX: 0 ; DUMMY ADDRESS FOR END
 08 ; OF TESTING
 09 ; THE FOLLOWING ROUTINES REPLACE THE OCTAL AND DECIMAL
 10 ; INPUT ROUTINES TO SEARCH FOR CO, CR OR CD AND JUMP TO
 11 ; THE APPROPRIATE PLACE IF ONE OF THESE CONTROL CHARS
 12 ; IS STRUCK.
 13 000000 . TXTM 0
 14
 15
 16 ; OCTAL INPUT ROUTINE
 17 02142 000000 0
 18 02143 054777 T11I: STA 3, -1
 19 02144 050430 STA 2, TT52 ; SAVE AC2
 20 02145 006224 JSR@ 1II?0 ; GET CHARACTER
 21 02146 004404 JSR 1II11
 22 02147 010772 ISZ TT11-1
 23 02150 030424 T11I2: LDA 2, TT52
 24 02151 002771 JMP@ 1III-1 ; RETURN+2
 25
 26 ; ROUTINE LOOKS FOR: CO, CR, OR CD
 27 02152 030417 1II11: LDA 2, TTCD
 28 02153 142415 SUB# 2, 0, SNR ; IS 11 CONTROL 0?
 29 02154 006233 JSR@ 10DT? ; YES
 30 02155 030415 LDA 2, T1CR , ; OR CONTROL R?
 31 02156 142415 SUB# 2, 0, SNR
 32 02157 000487 JMP CNTRR ; YES
 33 02158 030413 LDA 2, TTCD
 34 02159 142415 SUB# 2, 0, SNR ; CONTROL D ?(RESTART WITH SW REG CLEAR)
 35 02162 000402 JMP CNTRD ; YES
 36 02163 001401 JMP 1, 3 ; NO
 37 ;
 38 CNTRD:
 39 02164 152400 SUB 2, 2
 40 02165 052213 STA 2, @ISWR?EG ; CLEAR SW REG
 41 02166 152400 CNTRR: SUB 2, 2
 42 02167 050203 STA 2, PR5?S
 43 02170 000234 JMP RES?T ; AND RESTART
 44
 45 02171 000017 TTCD: 17
 46 02172 000022 T1CR: 22
 47 02173 000004 TTCD: 4
 48 02174 000000 TT52: 0
 49
 50
 51 ; DECIMAL INPUT ROUTINE
 52 02175 054745 TT1D: STA 3, 1II11-1 ; DECIMAL INPUT ROUTINE
 53 02176 050776 STA 2, TT52 ; KEEP AC2
 54 02177 006225 JSR@ 1II?0 ; GET DECIMAL
 55 02200 004752 JSR 1II11
 56 02201 010741 ISZ 1II11-1 ;
 57 02202 000746 JMP 1III2

0020 FEDIR
01 000401 B0000= 401
02 03760 000000 EGGS: 0 ;AUTO RUN SWITCH
03 03761 000000 0 ;DEVICE CODE
04 03762 000000 0 ;CRT SWITCH
05 03763 000000 0 ;# OF PHSES
06 03764 000000 0 ;RETURN ADDRESS
07 03765 000000 SWREG: 0 ;SWITCH REGISTER
08 03766 006412 MSLCR: .TXT !K12>{15}>!
09 03770 006412 MS000: .TXT !K12>{15}>WHAT IS THE EVEN DEVICE CODE MASK BIT SET FOR? !
10 04021 006412 MS001: .TXT !K12>{15}>WHAT IS THE ODD DEVICE CODE MASK BIT SET FOR? !
11 04052 006412 MS002: .TXT !K12>{15}>WHAT DEVICE CODE IS THE BOARD SET FOR? !
12 04077 006412 MS003: .TXT !K12>{15}>BOTH DEVICES DATA REGISTERS HAVE BEEN TESTED. !
13 04130 006412 MS004: .TXT !K12>{15}>EVEN DEVICE CODE FOR FIRST GROUND IS !
14 04154 006412 MS005: .TXT !K12>{15}>ODD DEVICE CODE FOR SECOND GROUND IS !
15 04200 006412 MS006: .TXT !K12>{15}>ROUT TO OUTPUT AND INPUT BIT PATTERNS ON DEVICE !
16 04232 006412 MS007: .TXT !K12>{15}>ALL PATTERNS OUTPUT MATCHED INPUT FOR THIS DEVICE!
17 04264 006412 MF000: .TXT !K12>{15}>TESTING START PULSE SETS DONE (ODD) AND BUSY (EVEN) FF'S. !
18 04322 006412 MF001: .TXT !K12>{15}>TESTING IORST CLEARS DONE AND BUSY. !
19 04345 006412 MF002: .TXT !K12>{15}>TESTING CLEAR PULSE CLEARS DONE AND BUSY. !
20 04373 006412 MF003: .TXT !K12>{15}>TESTING 1OP PULSE CLEARS DONE AND BUSY. !
21 04429 006412 MF004: .TXT !K12>{15}>TESTING INTA READS DEVICE CODE WHEN IT SHOULD. !
22 04451 006412 MF005: .TXT !K12>{15}>TESTING INTERRUPT OCCURS WHEN IT SHOULD. !
23 04477 006412 MF006: .TXT !K12>{15}>TESTING CLEAR PULSE CLEARS INTERRUPT FF'S. !
24 04526 006412 MF007: .TXT !K12>{15}>TESTING DOC CLEARS INTERRUPT FF'S. !
25 04551 006412 MF008: .TXT !K12>{15}>TESTING DOC DOES NOT CLEAR DONE AND BUSY. !
26 04577 006412 MF009: .TXT !K12>{15}>TESTING MSKO (MASK OUT) CAN DISABLE INTERRUPTS. !
27 04630 006412 MF010: .TXT !K12>{15}>TESTING EVEN DEVICE CODE UNDER INTERRUPTS. !
28 04657 006412 MF011: .TXT !K12>{15}>TESTING ODD DEVICE CODE UNDER INTERRUPTS. !
29 04705 006412 MF012: .TXT !K12>{15}>EVEN DEVICE CODE COMPLETE. !
30 04724 006412 MF013: .TXT !K12>{15}>ODD DEVICE CODE COMPLETE. !
31 04742 006412 MSERR: .TXT *K12>{15}>DATA ERROR RUNNING UNDER INTERRUPTS!!!*
32 04767 006412 MPHS: .TXT !K12>{15}>PASS !
33 04773 006412 DIRT: .TXT !K12>{15}>RDOS FOREGROUND/BACKGROUND COMMUNICATIONS BOARD DIAGNOSTICS REV. 00!
34 000000 .NOLOC 0
35 END
***000000 TOTAL ERRORS, 00000 FIRST PASS ERRORS

R01A	001760	16/28	16/34#	16/35
R00R	001757	16/25	16/33#	
R1	000630	5/51#		
R10	001170	9/54#		
R10IA	001201	10/62	10/63#	
R10S	001173	9/53	9/57#	
R11	001211	10/12#		
R11IA	001222	10/20	10/21#	
R11S	001214	10/11	10/15#	
R12	001243	10/40#		
R12S	001250	10/39	10/45#	10/50
R13	001266	10/68#		
R13S	001273	10/59	11/65#	11/10
R14	001325	11/33#		
R14C	001333	11/32	11/39#	11/45
R14S	001332	11/29	11/38#	11/44
R15	001355	11/58#		
R15C	001363	11/57	12/64#	12/10
R15S	001362	11/54	12/63#	12/09
R16	001416	12/37#		
R16DC	001424	12/32	12/39#	12/45
R16S	001423	12/29	12/38#	12/44
R17	001446	12/58#		
R17DC	001454	12/57	13/64#	13/10
R17S	001453	12/54	13/63#	13/09
R18	001513	13/37#		
R18BN	001521	13/32	13/43#	
R18DC	001523	13/36	13/45#	13/51
R18S	001520	13/29	13/42#	13/50
R19	001551	14/88#		
R19DC	001561	14/87	14/16#	14/22
R19DN	001557	14/83	14/14#	
R19S	001556	13/60	14/13#	14/21
R1BN	000636	5/50	5/56	5/57#
R15	000633	5/47	5/54#	5/55
R2	000656	5/03#		
R20	001613	14/42	14/45#	
R20L	001630	14/43	14/58#	
R20S	001620	14/41	14/59#	14/51
R21	001637	15/64	15/67#	
R21L	001654	15/65	15/26#	
R21S	001644	15/63	15/12#	15/13
R22	001663	15/28#		
R22S	001672	15/27	15/25#	15/40
R23	001710	15/50#		
R23S	001717	15/49	15/57#	16/92
R24	001753	16/29#	16/40	
R25	002004	16/60#		
R25D	002023	17/15	17/18#	
R26	002027	17/23#	18/06	
R26D	002046	17/38	17/41#	
R20N	000656	6/07	6/13	6/14#
R25	000653	6/04	6/11#	6/12
R3	000704	6/39#		
R3BN	000710	6/35	6/43#	
R3BZ	000716	6/28	6/48	6/49#
R31R	000712	6/45#	6/47	
R3S	000767	6/32	6/42#	6/46
R4	000733	7/04#		

R4DN	000737	6/59	7/09#					
R4D2	000745	7/02	7/13	7/14#				
R4IR	000741	7/16#	7/12					
R4S	000736	6/56	7/07#	7/11				
R5	000776	7/41#						
R5EN	001002	7/24	7/45#					
R5B2	001010	7/40	7/50	7/51#				
R5C	001004	7/37	7/47#	7/49				
R5S	001001	7/21	7/44#	7/48				
R6	001030	8/09#						
R6C	001036	8/04	8/15#	8/17				
R6DN	001024	8/01	8/13#					
R6D2	001042	8/07	8/18	8/19#				
R6S	001033	7/38	8/12#	8/16				
R7	001073	8/47#						
R7BN	001077	8/40	8/51#					
R7B2	001105	8/46	8/56	8/57#				
R7P	001101	8/43	8/53#	8/55				
R7S	001076	8/37	8/50#	8/54				
R8	001125	9/15#						
R8DN	001131	9/07	9/19#					
R8D2	001137	9/13	9/24	9/25#				
R8P	001133	9/10	9/21#	9/23				
R8S	001130	9/04	9/18#	9/22				
R9	001153	9/40#						
R91A	001160	9/44	9/45#					
R91R	001155	9/42#	9/43					
RA18B	001533	13/33	13/52	13/53#				
RA19D	001571	14/04	14/23	14/24#				
RC0?	003555	20/03#						
RC1?	003556	20/03#						
RC2?	003557	20/03#						
RC3?	000207	3/14#	20/02					
BB	000566	5/12#						
BBB2	000572	5/11	5/15	5/16#				
BDDDD	000461	20/02#	20/03					
BEG1	000509	3/14	4/08#					
BGN?A	000202	3/14#						
C100K	000251	3/34#						
C1070	002642	19/60#						
C144	000247	3/32#	16/09					
C1B?6	003712	20/03#						
C2	000241	3/26#						
C4	000242	3/27#						
C40	000245	3/20#	5/25	6/20	7/26	8/25	9/31	10/28
		11/18	12/18	13/18	14/20			
C5	000243	3/28#						
C70K	000250	3/33#	5/06					
C76	000246	3/31#						
C9.	000244	3/29#						
CHC?0	002227	19/60#						
CAR?	003550	20/03#						
CHR?3	002242	19/60#						
CHC?1	002230	19/60#						
CHE?%	002246	19/60#						
CHNG	000114	3/09#						
CHR?E	002226	19/60#						
CHR?2	002355	19/60#						

CN256	000254	2/37#							
CMA4	000252	2/25#							
CN512	000255	2/38#							
CMB9	000253	2/26#							
CNTRD	002164	19/35	19/38#						
CNTRR	002166	19/32	19/41#						
CRL?P	002252	2/14	19/60#						
CYC?2	002501	28/03#							
CYC?I	003442	28/03#							
CYC?J	003424	2/14	28/03#						
CYC?X	003415	2/14	28/03#						
DO	000601	5/24#							
DOD2	000605	5/23	5/27	5/28#					
DATA	000111	3/06#	16/21	16/31	16/39	17/17	17/40	17/60	
		18/81	18/07	18/08	18/34				
DATA0	000112	3/07#							
DATA1	000113	3/08#							
DC1	000100	2/55#	4/23	4/29	5/08	5/44	6/29	7/29	
		8/24	9/30	10/81	10/36	11/26	12/26	13/26	
		14/38	15/24	16/53	17/12	17/47			
DC2	000101	2/56#	4/25	4/25	5/28	6/01	6/53	7/55	
		9/61	10/08	10/19	10/56	11/51	12/51	13/57	
		14/68	15/46	16/49	17/25	17/56			
DCL00	001734	16/11#	16/52						
DCD?1	002317	19/60#							
DEC?1	002220	19/60#							
DET?B	002360	19/60#							
DEV1	000504	4/15#	4/30						
DIRT	004773	4/11	28/24#						
DIVD	000107	3/03#							
DIV?	003511	3/03	28/03#						
DIV?0	003512	28/03#							
DIV?D	003515	28/03#							
DMESS	000143	MC	4/09	4/12	4/16	4/26	4/32	4/37	4/40
			4/51	16/12	16/17	16/41	16/44	16/55	17/61
			17/64	17/97	17/24	17/27	17/36	18/03	18/16
			18/22	18/27	18/42				
D1075	000206	2/38	2/14#						
D?IAG	025272	MC	28/62						
EGGS	003760	2/38	3/14	28/03#					
EHALT	006230	3/24#	5/17	5/29	5/58	6/15	6/50	7/15	
			7/52	8/20	8/58	9/26	9/47	10/05	10/23
			10/52	11/13	11/48	12/13	12/48	13/12	13/54
			14/25	14/57	15/19	15/43	16/05	16/37	
EMMB	000536	4/47#	4/49						
END	002124	18/13	18/24#						
ENI?R	003375	3/14	28/03#						
ERR?1	003656	28/03#							
ERR?2	003647	28/03#							
ERR?3	003676	28/03#							
ERR?4	000211	3/14#	5/07	16/10	28/03				
ERR?5	002785	28/03#							
ERR?6	003611	28/03#							
ERR?7H	003630	28/03#							
ERR?1	003576	28/03#							
ERR?J	002560	3/14	28/03#						
ERR?N	003654	28/03#							
ERT?N	003552	28/03#							

PS17D	002535	19/60#						
GMSKE	000530	4/48#	4/44					
GMSKO	000542	4/51#	4/55					
GO	000554	5/62#						
GO2	000556	5/84#	18/48					
HEA?D	003713	20/03#						
HEL?P	000201	3/14#	20/03					
I	000012	2/28#	5/13	5/25	5/52	6/89	6/40	7/05
			7/42	8/18	8/48	9/16	9/41	9/55
			10/41	11/01	11/34	11/59	12/04	12/59
			14/89	14/46	15/68	15/29	15/51	13/38
112?1	003132	19/60#	20/02					
ICRL?	000216	3/14#	19/68	20/62	20/63			
ICWC?	000227	3/14#	20/03					
ICYC?	000231	3/14#	3/23					
IDUMM	000006	2/24#	2/68					
IEGG?	000212	3/14#	20/03					
IENT?	000226	3/14#	5/12	5/24	5/51	6/86	6/39	7/04
			7/41	8/09	8/47	9/15	9/48	9/54
			10/49	10/60	11/32	11/58	12/23	12/58
			14/88	14/45	15/67	15/29	15/50	16/29
IERR?	000239	3/14#	3/24					
IINP?	000214	3/14#	20/03					
IINR?	003163	20/02#						
IMES?	000215	3/14#	4/18	4/13	4/17	4/27	4/33	4/38
			4/41	4/52	5/41	6/26	7/26	8/31
			10/34	11/24	12/24	13/24	14/36	16/13
			16/42	16/45	16/56	17/62	17/65	17/68
			17/26	17/31	18/04	18/11	18/23	18/38
			20/62	20/63				
IN6?	002722	19/60#						
IN1?	003017	19/60#						
IN120	002713	19/60#						
IN122	003134	19/60#	20/02					
IN123	002712	19/60#						
IN125	003135	19/60#	20/02					
IN2?	003024	19/60#						
IN3?	002701	19/60#						
IN373	002711	19/60#						
IN4?	003065	19/60#						
IN5?	002751	19/60#						
IN6?	002765	19/60#						
IN670	003132	19/60#	20/02					
INB?H	003127	19/60#	20/02					
INB?I	003130	19/60#	20/02					
IN10C	002065	17/55	17/56#					
INL?K	002715	19/60#						
INM?	003041	19/60#						
INP?1	002717	19/60#						
INP?2	002722	19/60#						
INP?J	002725	19/60#						
INP?K	003055	3/14	20/02#					
INP?O	003117	19/60#	20/02					
INP?R	003125	19/60#	20/02					
INR?	002767	19/60#	20/02					
INR?1	003366	20/02#						
INR?K	003365	20/02#						
INR?O	003367	20/02#						

INS?	003121	19/68#						
INS?0	003121	19/68#						
INS?1	003122	19/68#						
INS?2	003123	19/68#						
INS?3	003124	19/68#	20/82					
INS?4	003126	19/68#	20/82					
INS?W	003101	19/68#	20/82					
INS?X	003162	20/82#						
INT0	002070	17/49	17/68#					
INT1	002076	17/52	18/07#					
INTC	002116	18/21	18/27#					
INTM	002053	17/45	17/46#					
INTR	002066	17/57#	18/32					
INT?	002605	19/68#						
INT?E	002714	19/68#						
INT?PR	002716	19/68#						
1001?	000233	3/15#	18/25	19/29	20/82			
10M?0	000235	3/19#	19/69	20/03				
IPDC?	000221	3/14#	19/68	20/82	20/83			
IPDE?	000220	3/14#	18/41					
IPOC?	000222	3/14#	20/82	20/03				
ISWR?	000213	3/14#	5/34	6/19	7/19	8/24	9/30	10/27
		11/17	12/17	13/17	14/29	19/46	19/66	20/03
ITI?0	000225	3/14#	19/54					
ITI?0	000224	3/14#	19/28					
ITPS?	000232	3/14#	19/68	20/82				
ITR?	003544	20/03#						
ITR?0	003546	20/03#						
ITR?K	000206	3/14#	20/83					
ITR?T	003545	20/03#						
ITTA	000240	3/22#						
ITTD	000236	3/26#	4/43	4/54				
ITTI	000237	3/21#	4/19					
ITYP?	000217	3/14#	19/69					
IZOC?	000223	3/14#	4/38	4/36	16/16	20/82	20/03	
K10?0	002553	20/03#						
K12?	002261	19/68#						
K15?	002262	19/68#						
L008	000000	2/28#	10/44	10/51	11/04	11/11	11/37	11/46
		12/02	12/11	12/46	13/02	13/11	13/41	14/12
		15/22	15/41	15/54	16/03			
L001	000001	2/29#	5/85	14/49	15/11	15/23	17/11	17/34
		18/36						
L00PK	006231	3/23#	5/18	5/36	5/59	6/16	6/51	7/16
		7/53	8/21	8/59	9/27	9/48	10/06	10/24
		10/54	11/14	11/49	12/14	12/49	13/14	13/55
		14/26	14/58	15/20	15/44	16/06	16/38	
L00?R	003543	20/03#						
L00?T	003733	20/03#						
LOP?E	003547	20/03#						
MASKE	000103	2/58#	4/50	15/13				
MASK0	000104	2/59#	5/01	15/55				
MDV?1	003554	20/03#						
MDV?2	003520	20/03#						
MDV?3	003527	20/03#						
MDV?4	003534	20/03#						
MDV?5	003551	20/03#						
MES?M	002211	19/68#						

0026 FB01R

MES?S 002203	3/14	19/60#					
MF000 004264	5/42	20/18#					
MF001 004322	6/27	20/19#					
MF002 004345	7/27	20/20#					
MF003 004372	8/32	20/21#					
MF004 004429	9/38	20/22#					
MF005 004451	10/35	20/23#					
MF006 004477	11/25	20/24#					
MF007 004526	12/25	20/25#					
MF008 004551	13/25	20/26#					
MF009 004577	14/37	20/27#					
MF010 004630	17/96	20/28#					
MF011 004657	17/29	20/29#					
MF012 004705	18/05	20/30#					
MF013 004724	18/12	20/31#					
MPHS 004767	18/39	20/33#					
MS000 003770	4/42	20/10#					
MS001 004021	4/53	20/11#					
MS002 004052	4/18	20/12#					
MS003 004077	16/57	20/13#					
MS004 004139	4/28	20/14#					
MS005 004154	4/34	20/15#					
MS006 004200	16/14	20/16#					
MS007 004232	16/43	20/17#					
MSERR 004742	18/24	20/22#					
MSLCR 003766	4/14	4/39	16/19	16/46	17/63	17/89	17/26
	17/32	18/44	20/09#				
MUL1 000105	3/02#						
MUL2 003531	3/02	20/03#					
MUL2H 003532	20/03#						
NEXTD 001773	16/47#						
NOP 000401	3/25#	9/58	9/59	9/60	10/16	10/17	10/18
	10/47	10/48	10/49	11/87	11/88	11/89	11/41
	11/42	11/43	12/06	12/87	12/88	12/41	12/42
	12/43	12/06	13/07	13/88	13/47	13/48	13/49
	14/18	14/19	14/28	14/53	14/54	14/55	14/56
	15/15	15/16	15/17	15/18	15/37	15/38	15/39
	15/59	15/60	16/01	18/46			
01021 003137	20/02#						
00172 003134	20/02#						
00175 003135	20/02#						
00676 003133	20/02#						
00777 003341	20/02#						
00H7? 003140	20/02#						
0DH?C 003136	20/02#						
0DH?L 003263	20/02#						
0DB?E 003324	20/02#						
0DB?P 003203	20/02#						
0DD?B 003200	20/02#						
0DD?R 003305	20/02#						
0DD?T 003345	20/02#						
0DE?1 003339	20/02#						
0DE?2 003332	20/02#						
0DE?4 003337	20/02#						
0DE?9 003343	20/02#						
0D1?N 003350	20/02#						
0D1?T 003342	20/02#						
0DL?C 003251	20/02#						

00L?T	003347	20/02#
00D?C	003271	20/02#
000?F	003346	20/02#
000?K	003218	3/14# 20/02
00P?C	003147	20/02#
00R?T	003150	20/02#
00T?I	003174	20/02#
00T?2	003217	20/02#
00T?3	003353	20/02#
00T?1	003164	20/02#
00T?J	003167	3/15 20/02#
00T?K	003370	20/02#
00T?P	003352	20/02#
00U?H	003344	20/02#
00W?I	003222	20/02#
0MMB	000550	4/58# 4/60
0UTLN	000110	2/64#
0?DTD	000723	NC 20/01
0?D1P	023134	MC 20/01
P17???	002536	19/60#
P37???	002224	19/60#
PAC?0	002353	19/60#
PAC?1	002500	19/60#
PAC?2	002354	19/60#
PAS?S	000203	3/14# 5/03 5/38 6/23 7/23 8/28 9/34 10/31 11/21 12/21 13/21 14/23 18/40 18/45 19/42 20/03
PA?C1	002644	19/60#
PA?C3	002643	19/60#
PA?S1	000204	3/14#
PA?SV	000205	3/14#
PC1???	002357	19/60#
PC1?1	002225	19/60#
PC1??2	002636	19/60#
PC1?5	002637	19/60#
PC4???	002507	19/60#
PC6???	002356	19/60#
PC7???	002510	3/04 19/60#
PCR?Y	002352	19/60#
PD0?1	002312	19/60#
PD0?2	002310	19/60#
PD0?5	002275	3/14 19/60#
PDE?C	002305	3/14 19/60#
PLP?T	002435	19/60#
POC?I	002267	3/14 19/60#
PSP?I	002222	19/60#
P?GOU	025046	MC 3/13
RB6???	002700	19/60#
REND	002104	18/62 18/89 18/15#
RES?T	000234	3/16# 19/43 19/60
RST?R	002370	19/60#
RTN?A	002351	19/60#
RUB?	002664	19/60#
SAV?E	002362	19/60#
SPT?G	002223	19/60#
STA?I	003126	19/60#
STO?P	003746	20/03#
SWREG	003765	3/14 20/06#

0028 FED1R

S2WPD	000244	MC	2/96	19/60					
S2WPK	001524	MC	19/60						
TAC70	002502		19/60#						
TAC7C	002506		19/60#						
TEM	000115		3/10#						
TIN?1	002640		19/60#						
TIN?2	002641		19/60#						
TIN?3	002645		3/22	19/60#					
TIN?C	002511		19/60#						
TIN?D	002551		3/14	19/60#					
TIN?M	002606		19/60#						
TIN?N	002617		19/60#						
TIN?O	002545		3/14	19/60#					
TIN?P	002554		19/60#						
TIN?R	002514		19/60#						
TIN?S	002561		19/60#						
TIN?W	002565		19/60#						
TIN?X	002513		19/60#						
TIN?Z	002555		19/60#						
TMESS	030655	MC	2/14#	5/33	6/18	7/18	8/23	9/29	10/26
			11/16	12/16	13/16	14/28			
TM?P	002361		19/60#						
TO?D1	002537		19/60#	20/62					
TO?D1	003354		20/62#						
TPR?T	002455		19/60#						
TPS?P	002376		3/14	19/60#					
TP??	003351		20/62#						
TS1?	002516		19/60#						
TT1DC	000182		2/57#	4/31	16/15	16/22	16/48	16/50	16/54
TTCD	002173		19/33	19/47#					
TTCO	002171		19/27	19/45#					
TTCR	002172		19/20	19/46#					
TT1D	002175		3/26	19/52#					
TT1I	002143		3/21	19/18#	19/22	19/24	19/52	19/56	
TT111	002152		19/21	19/27#	19/55				
TT112	002156		19/23#	19/57					
TT52	002174		19/19	19/23	19/48#	19/53			
TY1Y?	002420		19/60#						
TYP?E	002401		3/14	19/60#					
TYP?R	002505		19/60#						
T?TY0	016562	MC	19/60						
WRL00	001755		16/31#						
XDIR1	000067		2/46#	16/26					
XDIRC	000066		2/45#	18/15					
XDOAB	000065		2/44#	16/23					
YD0RS	000064		2/43#	17/13	17/36	18/27			
XDOOC	000070		2/47#	12/36	13/55	13/34	14/65		
XNI0C	000072		2/54#	7/35	8/82	11/30	11/55	17/53	
XNIOP	000076		2/53#	8/41	9/88				
XNIOS	000075		2/52#	5/45	6/62	6/38	6/54	7/29	7/56
			8/35	9/82	9/51	10/89	10/57	10/57	11/27
			11/52	12/27	12/52	13/27	12/58	14/39	15/81
			15/25	15/47					
XSBN	000072		2/49#	5/48	6/33	7/22	8/38	13/38	
XSB2	000071		2/48#	5/89	6/36	7/38	8/44		
XSDN	000074		2/51#	6/85	6/57	7/59	8/85	14/81	
XSDZ	000073		2/50#	5/21	6/68	8/85	9/11		
XXX	002141		19/87#						

0029 FBD1A

ZOC?1	002263	3/14	19/60#		
ZP0?1	002272	19/60#			
ZSU?P	002501	19/58#			
A20L	001612	14/43#	14/43		
A21L	001636	15/85#	15/10		
DIA	002107	18/17	18/18#		
D0AS	002122	18/29	18/31#		
IDUM	000105	2/68#	5/64	15/22	18/25
INTM	002052	17/10	17/33	17/45#	
?F	000000	3/14#			
?G	000001	3/14#			

