Model BMX-1

Storage Module Disk Controller

24790

600400-000 A

REVISION HISTORY		
ECO #	DATE	DESCRIPTION
0385	10/18/84	See ECO

BMX-1

Disk Drive Controller

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PREFACE

This manual provides complete instructions for installing ZETACO's model BMX-1 disk controller with cabling and tailoring the controller to meet your specific requirements. Instructions are also provided for using the programs and utilities contained on the software support tape. Detailed programming information and command descriptions have been included to aid in program development and fault analysis. The installation section steps through all phases from controller and cable installation to controller preparation using the Configurator Program to do testing and disk initialization. The information in this manual is divided into the following sections:

SECTION 1 — PRODUCT DESCRIPTION - Briefly describes the Controller and its features.

SECTION 2 — SPECIFICATIONS - Lists functional and physical characteristics of the Controller.

SECTION 3 — INSTALLATION - Contains procedures for unpacking and installing the Controller, tailoring it per system requirements, testing disk and tape subsystems and initializing disk media.

SECTION 4 — BMX-1 SOFTWARE SUPPORT PACKAGE - Describes the contents and use of the 1/2" tape included with the Controller.

SECTION 5 — TROUBLESHOOTING, CUSTOMER SUPPORT - Contains information to be used in analyzing subsystem faults and instructions on returning suspect equipment for repair.

SECTION 6 — PROGRAM CONTROL - Describes controller programming and operation.

1.0 PRODUCT DESCRIPTION

The ZETACO BMX-1 storage module disk (SMD) controller provides a full emulation integration of Data General (DG) Nova/Eclipse/MV minicomputers, SMD interface disk drives and RDOS,AOS,MP/AOS,AOS/VS operating systems. (RDOS 7.0 and above is required for operation greater than 32 sectors). It is fully compatible with DG and DG emulating minicomputers.

It supports both Burst Multiplexer Channel (BMC) and Data Channel (DCH) transfer methods.

Advantages:

- .EEPROM allows controller to be software configured
- .Meets FCC requirements
- .Faster systems throughput
- .Increased Reliability
- .Increased capacity without patching AOS
- .Hardware or software correctable ECC
- .Full two year warranty

1.1 FEATURES

- Emulation of DG 6060,6061,6067,6160,6161,6122 and 6214 disk subsystems
- Single controller is compatible with DG's full range of BMC equipped computers
- Supports two logical disks with one physical disk drive
- Simultaneous control of up to (4) SMD interfaced disk drives
- Incorporates an eleven bit SMD tag bus to accommodate full capacity of the larger drives
- Mix drives of different capacities, transfer rates, and media formats
- On-board 32 bit error checking and correcting of burst errors up to 11 bits in length
- High speed microprocessor design supports maximum transfer rates
- On-board Self-test with error reporting and LED display
- Two sector buffer
- User-definable sector interleaving
- Adjustable DCH/BMC throttle control
- Supports overlap seeks
- Offset positioning for data error recovery
- Automatic data strobe early/late for data error recovery
- Two methods of power fail detection control open cable detect
- Logging of the number of data corrections that have occurred on a per unit basis
- One second pick delay on power up controls disk drive power sequencing
- Header CRC error auto re-try

.Adjustable BMC bus break time

.SMD cable test LED

.Controller busy LED

.Dual volume drives supported (two physical volumes)

.Supports dual ported drives

.Disk drive sector setting verification

.BMC or DCH data transfer methods

.User-definable header sync byte

.Program load (BOOT) waits for drive ready

.Meets FCC requirements (4 SMD ports off backplane)

.EEPROM eliminates switches and provides total software configurability

.EEPROM Configurator Program provides total flexibility with a "user friendly" format

.Fairchild "FAST" logic used to increase performance and reduce power consumption

2.0 SPECIFICATIONS

2.1 INTERFACE

2.1.1 DRIVE

Electrical: Standard SMD interface

Driver/Receiver: MC3450/3453 differentials

Cabling:

External: One 60 pin shielded round cable ("A" cable) for the first disk drive (daisy chained).

One 26 pin shielded round cable ("B" cable) for the first disk drive (radial).

Internal: One 60 pin ribbon cable with D connector on one end that mounts in backpanel. The other end plugs into a paddleboard. See Figure 3.1.1.

One to four 26 pin ribbon cables with D connector on one end that mounts in the backpanel. The other end plugs into a paddleboard. See Figure 3.1.1.

Multiple Drives: Up to four drives (dual volume counts as two) per controller. The 60 pin "A" cable, daisy chains from drive to drive, with the last drive in the chain receiving an "A" cable terminator. The 26 pin "B" cable connects radially to each drive. (No terminators required). Reference Figure 3.8.

Performance: The BMX-1 will function with disk drives that have data rates as high as 16 MHZ bit rate or 2.0 MByte transfer rate.

2.1.2 COMPUTER

The BMX-1 can be configured for either BMC transfer or DCH Transfer.

The BMX-1 controller is compatible with the S/140, S/280, S250*, C350*, M600*, MV/4000, MV/6000, MV/8000, MV/8000II, MV8000C and the MV/10000 for BMC applications. NOTE: Do not use Slot 25 in the MV10000. For DCH applications, the BMX-1 will function in any DG minicomputer except for the Nova 3, C150 and any other side mounted backplane models which presents paddleboard space limitations. The Nova 4, S120, S140 and S280 will accommodate the DCH applications. In addition, this controller must be plugged into an I/O only slot which some computers may not have.

CAUTION: THE BMX-1 MUST BE PLUGGED INTO AN I/O ONLY SLOT.

MODEL	I/O ONLY SLOTS
S140	12-16
S280	11-20
MV4000	12-20
MV8000-II	9-21
MV10000	13-24, 26-36
Nova 4C(5 Slot)	3-5 (DCH Only (BMC not supported))
Nova 4S/x	12-16 (DCH Only (BMC not supported))
S120	12-16 (DCH Only (BMC not supported))
MV6000	13-16 (Main Chassis)
MV8000	29-42, 48-56
MV8000C	14-20
*M600	30-37
*S250	I/O Only Backplane Option
*C350	I/O Only Backplane Option

*NOTE: BMX-1 REQUIRES MODE 3 MODIFICATION.

2.2 POWER

+5 VDC @ 8.0 Amps typical
-5 VDC @ 0.45 Amps typical

2.3 PHYSICAL

Dimensions: 15" x 15" x 1/2"

Shipping Weight: 15 pounds - includes cables, diagnostics and documentation.

Cables:
60 pin round "A" cable (external) - 15 feet
26 pin round "B" cable (external) - 15 feet
60 pin ribbon (internal) - 1.5 feet
26 pin ribbon (internal) - 1.5 feet

Paddleboards: Passive backplane paddleboard with 4 - 26 pin cable connectors. (A backplane)

Passive backplane paddleboard with 1 - 60 pin cable connector. (B backplane)

2.4 ENVIRONMENTAL

Operating Temperature: 0 to 55 degrees C

Relative Humidity: 10% to 90% (non-condensing)

Exceeds all Nova/Eclipse/MV minicomputers temperature and humidity specifications.

3.0 INSTALLATION

Please read the following BMX-1 installation section carefully.

3.1 UNPACKING AND INSPECTION

All parts comprising of the Model BMX-1 are shipped in one container consisting of:

a) Controller (500400-000)

b) Backplane Paddleboards
   A - 300024-000
   B - 300025-000

c) Internal FCC Cables (Optional)
   A - 300000-000
   B - 300014-000

d) External FCC Cables (Optional)
   A - 300013-002 (16 feet)
   B - 300011-002 (16 feet)

e) BMC Bus Cables
   (100910-000)

f) Diagnostic Software Including Configurator
   (M276)

g) Technical Manual (600400-000)

On receipt of the Model BMX-1 from the carrier, inspect the shipping carton immediately for any evidence of damage or mishandling in transit.

If the shipping carton is water stained or damaged, contact the carrier and shipper immediately, specify the nature and extent of the damage and request that the carrier's agent be present when the carton is opened.

ZETACO'S warranty does not cover shipping damage.

For repair or replacement of any ZETACO product damaged in shipment, call ZETACO to obtain return authorization instructions.

3.2 MODE AND FEATURE SELECTION

The BMX-1 has been set to Mode 1 at the factory unless specified otherwise. The BMX-1 must be configured in two ways: 1) configure to the correct CPU via 24 pin mode plug on the circuit board and the B paddleboard 2) choosing a feature select with a jumper plug on the circuit board and the B paddleboard.

First configure the mode plug to your CPU type. Three modes are possible. Modes 1 and 2 can be set or changed by the end user, while Mode 3 is configurable only at the factory. Table 3.2 clearly indicates the CPU mode selections.

MODE 1 - This is the standard configuration and provides all features to include: dual port, extended 11 bit tag bus (for use with disk drives in excess of 1024 cylinders) and remote pick hold spin up sequencing.

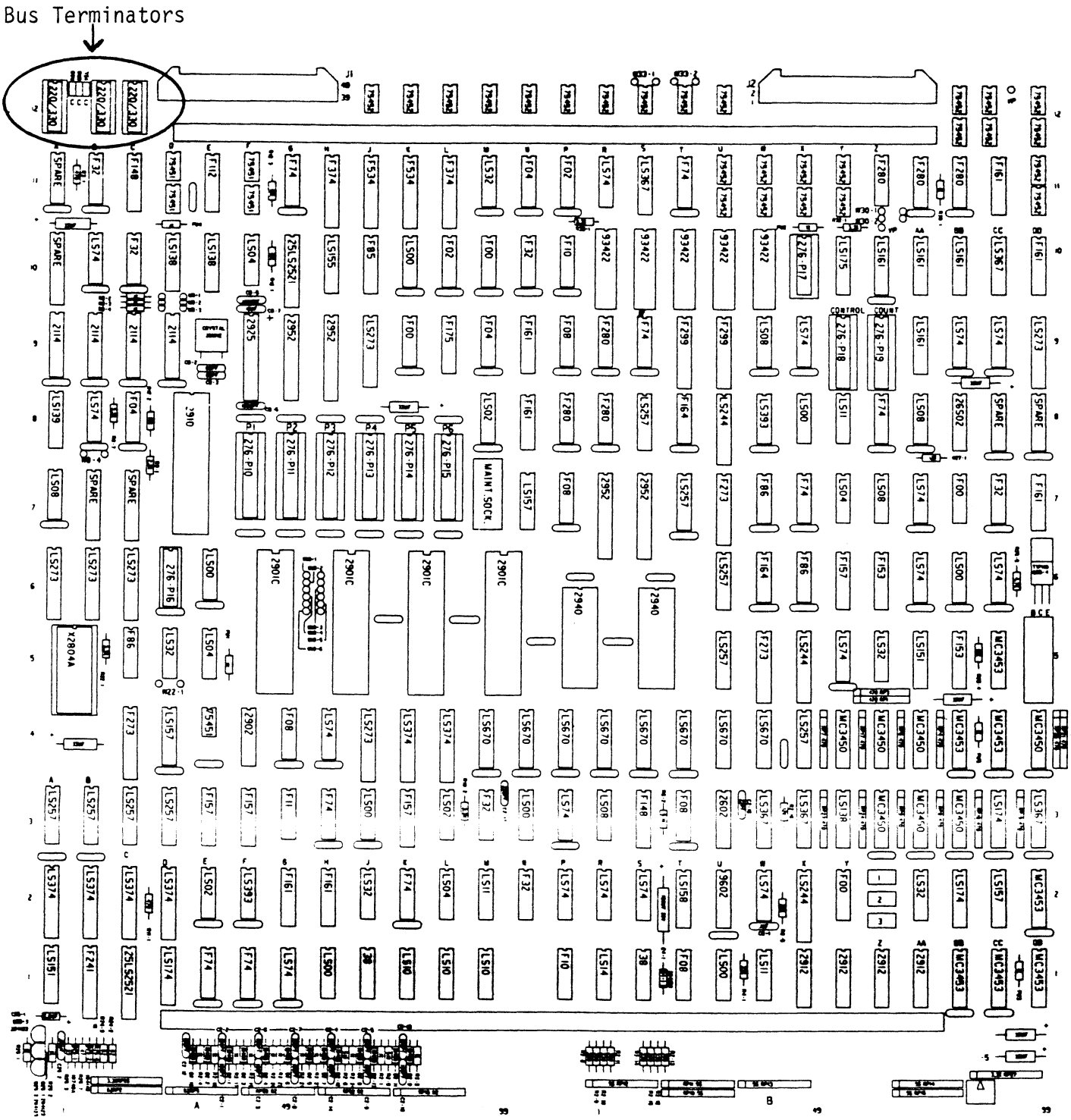
MODE 2 - Required on certain CPU types but sacrifices two of the 3 available features. You would choose one of the three following features: dual port, extended 11 bit tag bus (for use with disk drives in excess of 1024 cylinders) and remote pick hold spin up sequencing.

MODE 3 - Factory configurable only. Provides for all 3 features mentioned above.

When changing from Mode 1 to Mode 2 simply remove the 24 pin mode plug (on the BMX-1 and B paddleboard) turn it 180 degrees and re-insert. Second, choose the feature plug (on the BMX-1 board and paddleboard) for the desired feature. To determine which CPU mode to choose reference Table 3.2.

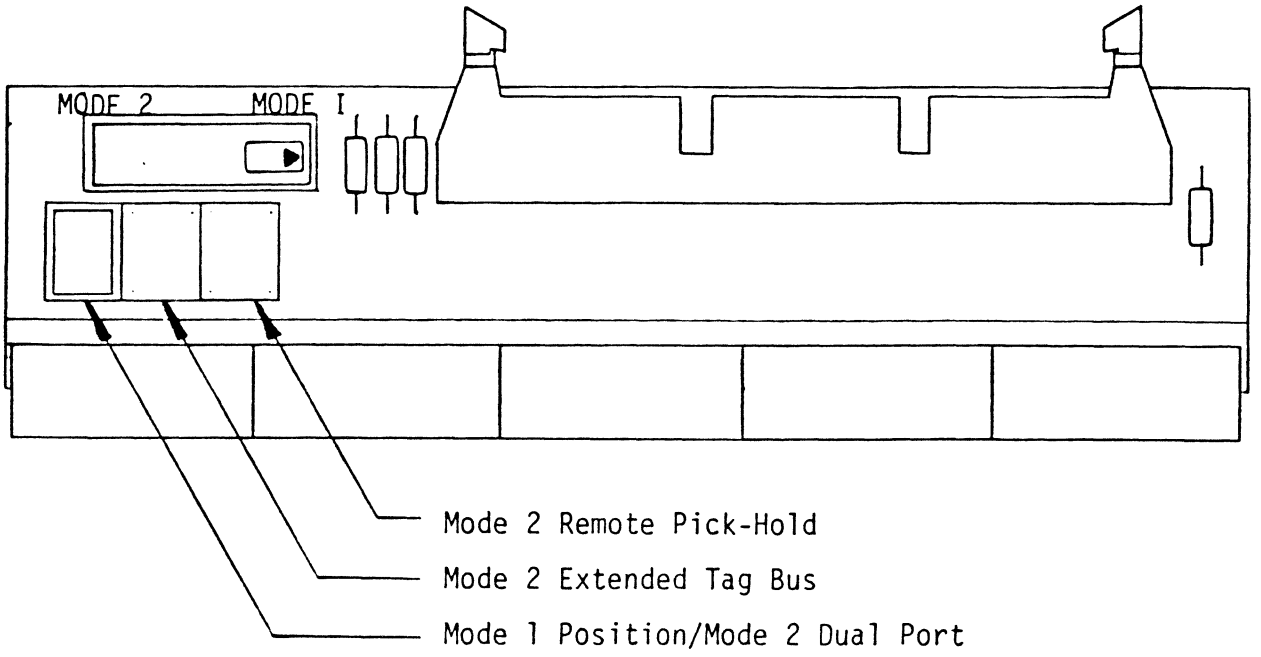
NOTE: For some CPU's the BMX-1 can be configured in either Mode 1 or Mode 2. Mode 1 is preferred since it retains all 3 features.

The feature selections are referenced in Figures 3.1 and 3.2. The 3 features are dual port, extended 11 bit tag bus (for use with disk drives in excess of 1024 cylinders) and remote pick hold spin up sequence. When you have chosen CPU Mode 1 you must have the feature select plug installed in the "Mode 1 Default/Dual Port" position. Without this feature select plug inserted properly, the Controller will not function properly.



BOARD LAYOUT

FIGURE 3.1



"B" PADDLEBOARD

FIGURE 3.2

NOTE: When you have chosen Mode 2 you have a choice of 1 of the 3 features. You cannot choose more than one. Example: for dual port-insert the feature plug marked "Mode 1 Default/Dual Port" on the circuit board and the B paddleboard.

MODEL	MODE 1	MODE 2	MODE 3 ⚠3
S140 ⚠5	X		
S250 ⚠1			X
S280	X	ALT.	
C350 ⚠1			X
M600			X
MV4000	X	ALT.	
MV6000 ⚠2		X	
MV8000		X	
MV8000-II		X	
MV8000C		X	
MV10000 ⚠4	X	ALT.	

NOTES:

⚠1 Require optional I/O only backplane.

⚠2 Do not use I/O expansion chassis slots.

⚠3 Mode 3 is factory configurable only.

⚠4 MV10000 REV 02 backplane and less requires Mode 2 operation.

⚠5 Nova 4C, Nova 4S, Nova 4X and Eclipse S120 also require Mode 1 for DCH.

NOTE: Mode 3 is factory set with hardware changes. Mode 3 will display the Mode 1 selection plug but in reality will be configured to Mode 3. BMX-1's factory set for Mode 3 will be identified with a paste on sticker.

CPU MODE SELECTION

TABLE 3.2

3.3 BMC BUS TERMINATION

If there is more than one BMC device daisy chained on the BMC bus, then the BMC controller at the end of the bus must have the bus terminators installed. If the BMX-1 is to be installed as the last or only BMC controller, then make sure the 3 terminator DIP's are installed at locations A12,B12, and C12 on the controller board. Reference Figure 3.1.

NOTE: The BMX-1 is shipped from the factory with these terminators installed unless otherwise specified.

3.4 POWER FAIL PROTECTION

The BMX-1 disk controller contains a double protection power fail scheme. The DG CPU outputs a signal on pin B21 called "Power Fail" which gives an early warning of power loss. This is used on the BMX-1 to disable the drives write circuitry through the open cable detect line.

Slots 12-15 in the S140 do not have power fail, therefore, a jumper wire should be installed to enable this feature. Slot 16 has it available on B21. Refer to your CPU manufacturer's manual if additional information is needed.

In addition, the BMX-1 contains its own power fail circuitry to further protect drive data integrity in the event the slot where the board is installed loses power.

3.5 BOARD INSERTION

Carefully select an I/O only slot and guide the controller board into the desired slot by allowing the edges of the board to follow the guides evenly. Use the lock tabs on the two outside corners to provide leverage when the board meets the connector. Use equal pressure on both lock tabs until the board seats firmly into the backplane connectors.

CAUTION: AN I/O ONLY SLOT MUST BE USED. COMPONENT DAMAGE WILL OCCUR IF A SLOT OTHER THAN AN I/O ONLY SLOT IS USED. REFER TO SECTION 2.1.2. ZETACO'S WARRANTY IS VOID IF A NON-I/O ONLY SLOT IS USED.

3.5.1 PADDLEBOARD INSTALLATION

Two paddleboards connect onto the minicomputer backplane pins (observe which slot the BMX-1 occupies in order to determine which set of backplane pins to use for connection), one paddleboard connects to the "A" backplane and one on the "B" backplane. Make sure the CPU backplane pins are straight first, then reference Figure 3.3 for proper installation. The paddleboard (labeled "B") with the 60 pin header goes on the "B" backplane. The paddleboard (labeled "A") with the 4-26 pin headers goes to the "A" backplane.

3.6 PRIORITY SELECTION

The controller must receive two priority signals from the DG minicomputer backplane, DCH priority in (Pin A94) and interrupt priority in (Pin A96).
If there are vacant slots between the controller and the processor, priority jumper wires must be installed to obtain priority continuity between controllers. To jumper across unused slots, connect DCH priority out (Pin A93) to DCH priority in (Pin A94) and interrupt priority out (Pin A95) to interrupt priority in (Pin A96).

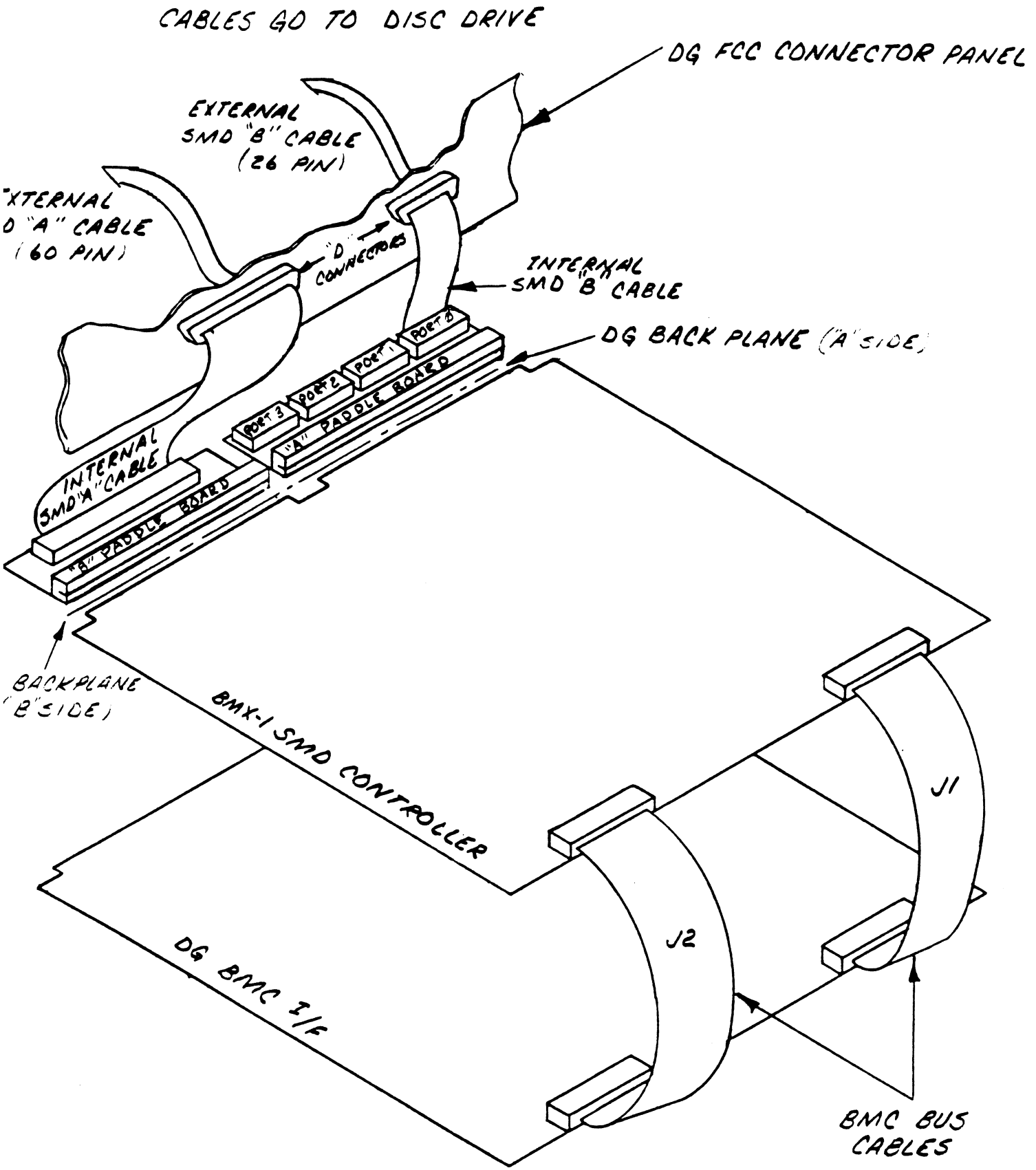
If the BMX-1 is to be configured at or near highest priority in an S140 computer, (Slots 12-16 I/O Only) jumper the priority first up to the BMX-1, then back down to the additional controller boards in Slots 4 and up.

3.7 CABLING

3.7.1 INTERNAL DISK CABLING

As shown in Figure 3.3, the 60 pin (female end) conductor cable (referred to as internal SMD "A" cable) plugs into the "B" paddleboard. The other end of this cable (D connector) mounts into the backpanel.

The 26 pin (female end) conductor cable (referred to as internal SMD "B" cable) plugs into the "A" paddleboard. The other end of this cable (D connector) mounts into the backpanel. (Observe the port assignments on the paddleboard in order to keep track on the backpanel which port is 0-3.) If more than one drive is to be connected we recommend labeling the associated port/s on the CPU connector panel.



BOARD DIAGRAM

FIGURE 3.3

3.7.2 EXTERNAL DISK CABLING

As shown in Figure 3.4, the 60 pin "A" cable connects between the appropriate backpanel D connector and the first drive then continues from drive to drive in a daisy chain fashion. The last drive in the chain must have a terminator installed in place of the daisy chain cable. This terminator is located within the disk drive.

Each drive must have a 26 pin "B" cable connected between the drive and the backpanel D connector in a radial fashion.

Insure that the port is configured (by use of Configurator Program) to match the corresponding drive type plugged into that port.

Refer to the drive manufacturer's manual for proper subsystem grounding if required.

3.7.3 BMC BUS CABLING

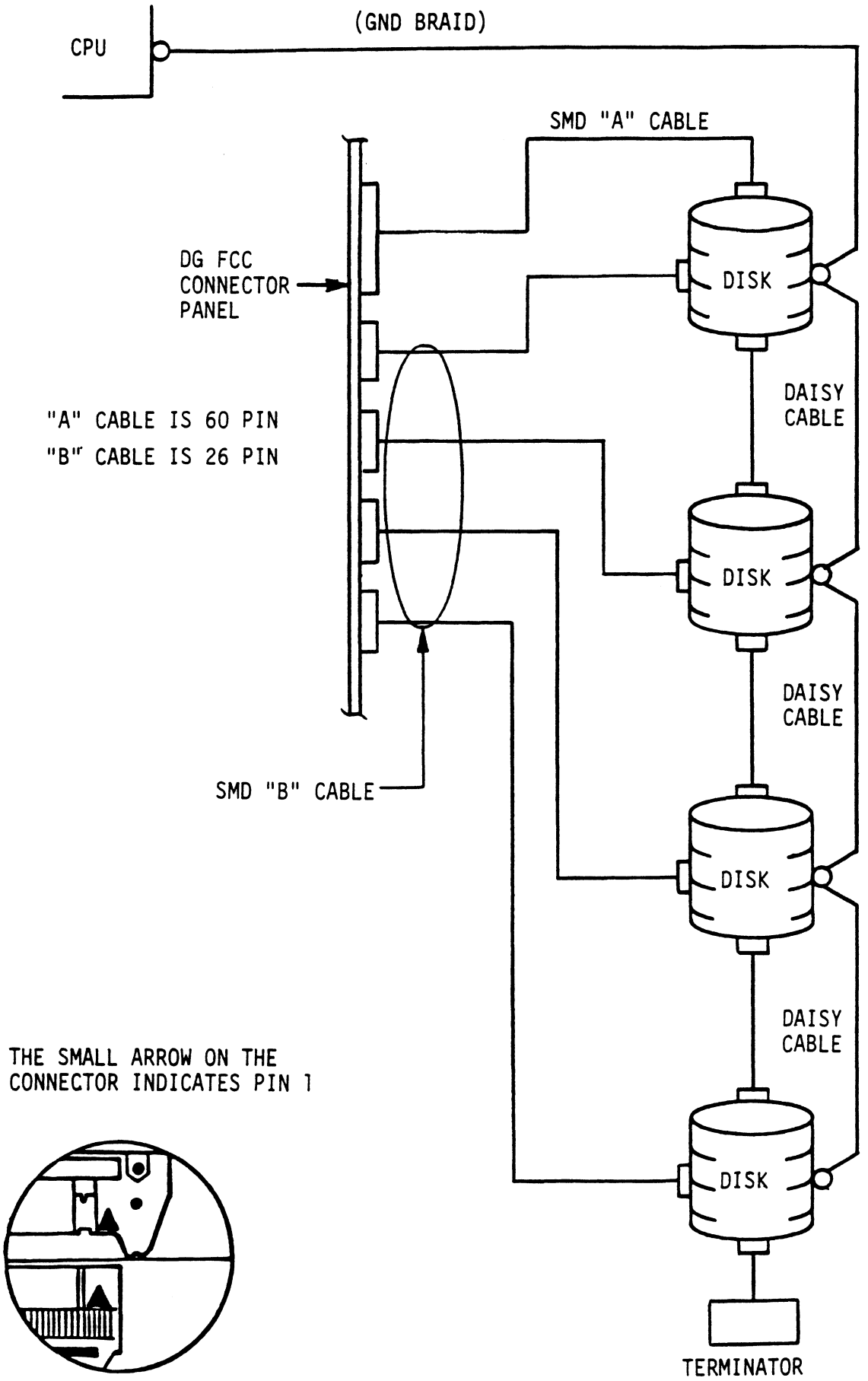
The BMC bus cables (2) provided have a single 40 conductor plug on one end and a group of 4,6, or 8 plugs on the other end. Install the BMC bus cables as shown in Figure 3.3, observing proper connector orientation, by plugging the single plug end of the cables into the DG BMC I/F and the multiple plug end of the cables into the BMX-1 and other BMC controllers.

Reference Section 3.3 for BMC termination installation.

3.7.4 SYSTEM GROUNDING

Because the power system safety ground does not necessarily satisfy all system grounding requirements, additional connections are required to earth ground, referred to as system ground. The controller and its attached drive/s must be connected to a single-point ground system. Ground connections are made via ground braids that pass from drive to drive, drive to computer chassis and computer chassis to earth ground.

WARNING - To ensure proper ground return to earth, each component in the system must be connected using a daisy chain ground system. The AC and DC grounds within each drive may need to be joined (consult your drive manual). The drives must then be joined by a daisy chain grounding braid and connected to the grounding post at the rear of the computer cabinet.



THE SMALL ARROW ON THE
CONNECTOR INDICATES PIN 1

DAISY CHAINING DRIVES

FIGURE 3.4

3.8 DISK DRIVE CONFIGURATION

Insure the disk drive you are installing has the index and sector signals on the A cable. If these signals are on the B cable only, the controller will not function correctly.

To set up the BMX-1 you must know the disk drive manufacturer and model number, the operating system revision level (RDOS Only), the DG CPU Model and the type of Format (ZETACO, ZETACO High Speed or ALT). If you are unsure of the correct sector count to use for set up of the disk drive, you may refer to the BMX-1 Configurator Program which reflects the sector count to be used for the various disk emulations. The BMX-1 Configurator Program can be run only after the BMX-1 is installed. To find your disk drive (listed alphabetically by manufacturer) within the Configurator Program, refer to the main menu and choose D (Disk Type/s) then answer the number of disks on the controller. Press H (Help) to review the disk drive characteristics. Listed with the drive manufacturer and model number are the characteristics for that drive to include heads, cylinders, sectors and unformatted capacity.

NOTE: The Configurator Program is a stand-alone utility. See Sections 3.10 and 4.2 for additional information.

3.8.1 SPECIAL CONSIDERATIONS

SPECIAL CONDISERATIONS FOR THE FUJITSU 2351 SECTOR SELECTION

The FUJITSU 2351 should be set to 48 sectors per track by setting the number of bytes per sector to 586 and not 587 as in the Fujitsu 2351 manual. The following jumpers should be set for 586 bytes per sector:

BC7	2-3	6-7	10-11	12-13
BD7	3-4	6-7	9-10	13-14
BE7	3-4	5-6	10-11	13-14
BF7	3-4	6-7	10-11	13-14

SPECIAL CONSIDERATIONS FOR THE CDC 9457 (LARK II) AND

CDC 9455 (LARK)

Insure options W-4 and W-8 are installed within the disk drive. W-4 identifies Auto Seek on head change. W-8 identifies two volumes (CDC terms it CMD). The CDC Lark is 32 sectors.

3.8.2 DRIVE PICK-HOLD

On initial power up, the Controller will delay activating pick-hold (spins up drive) for one second. This feature eases the initial current demand on the AC power source. This feature requires that the disk drive be selected for remote operation. If the remote spin up feature has not been selected in Mode 2, then pick-hold is grounded which will issue a continuous pick-hold.

3.9 JUMPER SELECTABLE OPTIONS

The configuration of the BMX-1 is eased by making most of the features software configurable through a program called Configurator. Section 3.2 describes the jumper selectable features. The jumpers are configured from the factory to satisfy most cases.

3.9.1 EEPROM WRITE DISABLE

The BMX-1 provides the means to hardware disable any further alterations to the configuration EEPROM. To write disable the EEPROM, cut foil jumper W22-1 located at D5 on the controller board. Foil jumper W22-1 is factory installed.

3.9.2 LOOP ON SELF-TEST

To loop on Self-test, insert jumper W8-4 location B8 on the controller board. This is an added feature for diagnostic purposes. For typical use of diagnostics leave Loop On Self-test disabled. The BMX-1 is factory set with Loop On Self-test disabled.

3.9.3 DISABLE BMC BUS PARITY

The BMX-1 performs address and data parity checks on the BMC bus when this jumper is installed. The BMX-1 is factory set with BMC bus parity enabled unless otherwise specified. To disable parity checks, cut foil jumper W30-1 and install a jumper W30-2 at location Z11 on the controller board.

3.9.4 FAILSAFE PRIMARY DEVICE CODE

In the remote case that it becomes desirable to hardware force the device code of the BMX-1 to the primary value of 27 octal, it can be done by cutting foil jumper W33-1 at location U11 on the controller board.

NOTE: This feature would seldom be required and the jumper is factory installed to allow any device code. The BMX-1 EEPROM is factory configured to a device code of 41 octal to eliminate the possibility of conflicting device codes on initial installation.

3.10 POWERING UP AND CONFIGURING

Turn System power ON. The BMX-1 will perform an initial "Self-test" by briefly lighting a red LED. A good test is indicated by the LED turning OFF. For more details refer to Section 5.0.

Once the Self-test LED goes out, a cable test is performed. In order for the cable test to pass, the disk drive must be cabled up properly and powered on. If the yellow LED comes on, then a cabling problem may exist. An I/O reset switch will re-execute the test. Refer to Section 3.7 for proper disk cabling.

NOTE: Some disk drives may not be capable of being selected until they are spun up which will cause a cable fault. If this occurs, wait until the drive is ready and then depress the I/O Reset Switch. The green LED is used to display controller busy.

CONFIGURING THE BMX-1:

A program called BMX-1 Configurator (File #2) is supplied with the controller board on a 1/2" magnetic tape labeled M276. The Configurator Program replaces hardware switches. You must run File #2 on tape M276 in order to install your BMX-1.

NOTE: The BMX-1 has been factory set to device code 41 octal unless otherwise specified. This is to eliminate the possibility of conflicting device codes on initial installation. However, it is intended to be changed to 27 octal or whatever device code you desire. The BMX-1 disk controller has been shipped from ZETACO with most Configuration facts set to standard recommended values. However, the Controller must be tailored for the disks you will be using. Section 4.2 describes the operation of the Configurator Program.

The following is a description of the configurable features supported by the BMX-1.

3.10.1 DATA TRANSFER MODE

The BMX-1 can be configured for either DCH or BMC.

3.10.2 DEVICE CODE

The BMX-1 can be configured to any device code between 20 octal and 76 octal. However, the primary is 27 octal and the secondary is 67 octal. Device code 41 octal has been set at the factory unless otherwise specified.

If the device code is changed, it will not take affect until the computer is powered down and back up. See Section 3.10 for additional information.

3.10.3 BMC BUS PRIORITY

The BMX-1 has the capability of co-existing with up to seven other BMC controllers. However, some DG computers only support up to four BMC devices such as the MV/4000. In this case you must select priority 0 thru 3. The lower the priority number the lower the priority level. If there is more than one BMC device, make sure you select a priority level that is different than the other BMC device.

3.10.4 THROTTLE BURST RATE

This is defined as the number of word transfers that take place over either the DCH or the BMC on a single bus access. Throttle adjustment is dependent on the type of system configuration the Controller is installed into. Too low of a throttle setting could result in slow disk performance and too high of a setting could cause a data late on another DCH or BMC device. The BMX-1 supports 4, 8, 16, 32, 64, 128 and 256 word Burst Rates. A Burst Rate of 16 is recommended for DCH and 32 is recommended for BMC.

The BMX-1 allows you to select a different Burst Rate for each SMD port thereby giving the ability to fine tune the bus to a particular speed of the disk drive.

3.10.5 BREAK COUNT

This is defined as the period of time that the BMX-1 is off the BMC bus. This is utilized only in the BMC mode. With the break count set to 0 there is an inherent 1.4 microsecond OFF time (delay between requests) which is the recommended break count setting. Each additional count adds 200 nanoseconds to the OFF bus time.

If there are other BMC devices present, it may be desirable to increase this count to allow more time for the other devices to access the bus. If the break count is set too large, slow disk performance may result. A larger break count also allows the CPU more memory time.

The BMX-1 allows you to set a different break count on each SMD port which gives the ability to fine tune the bus according to disk speed.

3.10.6 SYNC BYTE

The BMX-1 supports a media format which contains a header sync byte and data field sync byte versus a sync bit. The sync byte provides better header address and data integrity. This sync byte is user-definable for each SMD port on the BMX-1. Any value between 01 hex and FF hex is acceptable, although 93 hex (223 octal is the recommended value. When entering a sync byte use the octal number. This feature can provide a means for disk pack access security between different disk subsystems.

3.10.7 ERROR CORRECTION ENABLE

When this function is enabled, on-board error correction and data strobe early/late occur automatically on bad data. Also, a running count of ECC corrections and successful data strobe early or late data recoveries are logged in scratch pad memory (separate count for each unit). With this function disabled, ECC corrections must be handled by the software. This feature can be selected on any port.

3.10.8 MEDIA FORMAT

The BMX-1 has the capability of supporting, simultaneously, up to four different types of disk media format. This also means that each SMD port could be running a totally different media format.

Currently supported is a standard ZETACO format (ZETACO) a High Speed (ZETACO H.S.), version for 15-16 MHz, an alternate format, and a DG Kismet format (optional) See Section 4.3 for detailed media format information.

We recommend using the ZETACO format due to its added features; more error checks on header, conforms to neccesary drive specifications.

3.10.9 SECTOR INTERLEAVE RATIO

The BMX-1 supports any interleave from 1:1 to 6:1 and each SMD port can have a different interleave ratio. 1:1 interleave is recommended for optimum performance and should be sufficient on the BMC in most cases. Interleaving may be desired to fine tune a systems performance. This is to avoid going a full revolution on the disk when the CPU cannot respond fast enough to catch the next consecutive sector.

If system performance is too slow to access the next consecutive sector, which is indicated by extremely slow disk performance, then an interleave factor of 2:1 or greater should be selected. To maintain optimum performance, don't select an interleave greater than is required to access the next consecutive sector in a multiple sector transfer.

3.10.10 DISK TYPES

The BMX-1 is capable of running with virtually any disk drive that meets the SMD specifications. However, when running under AOS, only those drives that meet the sizing characteristics of the supported emulations can be used. Under RDOS the BMX-1 can take advantage of the full capacity of most disk drives because DKINIT has been modified (CSDKINIT) to allow deviation from the standard DG disk emulations.

DUAL VOLUME DRIVES:

If a dual volume drive is to be connected, the drives unit number plug must be an even number. A dual volume drive is treated as two logical units, so a maximum of two dual volume drives, or one dual volume drive and two single volume drives can be connected.

NOTE: The Kismet Family - 6160, 6161 and 6214 (under AOS and AOS/VS) only allow two (single volume) drives or one dual volume to be connected.

There are two forms of dual volume drives. One has two physical volumes. (Examples are CDC Lark, Amcodyne 7110 and CDC 9448 Series.)

The other form of dual volume is treating one physical drive as 2 logical units (if drive characteristics permit). For example, Dual 6061 emulation (AOS) operation for the Fujitsu 2351 Eagle, or dual 6161 emulation (AOS) operation for the APS 4835 drive, or Dual 6122 emulation operation for the APS 4865 drive.

In all cases, dual volume drives must have both their units formatted before reading or writing.

3.11 FORMATTING

Boot up the formatter program and run a minimum of three passes or preferably six passes. For ZETACO Disk Formatter refer to Sections 4.0, 4.3 and Appendix A. For the next installation step we recommend running disk Reliability in order to exercise and test the disk system. Refer to Section 4.4 and Appendix A. If you are using AOS we recommend you run Diagnostics in addition to Reliability. Under AOS run Diagnostics first and Reliability second. Refer to Diagnostics Section 4.4. The final step involves the use of CSDKINIT for RDOS or DFMTR for AOS. Before you load any RDOS or AOS onto a Model BMX-1 disk you must initialize the disk by running CSDKINIT (RDOS) or DFMTR (AOS). For CSDKINIT refer to Section 4.6. For DFMTR refer to DG's Manual.

3.12 SYSGEN CONSIDERATIONS

Listed below is an example of part of the RDOS system generator.

1. Number of 6060/6061/6067/6122/6160/6161 Disk Controllers (0-2)

2. Device Primary ("0") or Secondary ("1")

3. Controller #1 6160/6161 Type? ("0"=NO, "1"=YES)

4. Number of Devices for Controller #1 (1-4)

5. Number of other types of Moving Head Disk Controllers (0-2)

6. Device Primary ("0") or Secondary ("1")

NOTE: On line three answer NO when running RDOS. When you answer NO you allow up to four disk drives (6160 or 6161) to be connected to the BMX-1. If you answer YES you allow only two disk drives (6160 or 6161) to be connected.

4.0 SOFTWARE - DIAGNOSTICS, CONFIGURATOR AND UTILITIES

In addition to the diagnostic functions provided by the BMX-1 controller via on-board Self-test, ZETACO provides Diagnostic and utility software. The M276 magnetic tape included in the controller package contains these programs.

Each of the programs on the M276 tape have been written by ZETACO specifically for the BMX-1 controller. You should use this tape for media formatting, disk Diagnostics and Reliability, configurating and RDOS Utilities. DG's corresponding programs may not work on this controller. The disk media formatter on the M276 tape will let you format the media in any of the formats which are supported by the BMX-1 controller.

4.1 USING THE M276 TAPE

The M276 tape is structured so that the programs on Files 2-7 can be loaded and executed directly from the tape. Files 0 and 1 contain the software which enables you to boot from the tape and select the particular program you want loaded into the system. Each of the programs on Files 2-7 is a stand-alone program. This means that they do not need, and cannot have, an operating system running when they are executed.

Programs cannot be loaded onto your disk directly from Files 0-7. File 8 for RDOS and File 9 for AOS (or AOS/VS) contain the programs in the standard system dump format and you can load them from these files to your disk. Even after the programs have been transferred to your disk, you should retain the M276 tape in case of disk subsystem problems.

The following sequence of events is recommended by ZETACO. Each step is described in greater detail in the subsequent sections of this chapter.

1. Mount the M276 tape and boot it.

2. Select #2 on tape menu - configure the Controller.

3. Select #3 - format the media, if you need to.

4. Select #4 - disk Diagnostics.

5. Select #5 - disk Reliability.

   NOTE: It is not essential that you run Diagnostics or Reliability, however, they will locate disk sub-system problems. It is better that this be checked out at this point than after you have loaded your data.

6. If the controller is to run in an RDOS system, select #6 to initialize the disk. If the controller will not run in an RDOS system, proceed to the disk initializer program on the DG system tape for your operating system.

7. You can load the programs from File 8 or File 9 any time after you have built your disk.

The Bootstrap Procedure for the M276 tape is:

1. Mount the M276 tape on the drive and put it on-line. Be sure that the BPI setting matches that specified on the tape label.

2. Program Load - The method of program load varies for the different processors. Some of the possibilities are described here.

If your system has front-panel switches, set them to 100022 when loading from the primary tape drive, or to 100062 when loading from the secondary tape drive. Then press reset and the program load switch.

For the S140 virtual console, set 11A to 100022 (or 100062 for secondary tape drive). Then enter 100022L (or 100062L).

For the S120 virtual console, enter 22H (or 62H for the secondary tape drive).

For MV class CPU's you must enter the full virtual console and respond to the prompt:

```
SCP-CLI>
with BOOT 22 (or 62 for secondary tape)
```

3. M276 Menu will be displayed:

```
    FILE #      PROGRAM

      2         BMX-1 CONFIGURATOR
      3         DISK FORMATTER
      4         DISK DIAGNOSTICS
      5         DISK RELIABILITY
      6         CSDKINIT-RDOS DISK INITIALIZER
      7         CSDSKED-RDOS DISK EDITOR
      8         ".SV & .LS" Files and any Utilities in
                RDOS DUMP Format
      9         ".SV & .LS" Files and any Utilities in
                AOS DUMP Format
     10         AOS/VS Utilities in AOS DUMP Format

    File Number?
```

You should enter the number opposite the program you wish to execute.

To load files from File 8 or 9, use the standard CLI Command for loading from tape.

```
RDOS:   DIR %MDIR%
        INIT MT0
        LOAD/A/R/V MT0:8
        RELEASE MT0

AOS:    SUPERUSER ON
        DIR :
        LOAD/R/V @MTA0:9
        REW @MTA0
        SUPERUSER OFF

AOS/VS: SUPERUSER ON
        DIR :
        LOAD/R/V @MTC0:9
        DELETE/V AOSECC.PR
        LOAD/R/V @MTC0:10
        REW @MTC0
        SUPERUSER OFF
```

4.2 BMX-1 CONFIGURATOR

The BMX-1 controller is configured for your particular system via software. Before you will be able to access your disk/s, the BMX-1 controller must be configured to reflect your setup. To do this, load the BMX-1 Configurator from the M276 tape per instructions in the preceding section. The Configurator Program is located on File #2 of the tape.

The program displays a heading and an introduction. You should read the introduction carefully before proceeding. Initially you must specify on which device code the BMX-1 controller is currently running. This is so that the current configuration facts can be read from the EEPROM on the Controller. If this is the initial installation, the BMX-1 will be set at device code 41 octal to eliminate the possibility of conflicting device codes.

The BMX-1 Configurator Program includes both a HELP menu for general questions and a HELP command for each item. Please use these functions whenever you are uncertain as to what to do. The purpose of the Configurator is to change the pre-set facts to reflect your environment, and then to update the EEPROM on the Controller. The Controller will then perform according to your particular specifications.

SYSTEM REQUIREMENTS TO RUN CONFIGURATOR

Nova/Eclipse or MV Family CPU with 32K Words Memory
BMX-1 Controller Board/s
Console on Device 10/11
Non-DMA Printer at 17, in order to use Logging

4.3 DISK FORMATTER

The disk formatter program is a utility designed program to format and check disk packs to be used on the disk systems. The 1/2" magnetic tape supplied contains File #3 (disk formatter). File #3 in conjunction with the BMX-1 hardware supports the three formats (ZETACO, ZETACO High Speed and Alternate).

In most cases it is recommended you disable ECC correction with the Configurator prior to running the disk formatter.

The following is a sample dialogue:

```
ZETACO SMD DISK CONTROLLER FORMATTER REV. XX

STARTING ADDRESSES:

    500-FORMATTER/CHECK PROGRAM
    501-CHECK PROGRAM ONLY
    502-ERROR LOG RECOVERY
    503-COMMAND STRING INTERPRETER

ENTER DEVICE CODE [27]: 67

SET SWPAK AS PER SECT 8.0 OR HIT (CR) TO CONTINUE

START TIME? - MON,DAY,YR  HR,MIN

# PASSES TO FORMAT COMPLETION? - 6

UNIT     TYPE     HDS     CYLS     SEC/TRK     SECTOR PULSES

 0        0        5      823        32             32
 2        1        5      815        24             24

ENTER UNIT NUMBERS (0,1,2,3) TO RUN:  0,2

UNIT:  0

ENTER TYPE OF DISK:  0

UNIT:  2

ENTER TYPE OF DISK:  1

FORMATTING UNIT 0,2
```

See formatter text at end of manual for further details.

4.4 DISK DIAGNOSTIC

This Diagnostic program is provided to find failures that are related to the basic operations of the disk controller. The ID bits (AOS) shown in the sample below will aid in checking the configuration. Switch settings for AOS are described in the Installation Section, Figure 3.6.

Load the program from the tape provided. (See M276 tape loading in Section 6.0).

The following is a sample dialogue for 6160 (AOS):

```
ZETACO SMD DISK CONTROLLER DIAGNOSTIC REV. XX

STARTING ADDRESSES:

    200-DIAGNOSTIC (INITIALIZE)
    201-DIRECT ODT ENTRY
    202-RANDOM SEEK EXERCISERS
        SEEK EXER 1 IS A SINGLE DRIVE EXERCISER
        SEEK EXER 2 IS A TWO DRIVE EXERCISER WITH SEEK OVERLAP
    500-DIAGNOSTIC (RESTART)

ENTER DEVICE CODE [27]: 67

ANY DUAL VOLUME UNITS?  ENTER 1

ENTER UNIT NUMBERS (0,1,2,3) TO RUN:  0,1

SET SWPAK AS PER 8.0, LISTING OR ENTER RETURN (CR) TO CONT.

TESTING UNIT 0

.
.
.
.


UNIT      HDS      CYLS      SEC/TRK      SECTOR PULSES

  0        5        823         35             35
```

These are the units and characteristics found, do you want to loop on reading them? Enter 1, otherwise enter Return (CR).

.
.
.
.

See Diagnostic text at the end of the manual for further details.

```
ADDRESSABLE SECTORS/TRACK WITH THIS CONTROLLER IS 64.
DRIVE UNIT #0 WILL BE IDENTIFIED AS A 6160 (73 MBYTE)
BY AOS OR AOS/VS.
DRIVE UNIT #1 WILL BE IDENTIFIED AS A 6160 (73 MBYTE)
BY AOS OR AOS/VS.
TEST(S) COMPLETE.
SEEK EXERCISER TESTS.
PASS
```

DISK DRIVE SECTOR VERIFICATION -

The BMX-1 provides a feature which allows the diagnostic programs to display the actual number of sectors that the disk drive is set for.

This count will not include a small remaining sector at the end of the track which very often occurs.

4.5 DISK RELIABILITY

The disk Reliability program is a maintenance program designed to exercise and test the disk system. The program will test from one to four drives. (ZETACO Reliability supplied on 1/2" magnetic tape.)

The following is a sample dialogue:

```
ZETACO...DISK RELIABILITY REV. XX

STARTING ADDRESSES:

    500-RELIABILITY TEST
    501-RELIABILITY TEST WITH OPTIONS
    502-DISK ADDRESS TEST
    503-COMMAND STRING INTERPRETER
    504-FORMAT ONLY
    505-RUN ALL TESTS
    506-SEEK EXERCISER
    507-RANDOM SEEK EXERCISER
    510-ERROR COUNT/LOG RECOVERY
```

```
ENTER DEVICE CODE [27]: 67

STARTING ADDRESS = 505

SET SWPAK AS PER 8.0, OR HIT (CR) TO CONT.

ARE MAPS TO BE EXERCISED (YES/NO)?  YES

START TIME? - MON,DAY,YR  HR,MIN

ANY DUAL VOLUME UNITS (YES/NO)?  NO

UNIT      TYPE      HDS      CYLS      SEC/TRK      SECTOR PULSES

 0          0         5       823          32             32
 2          1         5       815          24             24

ENTER UNIT NUMBERS (0,1,2,3) TO RUN:  0,1

UNIT:  0

ENTER TYPE OF DISK:  0

UNIT:  1

ENTER TYPE OF DISK:  1
TESTING UNIT 0,1
```

See Reliability text at the end of manual for further details.

4.6 CSDKINIT - RDOS DISK INITIALIZER

(ZETACO's version of DSKINIT, referred to as CSDKINIT, is supplied on 1/2" magnetic tape.)

Initializing a Model BMX-1 disk -

Before you load any RDOS system onto a Model BMX-1 disk, YOU MUST INITIALIZE THE DISK BY RUNNING CSDKINIT. This is a stand-alone program which performs all the functions of DG's DKINIT. Please refer to DG manual on loading an RDOS system for full details on the functionality of disk initialization.

Remember that only CSDKINIT will work correctly for Model BMX-1 disks. If you are building your system from an RDOS release tape, do NOT run file 4 on the DG tape after running CSDKINIT. DG's DKINIT cannot be run on a Model BMX-1 disk. CSDKINIT can, however, be used to initialize any DG supported disk.

STEP 1 - LOADING

A) If loading from a M276 tape:

Perform the steps described for loading M276 tape in Section 4.0.

YOU RESPOND:

6

B) If loading from disk: (CSDKINIT.SV must have been previously loaded onto the disk.

Mount the disk pack which contains CSDKINT.

Set console switches to correct device code.

Press RESET and LOAD switches.

PROGRAM DISPLAYS:

FILENAME?

YOU RESPOND:

CSDKINIT or (DIR:CSDKINT, if the program file is located in directory, DIR, other than the master).

STEP 2 - DISK TYPE

PROGRAM DISPLAYS:

```
    DISK INITIALIZER - REV. NN.NN/with ZETACO Disk
                       Support-REV. 1
    DISK DRIVE MODEL NUMBER?
```

YOU RESPOND:

```
    6XXX
```

NOTE: Enter the X's as shown above.

A) If the disk type is not valid-

PROGRAM DISPLAYS:

```
    ILLEGAL DISK TYPE
```

Step 2 will be repeated until your response is acceptable.

B) if the disk type is valid -

PROGRAM DISPLAYS:

```
    6XXX (ZETACO Emulation) Drive Type
```

STEP 3 - DISK UNIT

PROGRAM DISPLAYS:

```
    DISK UNIT?
```

YOU RESPOND:

```
    DZx, where x indicates drive number:  0, 1, ..., 7
```

A) If the disk unit is not valid -

PROGRAM DISPLAYS:

```
    ILLEGAL DISK UNIT DECLARATION
```

Step 3 will be repeated until your response is acceptable.

B) If the disk unit is valid -

PROGRAM DISPLAYS:

# HEADS	# SEC/TRK	#CYLINDERS	MGB/BLK
99	99	999	Megabytes if disk >4000 blks. Blocks if disk <4000 blks.

STEP 4 - ECC CORRECTION

For most situations it is recommended that you disable ECC correction while running CSDKINIT. This will allow the initializer to flag those bad blocks which are potential problems even though they might be correctable at the time of running CSDKINIT. However, it is also possible to run with ECC correction enabled in cases where there is a need for using marginal media.

STEP 5 - COMMANDS AND SUBSEQUENT OUTPUT

The commands which can be selected are identical to those of DKINIT.

From this point on CSDKINIT will perform exactly as DKINIT.

4.7 CSDSKED - RDOS STAND-ALONE DISK EDITOR

CSDSKED provides the same functions for the BMX-1 disk as DG's DSKED does for standard DG disks.
It can also be used for any DG supported disk. Please refer to the DG stand-alone disk editor manual for a complete description of the commands.

We will describe the steps necessary to run CSDSKED.

STEP 1 - LOADING

A) If loading from a M276 Tape:

Perform the steps described for loading M276 tape in Section 4.0.

YOU RESPOND:

7

B) If loading from disk: (CSDSKED.SV must have been previously loaded onto the disk).

Mount the disk pack which contains CSDSKED.

Set console switches to correct device code.

Press RESET and LOAD switches.

PROGRAM DISPLAYS:

FILENAME?

YOU RESPOND:

CSDSKED or (DIR:CSDSKED, if the program file is located in directory, DIR, other than the master).

STEP 2 - DISK TYPE

PROGRAM DISPLAYS:

DISK EDIT - REV NN.NN WITH ZETACO DISK SUPPORT - REV. 1

DISK DRIVE MODEL NUMBER?

YOU RESPOND:

6XXX

NOTE: Enter the X's as shown above.

A) If the disk type is not valid -

PROGRAM DISPLAYS:

ILLEGAL DISK TYPE

Step 2 will be repeated until your response is acceptable.

B) If the disk type is valid -

PROGRAM DISPLAYS:

6XXX (ZETACO Emulation) Drive Type

STEP 3 - DISK UNIT

PROGRAM DISPLAYS:

DISK UNIT?

YOU RESPOND:

DZx, where x indicates drive number: 0, 1, ..., 7

A) If the disk unit is not valid -

PROGRAM DISPLAYS:

ILLEGAL DISK UNIT DECLARATION

Step 3 will be repeated until your response is acceptable.

B) If the disk unit is valid -

PROGRAM DISPLAYS:

# HEADS	# SEC/TRK	# CYLINDERS	MGB/BLK
99	99	999	Megabytes if disk >4000 blks. Blocks if disk <4000 blks.

STEP 4 - COMMANDS AND SUBSEQUENT OUTPUT

The commands which can be selected are identical to those of DSKED. From this point on CSDSKED will perform exactly as DSKED.

4.8 ECC - ECC ERROR CORRECTIONS COUNTER FUNCTIONS

The Model BMX-1 controller maintains a counter of ECC corrections for each drive connected to the board/s. These are the corrections performed by the firmware and are therefore invisible to the system except through these counters. The counters are automatically cleared by the reset switch on the front panel or if the controller is powered down.

The ECC program is currently available only for RDOS and AOS (RDOSECC.SV for RDOS and AOSECC.PR for AOS). It allows you to monitor the media by displaying or modifying the counters. Some installations may decide to reset the counters to zero on some regular basis: daily, weekly, monthly or whatever.

STEP 1 - EXECUTING THE PROGRAM UNDER CLI

A) RDOS Version

ENTER: RDOSECC

B) AOS Version

ENTER: X AOSECC

STEP 2 - MAIN MENU

CUSTOM SYSTEMS - ECC FUNCTIONS

1 - DISPLAY CONTROLLER ECC CORRECTIONS

2 - RESET CONTROLLER ECC CORRECTIONS

3 - STOP

NOTE - SELECT ONLY THOSE DRIVES WITH ZETACO CONTROLLER BOARDS. RESULTS ARE UNPREDICTABLE ON OTHER BOARDS!

ENTER SELECTION

YOU RESPOND:

1) To display the ECC corrections counter/s

2) To modify the ECC corrections counter/s

3) To terminate the program and return to the CLI

STEP 3 - ENTERING THE UNIT

If you selected 1 or 2,

PROGRAM DISPLAYS:

    ENTER UNIT:

YOU RESPOND:

    DZn   (n = 0, 1, ..., 7)   for RDOS
    DPFN (n = 0, 1, 2, 3, 10, 11, 12, 13) for AOS
    Carriage return or new line to return to Main Menu.

The program will display the (decimal) value of the corrections counter for the drive selected. This step will be repeated until the response to ENTER UNIT is carriage return or new line.

STEP 4 - MODIFYING THE COUNTER

If your response to the Main Menu was 2 - there will be another message after Step 3:

        ENTER NEW VALUE:

You respond with the (decimal) value to which you want the counter set. The number must be between 0 and 65,535. This step will be repeated until you enter a carriage return or new line which will return you to Step 3.

5.0 TROUBLESHOOTING

Self-test checks out all the internal functions of the controller board once for every time power is applied to the board. The test takes approximately 300 ms.

IF SELF-TEST PASSED, THE RED LED WILL GO OUT. If a failure was detected, the led will blink.

Looping on Self-test can be achieved by inserting a jumper at W8-4 which causes the microprocessor to continuously loop on the entire Self-test unless an error occurs. Refer to Section 3.2.2.

TEST	POSSIBLE FAILURE
EEPROM TEST	The data in the EEPROM did not compare with expected data (55 hex). EEPROM may not have been previously burned.
RAM TEST	Data read from RAM did not compare with data written. 2114, PBUS or RAM data bus may be bad.
BMC BUFFER TEST	Data transfer to and from the BMC buffer did not compare with the original data in buffer 0.
2940 ADDRESS GENERATOR TEST	Data read from 2940's did not compare with data written. 2940 may be bad.
ECC TEST	The generated ECC pattern did not compare with the expected pattern. The shift registers, ECC logic, or multiplexers may be bad.

If the Self-test LED does not blink or go out, then the 2925 clock circuitry, the 2910 or the power fail circuit may be bad.

SELF-TEST ERRORS

TABLE 5.1

1452 120 26

CUSTOMER SUPPORT

Our warranty attests the quality of materials and workmanship in our products. If malfunction does occur, our service personnel will assist in any way possible. If the difficulty cannot be eliminated by use of the following service instructions and technical advise is required, please phone ZETACO giving the serial number, board name, model number and problem description. You will be placed in contact with the appropriate technical assistance.

PRODUCT RETURN

Pre-return Checkout:

If controller malfunction is suspected, the use of test software is needed to determine if the controller is the problem and what in particular is wrong with the controller. The tests applicable to this board are listed on the next page of the manual. Please run the test sequence BEFORE considering product return.

Returned Material Authorization (RMA):

Before returning a product the ZETACO for repair, please ask for a RMA number. Each product returned requires a separate RMA number. Use of this number in correspondence and on a tag attached to the product will ensure proper handling and avoid unnecessary delays.

Returned Material Information:

Information concerning the problem description, system configuration, diagnostic program name, revision level and results, i.e., error program counter number should be included with the returning material. A form is provided for this information on the next page of the manual.

Packaging:

To safeguard your materials during shipment, please use packaging that is adequate to protect it from damage. Mark the box "Delicate Instrument" and indicate the RMA number/s on the shipping label.

(include with returning material)

MATERIAL RETURN INFORMATION

All possible effort to test a suspected malfunctioning controller should be made before returning the controller to ZETACO for repair. This will: 1) Determine if in fact the board is defective (many boards returned for repair are not defective, causing the user unnecessary system down-time, paper work, and handling while proper testing would indicate the board is working properly). 2) Increase the speed and accuracy of a product's repair which is often dependent upon a complete understanding of the user checkout test results, problem characteristics, and the user system configuration. Checkout results for the BMX-1 SMD Controller should be obtained by performing the following tests. (Include error program counter numbers and accumulator contents if applicable).

FUNCTION	TEST	RESULT
SMD	Self-test	
	Diagnostics	
	Reliability	

Other test performed:

Please allow our service department to do the best job possible by answering the following questions thoroughly and returning this sheet with the malfunctioning board.

1. Does the problem appear to be intermittent or heat sensitive? (If yes, explain).

2. What operating system are you running under? (AOS RDOS, DDOS, DTOS).

3. Describe the system configuration (i.e. peripherals, I/O controllers, model of computer, etc.)

4. Has the controller been returned before? Same problem?

To be filled out by CUSTOMER:

Model #:
Serial #:
RMA #:

Returned by:

(company name)

6.0 PROGRAM CONTROL

6.1 INSTRUCTION FORMAT

Symbolic form for I/O instructions:

DXXF AC, DSKP

DXX - DOA, DOB, DOC, DIA, DIB, DIC

F = Function:

C (Clear) - Resets Busy and Done flags to zero, aborts all data transfer commands, and clears data transfer status (DIA) fault bits 6, 7, 8, 9, 10, 11, 12, 13, 14 & 15. Also clears RD/WRT and drive attention flags and interrupt request.

S (Start) - Sets busy flag, clears done and initiates one of the following commands selected by a DOA: Read, Write, Format, Read Buffers or Verify. Also clears interrupt request and data transfer status (DIA) fault bits 6, 7, 8, 9, 10, 11, 12, 13, 14 & 15.

P (Pulse) - Sets control full flag and initiates one of the following commands selected by a DOA: Recal, Seek, Stop, Offset, Write Disable, Release, Trespass and Exam Controller RAM.

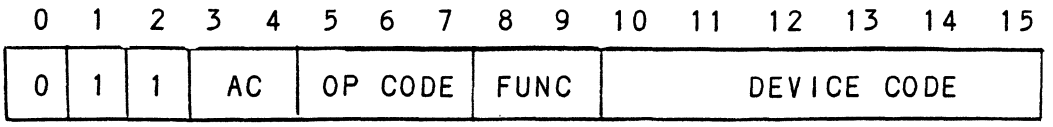
AC = Accumulator: 0, 1, 2 or 3.

DSKP = Device Code: Primary - 27 Octal

Secondary - 67 Octal

(Others available)

BINARY REPRESENTATION OF AN I/O INSTRUCTION



INTERRUPT MASK BIT 7

MSKO AC

Execution of the Mask Instruction with BIT 7 equal to a one in the selected accumulator will set the interrupt mask within the controller board. This will inhibit any further interrupt requests by the Controller until the interrupt mask is cleared, either by an IORST instruction or execution of the mask instruction with accumulator BIT 7 equal to a zero.

IORESET INSTRUCTION

IORST

Execution of an IORST instruction serves as a master reset to the controller board. Upon completion of an IORST the Controller will attempt to select unit zero and default the command register to a read operation. The Controller ECC correction LOG is not cleared out by this instruction.

IOSKIP INSTRUCTION

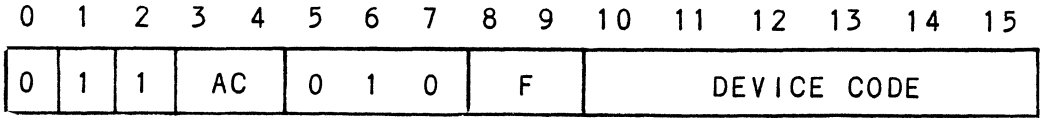
Used to poll the state of the controller board (command is done or busy). If the skip condition is met the next instruction is skipped, else the next instruction is executed.

SKPBZ DSKP - SKIP IF BUSY FLIP-FLOP IS CLEAR.
SKPBN DSKP - SKIP IF BUSY FLIP-FLOP IS SET.
SKPDZ DSKP - SKIP IF DONE FLIP-FLOP IS CLEAR.
SKPDN DSKP - SKIP IF DONE FLIP-FLOP IS SET.

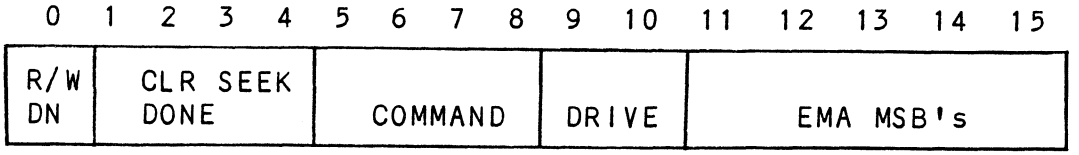
6.2 ACCUMULATOR FORMATS

6.2.1 DOA - SPECIFY COMMAND AND DRIVE

DOAF AC, DSKP



Accumulator



BIT POSITION

0 - Clear Read/Write Done if it is a one

1 - Clear Seek Done Attention Flag for Drive Unit 0 if it is a ONE

2 - Clear Seek Done Attention Flag for Drive Unit 1 if it is a ONE

3 - Clear Seek Done Attention Flag for Drive Unit 2 if it is a ONE

4 - Clear Seek Done Attention Flag for Drive Unit 3 if it is a ONE

5 - 8 Specify Command

		FUNCTION REQUIRED TO INITIATE
0000	READ	START
0001	RECALIBRATE	PULSE
0010	SEEK	PULSE
0011	STOP DISC	PULSE
0100	OFFSET FORWARD	PULSE
0101	OFFSET REVERSE	PULSE
0110	WRITE DISABLE	PULSE
0111	RELEASE DRIVE	PULSE
1000	TRESPASS	PULSE
1001	SET ALT MODE 1	NONE
1010	SET ALT MODE 2	NONE
1011	EXAMINE RAM	PULSE
1100	DATA VERIFY	START
1101	READ BUFFERS	START
1110	WRITE	START
1111	FORMAT	START

NOTE: See Section 6.3 for detailed command description

9 - 10 Drive Selection

00 - Drive Unit 0
01 - Drive Unit 1
10 - Drive Unit 2
11 - Drive Unit 3

DOA will reserve a previously unreserved drive

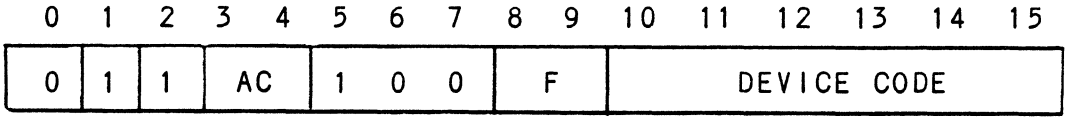
BIT Position 9 is not used if 616X

11-15 Extended Memory Address

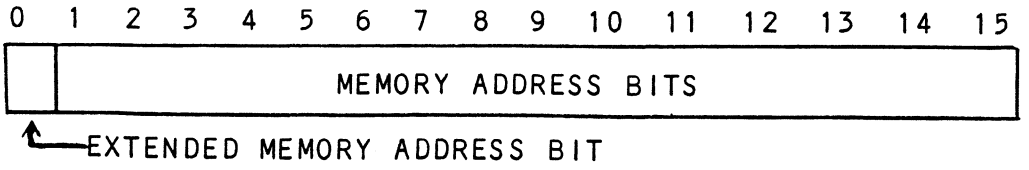
Specifies the MSB's of the Extended Memory Address

6.2.2 DOB - LOAD STARTING MEMORY ADDRESS

DOBF AC, DSKP



Accumulator

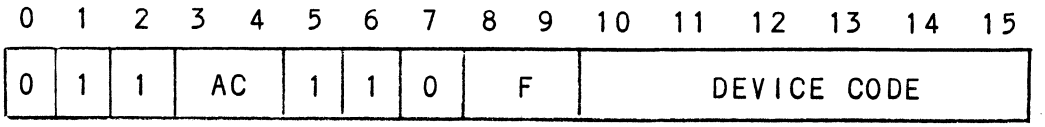


Execution of this instruction will load the controllers address counter with the contents of the specified accumulator and will be used as the starting memory address for a command that requires a DCH transfer or a BMC transfer operation.

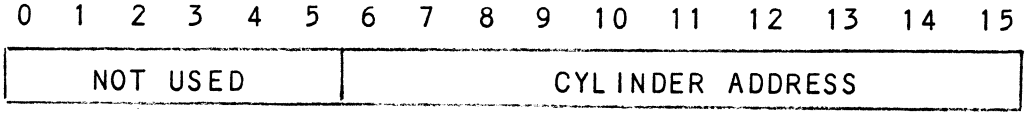
6.2.3 DOC - LOAD DRIVE ADDRESS

6.2.3.1 DOC - SPECIFY CYLINDER

DOCF AC, DSKP

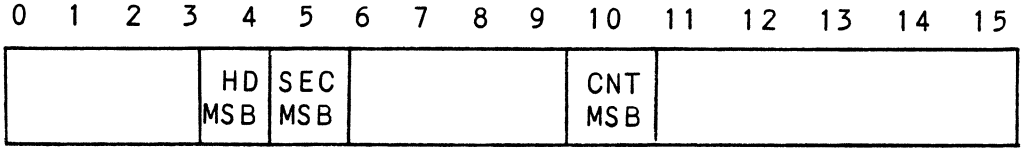


Accumulator (if previous DOA specified a Seek)

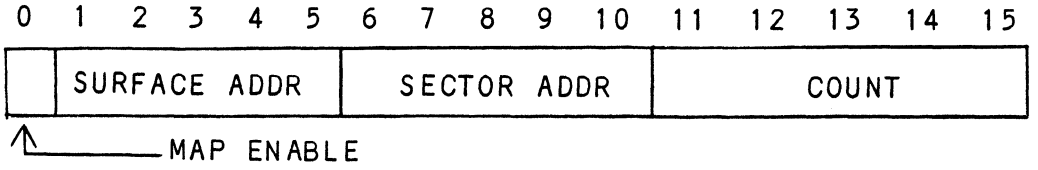


6.2.3.2 DOC - FIRST DOC SPECIFIES EXTENDED SURFACE, SECTOR AND COUNT (Double DOC mode only)

Accumulator (if previous DOA specified a Read, Write, Format or Data Verify)



6.2.3.3 DOC - SECOND DOC SPECIFIES LOWER FIVE BITS OF SURFACE, SECTOR AND COUNT (First and only DOC if single DOC mode)

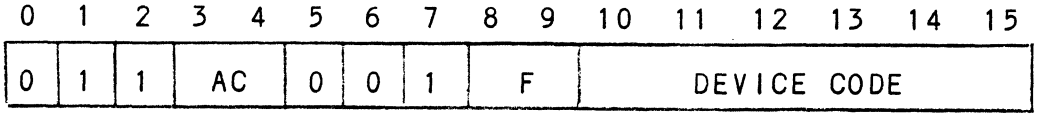


0 - Enable BMC Address Mapping
1 - 5 Starting Surface Address
6 - 10 Starting Sector Address
11-15 Two's complement of number of sectors to be transferred

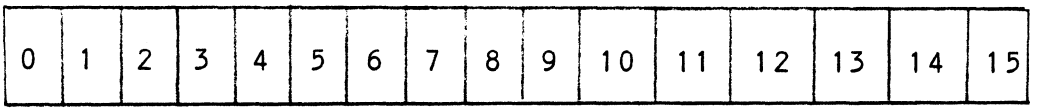
6.2.4 READ STATUS - NON ALTERNATE MODE

6.2.4.1 DIA - READ DATA TRANSFER STATUS

DIAF, AC, DSKP



Accumulator



0 - Control Full
1 - R/W Done
2 - Unit 0 Atten Done
3 - Unit 1 Atten Done
*4 - Unit 2 Atten Done
*5 - Unit 3 Atten Done
6 - BMC Bus Parity Error
7 - Illegal Sector Adr
8 - ECC Error
9 - Bad Sector Flag
10 - Cyl Addr Error
11 - Surf/Sect Addr Error
12 - Verify Error
13 - R/W Timeout
14 - Data Late
15 - Read/Write Fault

*BIT Positions 4 and 5 are not defined if 616X Emulation

0	CONTROL FULL	Will be a one when the Controller receives a pulse function. Will be a zero once the Controller completes the function to the drive that was specified by the command (Recal, Seek, Stop Disk, Offset, WRT DIS, Release, Trespass and Exam Ram).
1	R/W DONE	A one indicates that the done flag was set following a data transfer command.
2-5	UNIT ATTEN DONE (UNITS 0-3)	A one indicates that the respective drive completed a successful seek or recalibrate operation. If the drive was unsuccessful in its attempt to seek, a positioner fault status will be indicated. A recalibrate operation will clear the fault.
6	BUS PARITY ERROR	An Address or Data Parity Error occured on a Data Transfer between the Controller and the BMC Channel.
7	ILLEGAL SECTOR ADDR	The starting sector address (DOC) exceeded the capacity of the drive if set to a one. Done sets immediately.
8	ECC ERROR	A sector of data read from the disk did not correlate with the appended polynomial. This means that the data read does not agree with the data that was originally written.
9	BAD SECTOR FLAG	The Controller detected the bad sector flag set to a one within the sectors address header. (Done will set immediately). This implies that the format program originally determined that the surface within this sector could not support errorless data.

10	CYLINDER ADDRESS ERROR	The Cylinder Address contained within the Sectors Header did not match the requested cylinder given by the previous seek command. Bit 11 will set, instead, if there is no match due to a media flaw. The Read/Write Operation will be terminated immediately.
11	SURFACE/ SECTOR ADDRESS ERROR	This status bit may be set by one of the following cases: 1) The Surface or the Sector Address contained within the Sectors Header did not match the current contents of the controller's Surface/Sector Register (initiated by a DOC). 2) The CRC polynomial did not correlate with the Header Address. 3) The Data Sync on a Read Command could not be detected. The Read/Write operation will be terminated immediately.
12	VERIFY ERROR	Data in memory did not agree with the data on the disk. (See Verify Command).
13	READ/WRITE TIMEOUT	A Read or Write type of operation did not complete within one second.
14	DATA LATE	Not implemented.
15	READ/WRITE FAULT FLAG	A one indicates that at least one bit is set in bit positions 6 through 14 or a drive fault occurred during a Read/Write transfer operation.

Refer to Table 6.1 for detailed description.

	STATUS BIT POSITION	CONTROLLLER ACTION	ERROR RECOVERY
BUS ERROR	6	Sets done immediately if Address error. Sets done at the end of sector xfer if data error	New command. re-try Read/Write Transfer. Insure BMC Bus Terminators are installed. If a second BMC device is connected, make sure it has a different Bus Priority.
ILLEGAL SECTOR ADDRESS	7	Sets done immediately	New command if error re-occurs. Make sure the controller is configured to match the drive type.
ECC ERROR	8	Sets done at the end of sector transfer	New command. Re-tries with servo offset may correct the data. If this error is detected on a surface analysis, the bad sector flag should be set.
BAD SECTOR FLAG	9	Sets done immediately	New command. This sector should be ignored.
CYLINDER ADDRESS ERROR	10	Sets done immediately	New command. The system should diagnose this as a positioner fault.
SURF/ SECTOR ADDRESS ERROR	11	Sets done immediately	New command. Bad sector flag should be set if surface analysis.
VERIFY ERROR	12	Sets done at the end of the sector transfer	New command. Check ECC error also to determine if the error occurred due to a flaw in the media.
READ/ WRITE TIMEOUT	13	Sets done immediately	New command.

READ/WRITE FAULTS (DIA)

TABLE 6.1

6.2.4.2 DIB - READ DRIVE STATUS

DIB AC, DSKP

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	1	1	AC		0	1	1	F		DEVICE CODE					

Accumulator

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15

*0 - Invalid Status
*1 - Drive Reserved
*2 - Trespassed
 3 - Ready
 4 - Busy
*5 - Positioner Offset
 6 - Write Disabled
*7 - ID
*8 - Ill Sur/Cyl Addr
*9 - Illegal Command
*10 - DC Voltage Fault
*11 - Pack Unsafe
 12 - Positioner Fault
*13 - Servo Clock Fault
*14 - Write Fault
 15 - Drive Fault

*These Bits are undefined if 616X

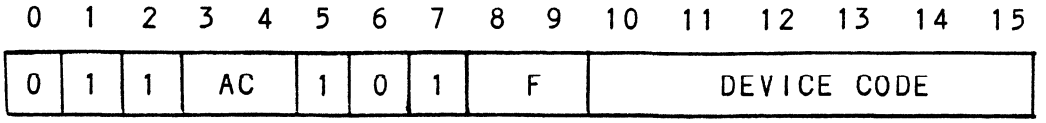
0	INVALID STATUS	A one indicates that Status Bits 1 through 15 should be ignored because the drive is not selected or it is in the process of being selected.
1	DRIVE RESERVED	In a dual port configuration the selected drive is currently in use by another processor.

2	TRESPASSED	Not implemented.
3	READY	Drive unit specified by a previous DOA command is selected, spindle is up to speed and positioner is on cylinder.
4	BUSY	The positioner within the currently selected drive is not on cylinder.
5	POSITIONER OFFSET	The selected Read/Write head was moved from on cylinder dead center as was specified by an offset forward or reverse command.
6	WRITE DISABLED	Status from the drive indicates that a write type of command cannot be executed.
7	ID	This Bit is a one if 6122 is selected, a zero for all other emulations.
8	ILLEGAL SURFACE OR CYLINDER ADDRESS	The requested surface or cylinder address exceeds the capacity of the drive. Read/Write operation will terminate immediately.
9	ILLEGAL COMMAND	The controller was requested to perform a write type of command while servo is offset or write disabled is active.
10	DC VOLTAGE FAULT	Not implemented.
11	PACK UNSAFE	Conditions exists within the drive which may impair the safety of the media. This bit will be a one if a fault status is received directly from the drive interface.

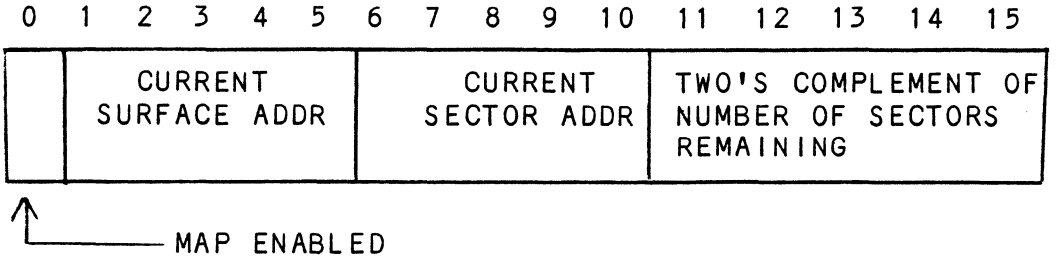
12	POSITIONER FAULT	This indicates that the drive was unable to complete a seed within 500 ms, or that the positioner has moved to a position outside the recording field. The system should send a recal command to recover from this error.
13	SERVO CLOCK FAULT	A clock synchronization failure occurred between the serial data being read and the reference clock coming from the disk drive. In most cases this means that the header or data sync was not encountered within a specified amount of time. This flag would set if the format on the disk did not agree with what the Controller expected. Check the configuration to make sure the proper format was selected.
14	WRITE FAULT	An abnormal condition was detected by the drive during a write type of operation.
15	DRIVE FAULT	One or more bits are set in positions 8 through 14 or the drive detected an abnormal condition.

6.2.4.3 DIC - READ SURFACE, SECTOR AND COUNT

DICF AC, DSKP



Accumulator



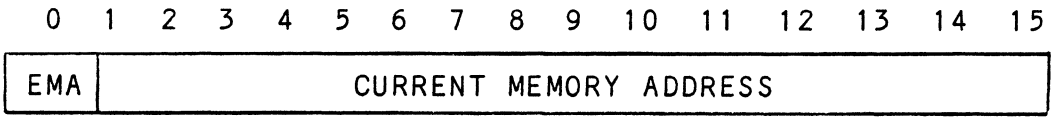
6.2.5 READ STATUS - ALTERNATE MODE ONE

See detailed description of Alternate Mode One Command. Previous DOA specified ALT Mode One for Sections 6.2.5.1 through 6.2.5.3.

6.2.5.1 DIA - READ CURRENT MEMORY ADDRESS

DIAF AC, DSKP

Accumulator



After the execution of this instruction the value of the accumulator will contain the memory address to where the next data word transfer will take place. The memory address counter is incremented by one after each DCH or BMC transfer.

6.2.5.2 DIB - READ EXTENDED MEMORY ADDRESS

DIBF AC, DSKP

Accumulator

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15

0 - BMC Mode
1 - Fixed Disk
2 - Drive 0 ID
3 - Drive 1 ID
4 - Surface Address (MSB)
5 - Sector Address (MSB)
6 - Drive 0 ID
7 - Drive 1 ID
8 - Not Used
9 - Not Used
10 - Sector Count (MSB)
11-15 - Extended Memory Address

The AC will contain the current most Significant Bits for the Surface (BIT 4), Sector Address (BIT 5) and Two's Complement Count (BIT 10). These Bits will allow the System to reference up to 64 heads or sectors.

6.2.5.3 DIC - NOT CURRENTLY IMPLEMENTED

6.2.6 READ STATUS - ALTERNATE MODE TWO

See detailed description of Alternate Mode Two Command. Previous DOA specified ALT Mode Two for Sections 6.2.6.1 through 6.2.6.3.

6.2.6.1 DIA - READ ECC REMAINDER UPPER

DIAF AC, DSKP

Accumulator

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X

6.2.6.2 DIB - READ ECC REMAINDER LOWER

DIBF AC, DSKP

Accumulator

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X

6.2.6.3 DIC - NOT CURRENTLY IMPLEMENTED

6.3 DETAILED COMMAND DESCRIPTIONS

The command set (16 in all) provided by the controller is basically broken up into three groups:

1. Data Transfer Command
2. Drive Commands
3. Alternate Mode Commands

The Command is stored in the controller via a DOA instruction. Before any Command is initiated, the selected unit must have valid status and be ready.

6.3.1 DATA TRANSFER COMMANDS

Start (Set Busy) will initiate any one of the following commands: Read, Write, Format, Verify or Read Buffers up to 64 contiguous sectors may be transferred.

Read/Write Initialization Steps:

1. Control full and Drive status must be tested for proper state before commencing with a Read/Write Command.

2. Send the Starting Surface and Sector Address along with the two's complement of the number of sectors transferred. (See DOC)

3. Send the Starting Memory Address of where the data should be stored or retrieved. (See DOB)

4. Send the Command type and the desired Drive Unit Number. (See DOA)

5. Issue a Start Pulse.

Read/Write Termination Possibilities (Done Set):

1. All the sectors implied by the two's complement sector count were transferred.

2. A Drive or Read/Write Error was encountered. DIC command should be issued to determine which sector the error occurred at.

3. Busy was cleared by an IORESET instruction or a clear pulse was issued to the controller during the Read/Write transfer. Done will not set in this case.

6.3.1.1 READ COMMAND

When busy sets, the Controller will wait for on cylinder if the previous seek command has not been completed yet. It will then search for the starting sector address specified by the previous DOC instruction. The header is read and compared with the starting sector address, starting surface address and stored cylinder address to insure that the proper sector has been physically located. Before the data can be accepted the header must match the specified address, the header CRC must be good and no bad sector flags encountered. If the header is in error or the bad sector flag is a one, the appropriate status bit and done flag is set immediately. When the drives RD/WRT head reaches the data field the serial data is sent to the SMD interface formed into parallel words by the Controller and transferred to the buffer. When all 256 words are contained within the buffer, the ECC Code appended in the data is checked to insure proper data by reading the results of the remainder. A data error occurred if the remainder is not equal to zero. In the case of an error the Controller will transfer the data into memory and then set ECC Error Flag and Done. If the ECC Enable feature is selected (refer to Configurating section), the Controller will attempt to correct the data within its own buffer prior to transferring it to memory.

If it determines that it is not correctable, the Controller will re-try on its own with a Data Strobe Early and if unsuccessful, again with a Data Strobe Late. If the data is still not correctable, then it will set ECC Error Flag and Done. If more sectors are to be transferred, the Controller will begin searching for the next sector while the data from the previous sector is transferred to memory.

6.3.1.2 WRITE COMMAND

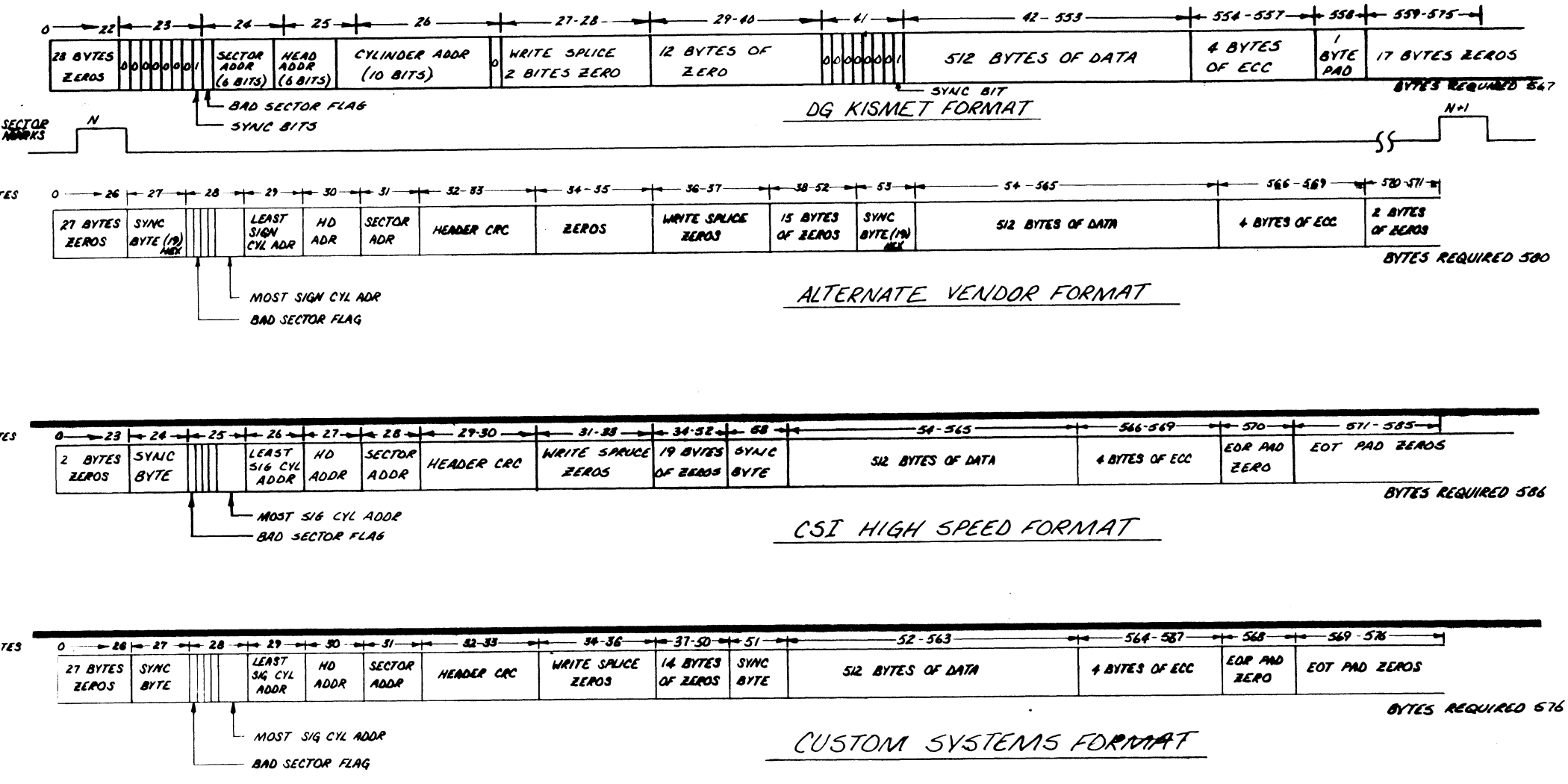
When busy sets, the Controller will wait for the positioner to be on cylinder if the selected drive is still in the process of seeking. Upon the completion of the previous seek operation, the Controller will transfer 256 words of data from memory to a sector buffer. The starting address of memory was specified by the previous DOB instruction. The Controller searches for the desired sector and performs a head verification (same as the read command) before data is written on to the surface of the disk. Once the correct sector is found, the Controller will select the sector buffer previously written by the DCH control. The contents of this buffer is then written on to the disk surface proceeded by a gap and data sync. The controller incorporates two sector buffers. Therefore, the BMC or DCH logic can write into one buffer while data is transferred to the disk from the other.

6.3.1.3 VERIFY

When busy sets, the Controller initially starts out as if it were a read command (i.e. wait for on cylinder, verify header etc). Once a full sector is transferred from the disk to a controller buffer a comparison is made against system memory. This is accomplished by reading a word from memory starting from the previous DOB and comparing each word of sector. If a word does not compare, data transfer status (DIA) Bit 12 and Done will set.

6.3.1.4 FORMAT

The objective of the format command is to write the header information (surface, sector and cylinder address) on a sector. Up to 64 contiguous sectors may be formatted per command. Data that was contained within the sector will be lost (replaced by all zeros). Refer to Figure 6.2 for format details. Format is also used to set the bad sector flag.



HEADER FORMATS

FIGURE 6.2

6.3.1.5 READ BUFFERS

Reads the contents of the currently used buffer and transfers all 256 words to memory specified by the starting address. Primarily used for diagnostic purposes.

6.3.2 DRIVE COMMANDS

IOPULSE (sets control full) initiates any one of the following commands: Recalibrate, Seek, Stop, Offset, Write Disable, Release, Examine Ram and Trespass.

6.3.2.1 RECALIBRATE

Moves the heads to cylinder 0, selects Head 0, and issues a fault clear to the drive.

An IORESET switch will automatically cause a recalibrate command to be issued to Unit 0.

This command moves the heads more slowly than a seek to 0, so it should not be used for data acquisition.

6.3.2.2 SEEK

Moves the heads to the cylinder specified by the DOC. The Controller stores the cylinder address for that particular unit, initiates the seek operation and clears control full. While that unit is busy seeking the Controller can accept another seek command for a different unit (overlapped seeks) or commence with a Read/Write Command for the unit busy seeking.

See the SMD specification for the Seek Timing.

6.3.2.3 OFFSET FORWARD

Offsets the heads forward off the track center-line. This operation is cleared by the next command. (The drive does not allow write operations when the positioner is offset).

6.3.2.4 OFFSET REVERSE

Offsets the heads reverse off the track center-line. This operation is cleared by the next command. (The drive does not allow write operations when the positioner is offset.) Offset forward or reverse may be used as an attempt to recover data that cannot be corrected by the error correction algorithm.

6.3.2.5 WRITE DISABLE

Not implemented.

6.3.2.6 RELEASE DRIVE

Clears the reserved condition of the specified drive which this processor had previously reserved.

6.3.2.7 TRESPASS

The Controller issues a priority select to the specified drive. The drive will immediately be reserved until a release command is issued or the drive timeout feature times out.

6.3.2.8 STOP DISK

All drives connected that are selected for remote operation will unload the heads and spin down via the pick-hold line. A console reset, IORESET instruction, or another command will spin the disk back up.

6.3.2.9 EXAMINE RAM COMMAND

This command gives the system the capability of reading from or writing to the BMX-1 controllers memory. This command must be proceeded by a DOC containing the address of the desired RAM location. See Tables 6.2/6.3 for memory map.

In order to write to RAM, Bit 0 (MSB) must be a one in the DOC address, and the data to be written is sent via the DOB. If a read RAM is implied (DOC Bit 0 = 0), the contents of the DIC will contain the RAM data after control full clears.

This feature is used for obtaining the following information:

a. Drive characteristics for the formatter and Reliability programs.

b. Number of ECC corrections by the Controller (each unit has a separate count).

c. Maintenance testing.

d. Configuring the EEPROM.

e. Features that may be considered in the future.

ADDRESS (HEX)	NAME
000 - 0FF	SECTOR BUFFER 0
100 - 1FF	SECTOR BUFFER 1
200 - 2FF	SECTOR BUFFER 2 (NOT USED)
306	CYL 0
307	CYL 1
308	CYL 2
309	CYL 3
30A	CURRENT SURFACE,SECTOR,SECTOR COUNT
30B	EXT. DOC
30C	ZADJ. SURFACE ADDR
30D	SURF - SECT
310	BAD SECTOR FLAG
311	UNIT SELECT
312	SOFT ECC DISABLE (NOT USED)
320	UNIT 0 PORT SEEK END MAP
321	UNIT 1 PORT SEEK END MAP
322	UNIT 2 PORT SEEK END MAP
323	UNIT 3 PORT SEEK END MAP
330	ZADJ. MAX SECTOR
331	ZADJ. MAX SURFACE
332	ZADJ. MAX CYLINDER
333	SYNC BYTE
334	VOLUME ADDR (CMD)
335	BANK SEL,BMC PRIORITY
340	UNIT 0 CORRECTION COUNT
341	UNIT 1 CORRECTION COUNT
342	UNIT 2 CORRECTION COUNT
343	UNIT 3 CORRECTION COUNT
348	SECTOR VERIFICATION ENABLE
349	SECTOR COUNT
34A	LENGTH OF LAST SECTOR (COUNT * 600 NANOSEC.)
3FF	PROM ID/REVISION LEVEL

BMX-1 MICROPROCESSOR MEMORY MAP

TABLE 6.2

4800	START OF PORT 0
4880	START OF PORT 1
4900	START OF PORT 2
4980	START OF PORT 3
XX00	RCHAR SWITCHES
XX01	RPARA SWITCHES
XX02	DEVICE SELECT CODE
XX03	INTERLEAVE FACTOR
XX04	THROTTLE BURST RATE
XX05	BREAK COUNT
XX06	# OF BURSTS
XX20	MAX SECTOR
XX21	MAX CYL-UPPER
XX22	MAX CYL-LOWER
XX23	MAX HEAD
XX24	MAX HEAD-ODD UNIT
XX25	HEAD MASK
XX26	BANK,PRIORITY
XX27	SYNC BYTE
XX30 - XX7F	INTERLEAVE MAP

EEPROM MAP

TABLE 6.3

1460-1462	SELECTED DRIVE CHARACTERISTICS	These locations will be updated whenever a new drive is selected. 1460 - Maximum sector address 1461 - Maximum surface address 1462 - Maximum cylinder address Allow invalid status to go away before a reference is made. Avoid writing to these locations.
1500-1503	UNIT CORRECTION COUNTS	These locations will be incremented each time the controller does a correction either by the ECC algorithm or an Early/Late re-try. The maximum count per unit is 65535 (the count will stay at maximum if there are any more corrections to that unit). The counts are initialized to zero on either a power on or an IORESET switch. A separate count is maintained for each unit. 1500 - Unit 0 1501 - Unit 1 1502 - Unit 2 1503 - Unit 3

EXAMINE RAM COMMAND

1777-8 PROM ID/REV

DIC ACCUMULATOR

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	RES	IDENTIFICATION						REVISION LEVEL							

EXAMPLE: Identification 80 (Hex) Revision Level 6

Location 1777-8 = 100006

NOTE: Avoid referencing any locations that are not defined here.

EXAM RAM EXAMPLE

READ Contents of Loc 1500 Octal (Unit 0 corrections)

Accumulator Set up:

```
A0 = 002600 (NOP Command Unit 0)
A1 = 001500 (RAM Address for DOC)

DOC 1, DSKP              ; Send RAM Address
DOAP 0, DSKP             ; Send NOP Command and IOPULSE
DIA 0, DSKP              ; Wait for Control Full
MOVZL# 0,0,SZC           ; To be zero
JMP .-2

DIC 2, DSKP              ; Put contents of RAM Location
                           1500 into Accumulator 2
```

WRITE To Location 1500 Octal (Clear Unit 0 Corrections)

Accumulator set up:

```
A0 = 002600 (NOP Command Unit 0)
A1 = 101500 (RAM Address for DOC)
A2 = 000000 (RAM Data)

DOC 1, DSKP              ; Send RAM Address
DOB 2, DSKP              ; Send RAM Data
DOAP 0, DSKP             ; Send NOP Command and IOPULSE
```

6.3.3 ALTERNATE MODES

A command that will change the context of the data received from a DIA, DIB or DIC. A command other than Alternate Mode or an IORESET will clear Alternate Mode.

6.3.3.1 ALTERNATE MODE ONE

It changes the context of DIA to read the current memory address. The ending address after a Read/Write transfer will point to the last address plus one.

6.3.3.2 ALTERNATE MODE TWO

It changes the context of the DIA and DIB command. This is used to extract the syndrome (ECC remainder not equal to zero after a read command) from the Controller in order to determine whether the data error within the sector read is correctable or not.

6.4 ERROR CORRECTION CODE (ECC)

When a write command is specified the ECC hardware divides the data field within the sector by a fixed *generator polynomial and appends the resulting checkword to the data field.

*Generator Polynomial

$$X-32 + X-23 + X-21 + X-11 + X-2 + 1$$

When a read command is specified the ECC hardware divides the data field and the appended checkword within the sector by a *factored version of the same generator polynomial. If a data error occurs, the resulting remainder is non-zero, and the data transfer status (DIA) bit position 8 is set (BIT 8 will not set if the controller was enabled to correct and the error is correctable). Be aware that there exists a small class of errors which are undetectable due to the cyclic properties of the generator polynomial.

*Factored Version

$$(X-1 + X-2 + 1)\ (X-21 + 1)$$

The ECC feature detects all error bursts contained within 21 or less contiguous bits in a sector and allows correction of all error bursts up to 11 contiguous bits.

6.5 FORMAT SEQUENCER

The BMX-1 disk controller features a format sequencer which controls the disk side of the Controller. The firmware which controls this sequencer is contained in PROMS allowing disk format changes to take place in the PROMS instead of the microprocessor firmware.

The format sequencer firmware is arranged in eight banks of 64 words each and is selectable for the format bank desired. Each bank consists of READ/WRITE/ FORMAT CODE. The last bank is reserved for Self-test.

6.5.1 READ/WRITE FORMATS

The BMX-1 will support up to 7 different types of media formats (see Figure 6.2).

APPENDIX A

DIAGNOSTIC SUPPORT PACKAGE
GENERAL INFORMATION

BOOTSTRAP PROCEDURES
LOADING DSP FROM TAPE

1) Load desired DSP tape and put Drive On-line.

2) Perform the following steps when the system has the program load option. (If system does not have program load option consult processor manual.)

   A) Put 100022 or 100062 on console data switches 0 - 15.

   B) Program load.

      a) Press program load switch if front panel has switches.
      b) DG virtual console, enter 100022L or 100062L (if 100062 first enter 100062 in 11A).

      c) Point 4 virtual console, set switches or CPU board, enter P22 or P62.

3) Enter tape file number, followed by a carriage return of desired test.

4) If program is not self starting perform the following steps:

   A) Front Panel Switches.

      a) Put starting address on console data switches (0-15).
      b) Press examine memory.
      c) Put switch settings on console data switches (0-15).
      d) Press continue.

   B) Virtual Console.

      DG

      a) Enter switch settings in 11A through keyboard.
      b) Enter starting address (XXXXR) through keyboard.
      c) To change switch settings, enter break, change 11A through keyboard, and enter PC address when break occurred. (XXXXR)
      d) To continue on error halt, enter PC address (XXXXR).

      POINT 4

      a) Set switches on CPU board.
      b) Enter starting address (JXXXXX).
      c) To continue on error halt, enter PC address (JXXXXX).

LOADING DSP FROM TAPE TO DISK

1. The last file on the DSP tape (reference menu for number) is a dump format copy of the previous files. This allows a user to load (use RDOS load command) the files onto a disk.

2. The files can now be booted from disk (enter file name in response to filename? or pathname?).

INDEX

```
0001  DISKD    AOS ASSEMBLER REV 04.20        11:55:43 06/22/84
01              ;
02              ;
03              ;
04              ;*************************************************************
05              ;
06              ;
07              ; DESCRIPTION: ZETACO SMD DISK CONTROLLER DIAGNOSTIC
08              ;
09              ;
10              ; PRODUCT OF ZETACO, 1984
11              ;*************************************************************
                        .TITL   DISKD
13      000001          .DUSR   X=1
14      000001          .NOMAC  X
15              ; 1.0 PROGRAM NAME:     DISKD.SR
16
17              ; 2.0 REVISION HISTORY:
18              ;
19              ;       REV.        DATE
20              ;        00      02/17/83        ;
21              ;        01      09/07/83        ;ANOTHER RDY UNIT WARNING,1 HD ERR C22,
22              ;                                ;AOS BOOTSTRAP(400'S), NO OFFSET TESTS
23              ;                                ;FOR CMD'S
24              ;        02      03/28/84        ;295C,296 AND BMX TESTS
25              ;                                ;DEVICE CODE CHANGE ROUTINE
26              ;        03      06/12/84        ;ZDF1 CHANGES, A5 TESTS 17-76
27              ;
28
29              ; 3.0 MACHINE REQUIREMENTS:
30              ;       NOVA OR ECLIPSE FAMILY CENTRAL PROCESSOR
31              ;       MINIMUM OF 16K READ/WRITE MEMORY
32              ;       ZETACO SMD DISK CONTROLLER
33              ;       0-3 DISK DRIVES
34              ;       TELETYPE OR CRT AND CONTROL
35
36              ; 4.0 TEST REQUIREMENTS:         N/A
37
38              ; 5.0 SUMMARY:
39              ;       THIS PROGRAM IS A HARDWARE DIAGNOSTIC FOR THE
40              ;       ZETACO SMD DISK CONTROLLER AND DRIVES.
41              ;       THE DEVICE CODE MAY BE 20-76 OCTAL WITH THE
42              ;       DEFAULT BEING 27
43
44
45              ; 6.0 RESTRICTIONS:
46              ;       THIS PROGRAM HAS NO RESTRICTIONS AS TO SINGLE OR
47              ;       DUAL PROCESSOR HARDWARE CONFIGURATION. HOWEVER, THE
48              ;       DIAGNOSTIC MAY BE RUN ON ONLY ONE CPU AT A TIME AND
49              ;       MUST BE THE ONLY PROGRAM BEING RUN WITHIN THE DISK
50              ;       SYSTEM.
51
52              ; 7.0 PROGRAM DESCRIPTION/THEORY OF OPERATION:
53
54              ;       7.1 "A" TESTS CHECK:
55
56              ;       - BUSY, DONE, I/O BUS SELECT LOGIC
57              ;       - DISK SELECT LOGIC, CONTROLLER RAM
58
59              ;       7.2 "B" TESTS CHECK:
60
```

```
0002  DISKD
01              ;       - START, BUSY, CLEAR LOGIC
02              ;       - RECALIBRATE, ATTN, INTERRUPT LOGIC
03              ;       - INTERRUPT DISABLE, INTA LOGIC
04              ;       - THAT SEEKS TO CYL'S 0,1/2 CYL MAX,AND CYL MAX CAN AT
05              ;         LEAST BE EXECUTED AND SET DRIVE BUSY.
06              ;       - READY/SELECT LOGIC
07
08              ;       7.3 "C" TESTS CHECK:
09
10              ;       - THAT THE CA REGISTER INCREMENTS PROPERLY
11              ;         VIA DCH  OR BMC REQUESTS
12              ;       - THAT A WRITE CAN BE EXECUTED
13              ;       - SELD, CLEAR LOGIC
14              ;       - THAT SEEK/WRITE OPERATIONS CAN BE EXECUTED
15              ;       - WRITES TO DIFFERENT HDS, SECTORS
16              ;       - MULTI-SECTOR WRITES
17              ;       - THE INCREMENT HEAD LOGIC
18              ;       - ILLEGAL SECTOR, SURFACE, CYLINDER CONDITIONS
19
20              ;       7.4  "E" TESTS CHECK:
21
22              ;       - THAT A READ MAY BE EXECUTED
23              ;       - 8 SECTOR WRITE/READ OPERATIONS (9 DIFFERENT
24              ;         DATA PATTERNS) AT CYL'S 0,1/2 CYL MAX AND CYL MAX WITH FULL
25              ;         CORE COMPARE
26              ;       - DATA VERIFY FUNCTION (NORMAL AND WITH FORCED ERRORS)
27              ;       - OFFSET MODES
28              ;       - ILLEGAL COMMAND TRAPS
29              ;       - WRITE CYL# TO HEAD 0,SECTOR 0 OF ALL CYLINDERS
30              ;       - WRITE HEAD # TO SECTOR 0 OF ALL HEADS ON CYL 0
31              ;       - WRITE SECTOR # TO ALL SECTORS OF HEAD 0,CYL 0
32              ;       - EACH OF THE ABOVE OPERATIONS IS FOLLOWED
33              ;         BY A CORRESPONDING READ/CHECK OPERATION TO VERIFY
34              ;         DISK ADDRESSING LOGIC.
35
36              ;       7.5  "F" TESTS CHECK:
37
38              ;         THE FORMAT LOGIC ON CYL 0,HEAD 0,SECTOR 0,
39              ;         A BAD SET FLAG IS SET AND TESTED
40              ;         THE FORMAT IS SET TO NORMAL AFTER COMPLETION OF
41              ;         THESE TESTS.
42              ;         ## SEE SWPAK 7 OPTION ##
43
44              ;       7.6 "S" TESTS ARE SEEK EXERCISERS
45
46              ;       - PERFORMS RANDOM SEEKING.  EACH SEEK IS FOLLOWED BY A
47              ;         READ TO HEAD 0,SECTOR 0
48
49              ;       - PERFORMS RANDOM OVERLAPPED SEEKING TO TWO DRIVES.
50              ;         EACH SEEK IS FOLLOWED BY A READ TO HEAD 0,SECTOR 0.
51              ;         U1 IS THE THE PRIMARY UNIT UNDER TEST AND U2
52              ;         IS THE NEXT DRIVE FOUND IN A 1,2,3,0 ETC.  SEARCH.
53              ;         IF ONLY 1 DRIVE, TEST IS BYPASSED.  TEST IS ONLY RUN
54              ;         AFTER A PASS IS ACHIEVED ON ALL DRIVES.
55
56              ; 8.0  OPERATING MODES/SWITCH SETTINGS:
57
58              ;8.1    SWITCH SETTINGS
59              ;
60              ;       LOCATION "SWREG" IS USED TO SELECT THE PROGRAM OPTIONS
```

```
0003  DISKD
01              ;       THIS LOCATION WILL BE SET ACCORDING TO THE ANSWERS
02              ;       SUPPLIED BY THE OPERATOR.  THE OPTIONS CAN BE CHANGED
03              ;       OR VERIFIED BY USING ONE OF THE COMMANDS GIVEN IN SEC.
04              ;       8.3
05              ;
06              ;
07              ;8.2    SWITCH OPTIONS
08              ;       DIFFERENT BITS AND THEIR INTERPRETATION AT LOCATION
09              ;       "SWREG" IS AS FOLLOWS:
10              ;
11              ;       BIT     OCTAL   BINARY  INTERPRETATION
12              ;               VALUE   VALUE
13              ;
14              ;       1               0       LOOP ON ERROR
15              ;               40000   1       SKIP LOOPING ON ERROR
16              ;
17              ;       2               0       PRINT TO CONSOLE
18              ;               20000   1       ABORT PRINT OUT TO CONSOLE
19              ;
20              ;       3               0       DO NOT PRINT % FAILURE
21              ;               10000   1       PRINT % FAILURE
22              ;
23              ;       5               0       DO NOT PRINT ON THE LINE PRINTER
24              ;               02000   1       PRINT ON THE LINE PRINTER
25              ;
26              ;       6               0       DO NOT HALT ON ERROR
27              ;               01000   1       HALT ON ERROR
28              ;
29              ;       7               0       N/A
30              ;               00400   1       DISABLE FORMATTING HEAD 0, CYLINDER 0, SECTOR 0
31              ;                               ##SEE 12.2##.
32              ;       8               0       N/A
33              ;               00200   1       RECALIBRATE DURING SCOPE LOOP
34              ;
35              ;       9               0       N/A
36              ;               00100   1       1 SECOND DELAY DURING SCOPE LOOP
37              ;
38              ;       10(A)           0       N/A
39              ;               00040   1       PROGRAM WILL PRINT TEST #'S AND FIRMWARE REVISION
40              ;
41              ;       11(B)           0       N/A
42              ;               00020   1       PROGRAM WILL EXIT TO ODT WHEN
43              ;                               NOT IN TESTS F1- ##SEE 7.5##
44              ;                               SWITCH IS SET TO 0 UPON EXIT
45              ;
46              ;       12(C)           0       SKIP LONG RAM TEST
47              ;               00010   1       LONG CONTROLLER RAM TEST
48              ;
49              ;8.3    SWITCH COMMANDS
50              ;       ONCE THE PROGRAM STARTS EXECUTING THE STATE OF ANY OF
51              ;       THE BITS CAN BE CHANGED BY HITTING KEYS 1-9, A-F.  THE
52              ;       PROGRAM WILL CONTINUE RUNNING AFTER UPDATING THE OPTIONS.
53              ;       EACH KEY WILL COMPLEMENT THE STATE OF THE BIT AFFILIAT-
54              ;       ED WITH IT, THUS BIT 4 CAN BE ALTERED BY HITTING KEY 4.
55              ;       SETTING OF ANY BIT OF LOCATION "SWREG" WILL SET BIT 0.
56              ;       (DEFAULT MODE IS DEFINED AS ALL BITS OF SWREG SET TO 0)
57              ;
58              ;8.4    OTHER COMMANDS  (^ = CONTROL KEY)
59              ;
60              ;       "CR"    A "RETURN" CAN BE TYPED TO CONTINUE THE PROGRAM
```

```
;               AFTER ITS LOCKED IN A SWITCH MODIFICATION MODE
;
;       ^D      THIS COMMAND GIVEN AT ANY TIME WILL RESET "SWREG"
;               TO DEFAULT MODE AND RESTART THE PROGRAM.
;
;       ^R      THIS COMMAND GIVEN AT ANY TIME WILL RESTART THE
;               PROGRAM. SWITCHES ARE LEFT WITH THE VALUES THEY
;               HAD BEFORE THE COMMAND WAS ISSUED.
;
;       ^O      THIS COMMAND GIVEN AT ANY TIME WILL CAUSE THE
;               PROGRAM CONTROL TO GO TO ODT (NOTE: THIS IS AN
;               OPTIONAL COMMAND AND IS AVAILBLE ONLY IF
;               ODTPK IS PRESENT)
;
;       M       THIS COMMAND GIVEN AT ANY TIME WILL PRINT THE
;               CURRENT OPERATING MODES.
;
;       0       THIS COMMAND GIVEN AT ANY TIME WILL LOCK THE
;               PROGRAM INTO SWITCH MODIFICATION MODE WHERE
;               MORE THAN 1 BIT CAN BE CHANGED.
;


; 9.0 OPERATING PROCEEDURE/OPERATOR INPUT:

;       9.1 LOAD USING THE BINARY LOADER

;       9.2 STARTING ADDRESSES
;               200-TO IDENTIFY DISK TYPE (INITIALIZE)
;                  PROGRAM THEN PROCEEDS TO 500.
;               201-ODT DIRECT ENTRY ONLY
;               202-RANDOM SEEK EXERCISERS. (1 PASS OF DIAG FOR EACH UNIT FIRST)
;                SEEK EXER 1 IS A SINGLE DRIVE EXERCISER
;                SEEK EXER 2 IS TWO DRIVE EXERCISER WITH SEEK OVERLAP
;               500-DIAGNOSTIC (RESTART)

;       9.3 THE PROGRAM PRINTS "PASS" FOLLOWING EACH
;          COMPLETE PASS THROUGH THE TESTS. RANDOM
;          SEEK EXERCISER PERFORMS 1000 SEEKS
;          PER "PASS" MESSAGE.

;       9.4 DEVICE CODE OF CONTROLLER IS REQUESTED (27 IS DEFAULT)

;       9.5 UNIT NUMBERS TO BE TESTED ARE REQUESTED TO WHICH THE OPERATOR
;          ENTERS THE UNIT NUMBERS TO BE TESTED, SEPARATING
;          THE INDIVIDUAL #'S BY A (,) OR (SPACE).

;       9.6 OPERATOR IS REQUESTED TO ENTER 1, IF UNIT CHARACTERISTICS
;          DISPLAYED ARE INCORRECT, AND WANTS TO LOOP ON READING THEM

; 10. PROGRAM OUTPUT/ERROR DESCRIPTION:
;       WHEN AN ERROR IS DETECTED THE PROGRAM PRINTS THE ERROR
;       PC, AC'S 0,1,AND 2 AT THE POINT OF ERROR. THE PROGRAM THEN
;       GOES INTO A SCOPE LOOP BETWEEN THE ENTRIES TO
;       .SETUP AND .LOOP ALLOWING THE OPERATOR TO SET  SWPAK.
;       IN GENERAL THE ERROR PC WILL POINT TO A CALL ERROR.

;       THE PRINTOUT WILL BE OF ONE OF THE FOLLOWING FORMATS:

;       A. STANDALONE CONTROLLER TEST FAILURES-
```

```
01
02              ;       B. STATUS ERRORS
03
04              ;       MODE    UNIT    #       DATA
05              ;       CYL     #       HEAD    #       SECTOR  #
06              ;       AC1(STATUS) SHOULD =AC0
07              ;       DESCRIPTIONS OF FAILING STATUS BITS
08
09              ;       C. MEMORY/DISK ADDRESS ERROR
10
11              ;       MODE    UNIT    #       DATA
12              ;       CYL     #       HEAD    #       SECTOR  #
13              ;       ENDING MEMORY/DISK ADDRESS ERROR
14              ;       AC1(MA/DA) SHOULD =AC0
15
16              ;       C. INTERRUPT TIMEOUT
17
18              ;       MODE    UNIT    #       DATA
19              ;       CYL     #       HEAD    #       SECTOR  #
20              ;       INTERRUPT TIMEOUT
21
22              ;       ADDITIONAL TEST SIGNIFICANCE CAN BE FOUND IN THE PROGRAM
23              ;       LISTING, ALTHOUGH IT IS HOPED THAT A NEED FOR THE
24              ;       LISTING WILL BE MINIMAL.  SWPACK(SWREG) WILL PROVIDE
25              ;       ALL CONTROL OVER TEST LOOP OPTIONS AND PRINTOUTS.
26
27
28              ;       DATA ERRORS WILL RESULT IN THE 1ST 3 GOOD/BAD
29              ;       PAIRS AND THEIR ADDRESSES BEING PRINTED ALONG WITH THE
30              ;       TOTAL COUNT.  IF AN ECC ERROR IS DETECTED, THE CALL
31              ;       EHECC WILL ACKNOWLEDGE THE FACT AND RETURN TO THE
32              ;       MAIN TEST FOR THE DATA COMPARE.  PRINTOUTS RESULT
33              ;       ON THE 1ST ERROR PASS ONLY.  AS THE CHECK ROUTINE
34              ;       CHECKS THE ENTIRE READ BUFFER, ANY ERROR ACCOMPANIED
35              ;       BY AN ECC ERROR, TERMINATING THE READ, MAY CAUSE ALL
36              ;       DATA IN SUCCEEDING SECTORS TO APPEAR BAD.
37
38              ;       TESTS THAT PERFORM A RECALIBRATE HAVE A 2 SEC.
39              ;       DELAY BUILT INTO THE SCOPE LOOP.    SET SWPAK 9 = 1
40              ;       TO INTRODUCE AN ADDITIONAL 1 SECOND DELAY DURING
41              ;       THE SCOPE LOOP.
42
43              ;       IN GENERAL EACH SUCCESSIVE TEST ASSUMES ALL
44              ;       PREVIOUS TESTS WORK.  BYPASSING ERRORS
45              ;       CAN RESULT IN CONFUSING SITUATIONS
46              ;       IN THE SETUP OF MORE COMPLEX TESTS.
47
48              ; 11. DEBUG HELP:
49                      O?DTD   11B
50
51              ; 12. SPECIAL NOTES/SPECIAL FEATURES:
52
53              ;       12.1 IF THE DISK PACK HAS BAD SECTOR FLAGS SET ON CYLINDER
54              ;       0, OR ON THE FIRST 8 SECTORS OF HEAD 0 OF ANY CYLINDER,
55              ;       ERROR PRINTOUTS WILL RESULT WHEN THE FLAGS ARE
56              ;       ENCOUNTERED.
57
58              ;       12.2  TESTS F1-F3 ALTER THE FORMAT ON
59              ;       CYL 0, HD 0, SEC 0 FOR PURPOSES OF
60              ;       CHECKING THE FORMAT LOGIC AND BAD SECTOR LOGIC.
```

```
0006  DISKD
01              ;       SWPAK7 SHOULD BE SET TO 1 IN ORDER TO STOP PROGRAM
02              ;       FROM EXECUTING THE FORMAT.
03
04              ;       12.3  SOME SCOPE LOOPS WILL REQUIRE A RECALIBRATE
05              ;       TO INITIALIZE THE DISK DRIVE FOLLOWING A FAILURE.
06              ;       SET SWPAK 8 = 1 TO INTRODUCE THE RECALIBRATE TO THE
07              ;       UNIT UNDER TEST.
08
09              ;       12.4  DISK PACKS
10              ;       ONLY USE DISK PACKS FORMATTED BY THE DISKF
11              ;       PACK FORMATTER PROGRAM.  THE DIAGNOSTIC PROGRAM
12              ;       WILL WRITE OVER MOST OF THE DISK SURFACE.
13
14              ; 13.  RUN TIME:
15              ;       THE RUN TIME FOR A PASS IS APPROXIMATELY:  3 MIN.
```

```
 0001  DISKF    AOS ASSEMBLER REV 04.20        18:25:35 06/22/84
01              ;
02              ;
03              ;
04              ;*************************************************************
05              ;
06              ;
07              ; DESCRIPTION: ZETACO SMD DISK CONTROLLER FORMATTER PROGRAM
08              ;
09              ;
10              ; PRODUCT OF ZETACO, 1984
11              ;*************************************************************
                        TITL   DISKF
13      000001          .DUSR  X=1
14      000001          .NOMAC X
15              ;1.0    PROGRAM NAME:   DISKF.SR
16
17              ;2.0    REVISION HISTORY:
18              ;
19              ;       REV.       DATE
20              ;        00     02/09/83        ;
21              ;        01     08/22/83        ;ADDB FOR ALT1 (STTD), AOS BSTRAP (400'S)
22              ;        02     03/28/84        ;DISK PULSE COUNTER, ERROR LOGS, 200.
23              ;                               ;ERRORS, MSB FOR BAD SECTOR LOG
24              ;                               ;DEVICE CODE CHANGE ROUTINE
25              ;        03     05/30/84        ;ECC ON WRITE, IDF1
26              ;
27
28              ;3.0    MACHINE REQUIREMENTS:
29
30              ;       NOVA/ECLIPSE FAMILY CENTRAL PROCESSOR
31              ;       16K READ/WRITE MEMORY
32              ;       TELETYPE OR CRT DISPLAY
33              ;       ZETACO SMD DISK CONTROLLER
34              ;       0-3 DISK DRIVES
35
36              ;4.0    TEST REQUIREMENTS:      N/A
37
38              ;5.0    SUMMARY:
39
40              ;       THE ZETACO SMD DISK CONTROLLER FORMATTER
41              ;       PROGRAM IS A PROGRAM DESIGNED TO FORMAT AND
42              ;       CHECK DISK PACKS TO BE USED ON DISK SYSTEMS.
43              ;       THE PROGRAM IS !NOT! A MAINTENANCE PROGRAM
44              ;       AND ASSUMES THE HARDWARE TO BE IN WORKING ORDER.
45              ;       THE PROGRAM WILL HALT ON ANY NON-DATA RELATED
46              ;       ERRORS. ALTHOUGH PRESSING CONTINUE WILL ALLOW
47              ;       THE PROGRAM TO PROCEED, IT IS NOT RECOMMENDED
48              ;       THAT THE PROGRAM BE RUN UNDER THESE CONDITIONS.
49              ;       IT IS ALSO RECOMMENDED THAT ON-BOARD ECC BE
50              ;       SOFTWARE OR CONFIGURED DISABLED WHEN FORMATTING.
51
52
53              ;       THE CONTROL CAN BE ANY DEVICE 20-76 OCTAL
54              ;       THE DEFAULT IS 27  ## SEE 9.
55
56              ;6.0    RESTRICTIONS:   N/A
57
58              ;7.0    PROGRAM DESCRIPTION/THEORY OF OPERATION:
59
60              ;       A.      FORMATTER PROGRAM (STARTING ADDRESS <SA> 500)
```

```
 0002  DISKF
01              ;       THE DISK IS FIRST FORMATTED AFTER WHICH A FORMAT
02              ;       DONE MESSAGE IS PRINTED.  THEN A 55555 PATTERN
03              ;       IS WRITTEN TO THE ENTIRE PACK AND READ BACK 2 TIMES,
04              ;       A RANDOM SEEK TEST IS PERFORMED, AND PASS IS PRINTED.
05              ;       THE DATA PATTERN IS THEN ROTATED
06              ;       1 BIT AND THE WRITE/READ/READ/SEEK PROCESS IS REPEATED.
07              ;       AT THE COMPLETION OF THE NUMBER OF PASSES ENTERED
08              ;       BY THE OPERATOR, A LOG IS PRINTED AND THE DRIVES
09              ;       ARE RELEASED.
10              ;*************************************************************
11              ;       IT IS RECOMMENDED THAT AT LEAST 3 PASSES (W/R/R/S),WITH
12              ;       ON-BOARD ECC SOFTWARE DISABLED, BE ALLOWED TO INSURE PACK
13              ;       QUALITY.  IF TIME PERMITS, LONGER RUNS WILL FURTHER INSURE
14              ;       RELIABILITY.
15              ;*************************************************************
16                      ANY HARD DATA OR ADDRESS ERRORS WILL RESULT IN THE
17                      BAD SECTOR FLAG BEING SET IN THAT SECTOR.  ANY
18                      "SOFT DATA" OR "ADDRESS ERROR" ADDRESS ENCOUNTERED
19                      TWICE CAUSE THE BAD SECTOR FLAG TO BE SET.  ANY OTHER
20                      ERROR WILL CAUSE THE PROGRAM TO PRINT THE FAILURE TO
21                      THE TTY AND THE PROGRAM WILL HALT.  **THIS PROGRAM IS NOT
22                      INTENDED TO BE A RELIABILITY PROGRAM FOR THE DISK SYSTEM
23                      AND IN GENERAL ASSUMES THE CONTROL AND DRIVE TO BE IN
24                      WORKING ORDER.
25
26                      A HARD ADDRESS ERROR IS DEFINED AS SUCH AFTER TWO
27                      ATTEMPTS HAVE BEEN MADE BOTH RESULTING IN AN ADDRESS
28                      ERROR.  A HARD DATA ERROR IS DEFINED AS SUCH AFTER
29                      2 OR MORE OF 10 WRITE/READ RETRY'S HAVE BEEN
30                      UNSUCCESSFUL.
31
32                      B       CHECK PROGRAM ONLY (SA 501)
33                      SAME AS SA 500 EXCEPT THAT INITIAL PACK FORMAT
34                      OPERATION IS BYPASSED.
35
36                      C.      STATISTICS
37                      TYPE C FOR 1ST 200. DISK ADDRESSES OF BAD SECTORS,
38                      DATA AND ADDRESS ERRORS, PLUS A STATISTIC TABLE OF
39                      OVERALL ERRORS.
40                      **NOTE** ANY CHARACTER TYPED WHILE EXECUTING
41                      THIS LOG WILL END IT AT THE NEXT CHANGE OF
42                      DATA TYPE.
43
44              ;       D.      LOG RECOVERY (SA 502)
45              ;       USE TO RECOVER LOG IF PROGRAM WAS STOPPED BEFORE
46              ;       LOG PRINTOUT.
47
48              ;       E.      COMMAND STRING INTERPRETER (SA 503)
49              ;       AS A TROUBLE SHOOTING AID THE SERVICE
50              ;       ENGINEER MAY TYPE IN HIS OWN TEST LOOP.
51              ;       AFTER STARTING AT 503, THREE ARGUMENTS
52              ;       MUST BE ENTERED IN RESPONSE TO THREE
53              ;       PROGRAM QUESTIONS; "UNIT", "DATA", AND
54              ;       "COMMAND STRING".  ALL NUMBERS MUST ENTERED
55              ;       IN OCTAL.
56
57              ;       I.      UNIT:   TYPE UNIT # OR CARRIAGE TO
58              ;                       USE THE PREVIOUS ENTRY
59
60              ;       II.     DATA:   RAN=RANDOM
```

```
0003  DISKF
01                ;                       ALO=ALL ONES
02                ;                       ALZ=ALL ZEROS
03                ;                       PAT=110110 PATTERN
04                ;                       FLO=FLOATING ONE PATTERN
05                ;                       FLZ=FLOATING ZERO PATTERN
06                ;                       ADR=ALTERNATING CYLINDER AND
07                ;                       HEAD, SECTOR WORDS
08                ;                       VAR=EXISTING WORDS ENTERED PREVIOUSLY AS
09                ;                       DESCRIBED BELOW
10
11                ;                       ALTERNATIVELY ENTER A STRING OF UP TO 7
12                ;                       OCTAL 16 BIT WORDS TO BE
13                ;                       USED AS DATA.  THE WORDS
14                ;                       ENTERED ARE USED REPEATEDLY
15                ;                       TO MAKE UP A SECTOR BLOCK.
16                ;                       TYPE CARRIAGE TO USE THE
17                ;                       PREVIOUS ENTRY.
18
19                ;       III.    COMMAND STRING:
20
21                ;       OPTIONS 1.      READ HEAD, SECTOR, #SECTORS
22                ;               2.      WRITE SAME
23                ;               3.      SEEK CYLINDER
24                ;               4.      RECALIBRATE
25                ;               5.      LOOP (GO TO BEGINNING OR LR)
26                ;               6.      DELAY N (N=DELAY IN MS)
27                ;               7       DISABLE (WRITE DISABLE)
28                ;               8.      TRESPASS
29                ;               9       STOP DISK
30                ;               10.     RELEASE
31                ;               11.     OFF (OFFSET FORWARD)
32                ;               12.     OFR (OFFSET REVERSE)
33                ;               13.     LR  (BEGIN LOOP HERE)
34                ;               14      VERIFY (WRITE)
35                ;               15      FORMAT CYL, HD, SECTOR
36                ;               16.     SAD (BAD SECTOR) CYL, HD, SECTOR
37                ;               17.     MEMORY ADDR, DATA(WRITE) (CONTROLLER MEMORY COMMAND)
38                ;               18.     TYPE CARRIAGE RETURN TO USE THE
39                ;                       PREVIOUS COMMAND STRING.
40
41                ;               NOTE THAT EITHER SPACES OR A COMMA
42                ;               MAY BE USED AS AN ARGUMENT DELIMITER.
43                ;               EACH RESPONSE IS TERMINATED BY
44                ;               TYPING CARRIAGE RETURN.  IF MORE
45                ;               ROOM IS NEEDED ON A LINE, TYPE
46                ;               LINE FEED TO SPACE TO THE NEXT LINE.
47                ;               THE WORD "SAME" USED WITH READ, OR WRITE,
48                ;               WILL CAUSE THE PREVIOUS DISK
49                ;               ADDRESS PARAMETERS TO BE USED.
50
51                ;       AN R TYPED WHILE A STRING IS BEING EXECUTED WILL
52                ;       CAUSE THE PROGRAM TO RETURN TO COMMAND STRING START.
53                ;       THE ESCAPE KEY WILL BYPASS UNIT AND DATA PROMPTS TO
54                ;       THE COMMAND STRING PROMPT.
55
56                ;       THE FOLLOWING EXAMPLE WOULD CAUSE UNIT
57                ;       1 TO SEEK CYLINDER 50, THEN REPEATEDLY
58                ;       WRITE SECTORS 2 AND 3 OF HEAD 5,
59                ;       THEN READ IT BACK AND CHECK.  DATA IS SPECIFIED
60                ;       AS ALTERNATE WORDS OF ZEROS THEN ONES.
```

```
0004  DISKF
01
02              ;       UNIT: 1
03              ;       DATA: 0,177777
04              ;       COMMAND STRING: SEEK 50 LR WRITE 5,2,2 READ SAME LOOP
05
06              ;       THE FOLLOWING EXAMPLE WOULD WRITE ZERO TO
07              ;       CONTROLLER MEMORY LOCATION 1500 (OCTAL)
08
09              ;       UNIT:   1
10              ;       DATA:   N/A
11              ;       COMMAND STRING: MEMORY 101500,0
12              ;       NOTE: UPPER MEMORY BIT = 1 DEFINES A WRITE
13              .
14              ,
15              .8.     SWITCH SETTINGS
16              ,       SWPD   8
17              ,
18              .8.1    SWITCH OPTIONS
19                      DIFFERENT BITS AND THEIR INTERPRETATION AT LOCATION
20                      "SWREG" IS AS FOLLOWS
21                      .
22                      BIT     OCTAL   BINARY  INTERPRETATION
23                              VALUE   VALUE
24                      .
25                      1               0       LOOP ON ERROR
26                      .               40000   1       SKIP LOOPING ON ERROR
27                      .
28                      2               0       PRINT TO CONSOLE
29                      .               20000   1       ABORT PRINT OUT TO CONSOLE
30                      .
31                      3               0       DO NOT PRINT ON THE LINE PRINTER
32                      .               02000   1       PRINT ON THE LINE PRINTER
33                      .
34                      11:8)           0       N/A
35                                      00020   1       ENABLE BAD SECTOR PRINTOUT
36                      .
37                      .
38              .9.0    OPERATING PROCEEDURE/OPERATOR INPUT
39                      .
40                      .       A. VERIFY DRIVE (DRIVES) ARE READY ON-LINE
41                      .       B. LOAD PROGRAM USING BINARY LOADER
42                      .       C. TO RUN OTHER THAN TEST 500, ENTER CONTROL "D"
43                      ,          AT 9.2, ENTER STARTING ADDRESS FOLLOWED BY AN "R"
44                      .
45              ,       STARTING ADDRESS (SA)
46              ,       200     READ UNIT CHARACTERISTICS AND THEN RUN FORMATTER (500)
47              ,       500     FORMATTER/CHECK PROGRAM
48              ,       501     CHECK PROGRAM ONLY
49              ,       502     ERROR LOG RECOVERY (SEE 7.8,8A)
50              ,       503     COMMAND STRING INTERPRETER
51              .
52              .9.1    OPERATOR IS REQUESTED TO ENTER DEVICE CODE OF
53              ,       CONTROLLER (DEFAULT 27)
54              .9.2    OPERATOR IS REQUESTED TO SET SWPAK FOLLOWED
55              .       BY A CARRIAGE RETURN (SEE 8.3)
56              .9.3    MONTH, DAY, YEAR (I.E. 77...), HOUR, & MIN
57              .       (IF [CR] IS GIVEN THIS ROUTINE IS BYPASSED)
58              .9.4    ENTER # OF PASSES FOR TEST COMLETION (IF [CR] IS
59              ,       GIVEN THIS ROUTINE IS BYPASSED)
60              .9.5    OPERATOR IS REQUESTED TO ENTER YES/NO TO CONTROLLER
```

```
0005  DISKF
01              ;       CORRECTION, IF IT IS ENABLED
02              ;9.6    UNIT NUMBERS, TYPES, AND THEIR CHARACTERISTICS
03              ;       ARE THEN DISPLAYED, "PLEASE VERIFY"
04              ;       OPERATOR IS THEN REQUESTED TO ENTER
05              ;       UNIT NUMBERS TO BE TESTED(0-3)
06              ;9.7    OPERATOR IS THEN REQUESTED TO ENTER
07              ;       TYPE OF DISK (USER DEFINED ENTER 10)
08              ;       A.      IF TYPE ENTERED DID NOT MATCH, ENTER 0
09              ;               1 2 OR 3 TO RE-DEFINE A DISK TYPE
10              ;       B.      # OF HEADS FOR NEW TYPE (IN DECIMAL)
11              ;       C.      # OF CYLINDERS FOR NEW TYPE (IN DECIMAL)
12              ;       D.      # OF SECTORS FOR NEW TYPE (IN DECIMAL, CANNOT BE DOWNSIZED)
13              ;       E.      RETURN TO 9.7
14
15              ;       OPERATOR INPUT CONTROLLED PRINTOUTS ARE AS FOLLOWS:
16              ;
17              ;       L       = FIRST 200. BAD SECTORS, DATA, OR ADDRESSES
18              ;               ALSO LISTED IS A COUNT FOR CONTROLLER
19              ;               CORRECTS/UNIT (ON BOARD ECC CORRECTION AND OFFSET CORRECTS)
20
21              ;10.0   PROGRAM OUTPUT/ERROR DESCRIPTION:
22
23              ;       1. ERRORS- ERROR STATUS IS PRINTED
24              ;       WHENEVER ENCOUNTERED.  WHEN DATA ERRORS
25              ;       ARE FOUND ONLY THREE ARE PRINTED PER
26              ;       ENCOUNTER.  (SEE PARAGRAPH 10.3)
27
28              ;       2. IF ERRORS ARE ENCOUNTERED MORE THAN ONCE,
29              ;       A COUNT WILL BE RECORDED AND A BAD SECTOR FLAG SET.
30              ;       ALL ADDRESS INFO. WILL BE PRINTED IN OCTAL.
31
32              ;       3  ERROR REPORTING AND RECOVERY
33
34              ;       ALL ERRORS ARE IDENTIFIED, AND THE
35              ;       PROGRAM IS ROUTED VIA BASE TO A CALL TO CKSW.
36              ;       WITH THE EXCEPTION OF ADDRESS AND DATA ERRORS
37              ;       THE PROGRAM WILL THEN LOOP FOR OPERATOR INTERVENTION,
38              ;       ON THE BASIS OF SWPAK (SEE 8.)
39
40              ;       RECALIBRATE - ANY UNUSUAL STATUS IS REPORTED
41              ;       IMMEDIATELY AND AN ERROR RETURN EXECUTED.
42
43              ;       SEEK - POSITIONER FAULT STATUS RESULTS
44              ;       IN STATUS PRINTOUT AND ERROR RETURN.
45
46              ;       WRITE - FOLLOWING "DONE" ON A WRITE, ERRORS ARE
47              ;       CHECKED IN THE SEQUENCE SHOWN BELOW.  ERROR
48              ;       RECOVERY PROCEDURE IS OUTLINED FOR EACH CASE.
49              ;       IF THE ERROR IS NOT PRESENT THE NEXT CHECK IS MADE.
50
51              ;       DRIVE STATUS (DIB) IS CHECKED 1ST FOR BOTH READ AND
52              ;       WRITE BEFORE ANY DIA CHECKS ARE MADE
53
54              ;       4. READ/WRITE TIMEOUTS, DATA LATE, ILLEGAL SECTOR,
55              ;       ECC(DATA OK), OR ANY DRIVE FAULT- PRINT THE ILLEGAL
56              ;       STATUS AND DO AN ERROR RETURN.
57
58              ;       5. ADDRESS ERROR- REPEAT THE WRITE, IF TEST PASSES
59              ;       THE SECOND TIME, DO A NORMAL RETURN; OTHERWISE
60              ;       FLAG AS HARD, SET THE BAD SECTOR FLAG FOR THAT SECTOR
```

```
0006  DISKF
01              ;       AND DO AN ERROR RETURN.
02
03              ;       IF A HARD CYLINDER ADDRESS ERROR OCCURS, A READ
04              ;       ON AN ADJACENT HEAD WILL BE ATTEMPTED TO DETERMINE
05              ;       WHETHER THE FAULT SHOULD BE CLASSED AS A SEEK ERROR
06              ;       OR AN ADDRESS ERROR. THE FIRST 30. HARD ADDRESS
07              ;       ERRORS WILL HAVE THEIR ADDRESSES LOGGED.
08
09              ;       6. ENDING MEMORY ADDRESS -PRINT THE ERROR MESSAGE,
10              ;       CHECK FOR A DISK ADDRESS AND DO AN ERROR RETURN.
11
12              ;       7. ENDING DISK ADDRESS -PRINT THE ERROR MESSAGE AND
13              ;       DO AN ERROR RETURN.
14
15
16              ;       READ - ALL READ ERRORS WITH THE EXCEPTION OF DATA RELATED
17              ;       ERRORS ARE HANDLED THE SAME AS DESCRIBED FOR THE WRITE
18              ;       OPERATIONS
19
20              ;       DATA ERRORS - DATA IS REREAD 9 TIMES.
21              ;       IF DATA IS BAD ON 2 OR MORE OF
22              ;       10 TRIES, A HARD ERROR COUNT IS INCREMENTED,
23              ;       THE BAD SECTOR FLAG IS SET IN THAT SECTOR, AND AN
24              ;       ERROR RETURN IS TAKEN. IF DATA IS GOOD ON ALL RETRIES,
25              ;       THE ERROR IS CONSIDERED SOFT AND A NORMAL RETURN IS
26              ;       TAKEN.
27
28              ;       THE 1ST 200. DATA ERRORS (HARD OR SOFT) ARE LOGGED
29              ;11.0   DEBUG HELP:
30                      ;DDTD   11
31
32
33              ;12.0   SPECIAL NOTES/SPECIAL FEATURES:
34
35              ;       1. THE PROGRAM IS 'NOT' A MAINTENANCE PROGRAM
36              ;       AND ASSUMES THE HARDWARE TO BE IN WORKING ORDER.
37              ;       THE PROGRAM WILL HALT ON ANY NON-DATA RELATED
38              ;       ERRORS. ALTHOUGH PRESSING CONTINUE WILL ALLOW
39              ;       THE PROGRAM TO PROCEED, IT IS NOT RECOMMENDED
40              ;       THAT THE PROGRAM BE RUN UNDER THESE CONDITIONS.
41
42              ;       2. IT IS RECOMMENDED THAT AT LEAST 3 PASSES (W/R/R/S)
43              ;       BE ALLOWED (SEE BELOW) TO INSURE PACK QUALITY.
44              ;       IF TIME PERMITS, LONGER RUNS WILL FURTHER
45              ;       INSURE QUALITY.
46
47              ;12.1   PROGRAM RUNTIME:
48
49              ;       PROGRAM RUNTIMES ARE SUBSTANTIALLY REDUCED WITH
50              ;       MEMORIES OF 24K OR LARGER. RUNTIMES ARE ALSO
51              ;       DEPENDANT ON CPU TYPE, DRIVE SIZE AND DRIVE TYPE.
52
53              ;       3 PASSES AFTER FORMAT ARE RECOMMENDED FOR
54              ;       SURFACE VERIFICATION.
55
56              ;       READ, WRITE AND SEEK OPERATIONS ARE TIMED
57              ;       BY SPECIAL ROUTINES. WHEN THE PROGRAM IS
58              ;       FIRST STARTED, THE TIMING ROUTINE WILL TEST
59              ;       FOR THE PRESENCE OF A REAL TIME CLOCK (RTC)
60              ;       TO DERIVE TIMING FROM IT.
```

```
 0001  DISKR    AOS ASSEMBLER REV 04.20        18:25:36 06/22/84
01              ;
02              ;
03              ;
04              ;*********************************************************
05              ;
06              ;
07              ; DESCRIPTION: ZETACO SMD DISK CONTROLLER RELIABILITY PROGRAM
08              ;
09              ;
10              ; PRODUCT OF ZETACO, 1984
11              ;*********************************************************
                        .TITL   DISKR
13      000001          .DUSR   X=1
14      000001          .NOMAC  X
15              ;1.0    PROGRAM NAME:   DISKR.SR
16
17              ;2.0    REVISION HISTORY:
18              ;
19              ;       REV.      DATE
20              ;        00     02/09/83              ;
21              ;        01     09/07/83              ;S120 # SKP TOGETHER, STACK AND
22              ;                                     ;AOS BOOTSTRAP AT 400, NO VERIFY
23              ;                                     ;WITH RANDOM DATA TEST 502 SWT 10
24              ;        02     03/28/84              ;ADD RELEASE COMMAND TO RC
25              ;                                     ;FOR DUAL PORT, DAISY CHAIN
26              ;                                     ;DISK SECTOR PULSE COUNTER
27              ;                                     ;DEVICE CODE CHANGE ROUTINE
28              ;                                     ;502 PAT 24 SECTOR
29              ;        03     05/30/84              ;ZDF1,
30
31              ;3.0    MACHINE REQUIREMENTS:
32
33              ;       NOVA/ECLIPSE FAMILY CENTRAL PROCESSOR
34              ;       16K READ/WRITE MEMORY
35              ;       TELETYPE OR CRT DISPLAY
36              ;       ZETACO SMD DISK CONTROLLER
37              ;       0-3 DISK DRIVES
38
39              ;4.0    TEST REQUIREMENTS:      N/A
40
41              ;5.0    SUMMARY:
42
43              ;       THE ZETACO DISK CONTROLLER RELIABILITY
44              ;       PROGRAM IS A MAINTENANCE PROGRAM DESIGNED TO
45              ;       EXERCISE AND TEST THE ZETACO SMD DISK SUB-SYSTEMS
46              ;       AND 1-4 DISK DRIVES.  THE DISK DRIVES MAY BE
47              ;       SHARED BETWEEN TWO COMPUTERS IN WHICH CASE
48              ;       THE FOLLOWING PROGRAMS MAY BE RUNNING IN EACH
49              ;       COMPUTER:
50
51              ;       STARTING ADRESSES'S (SA) 500,501 RANDOM RELIABILITY
52              ;       SA 503 COMMAND STRING (IF A RELEASE COMMAND IS
53              ;       INCLUDED IN THE COMMAND STRING)
54
55              ;       THE CONTROL CAN BE ANY DEVICE CODE 20-76 OCTAL
56              ;       THE DEFAULT IS 27 -SEE 9.1 FOR OTHER SETTINGS
57
58              ;6.0    RESTRICTIONS:
59
60              ;       1. THE DISK DRIVES MAY BE
```

```
0002  DISKR
01              ;       SHARED BETWEEN TWO COMPUTERS IN WHICH CASE
02              ;       THE FOLLOWING PROGRAMS MAY BE RUNNING IN EACH
03              ;       COMPUTER:
04
05              ;       STARTING ADRESSES'S (SA) 500,501 RANDOM RELIABILITY
06              ;       SA 503 COMMAND STRING (IF A RELEASE COMMAND IS
07              ;       INCLUDED IN THE COMMAND STRING)
08
09              ;       IF NO DRIVES ARE TO BE SHARED, THERE ARE NO OTHER
10              ;       RESTRICTIONS AS TO THE RUNNING OF THESE PROGRAMS ON
11              ;       A DUAL PROCESSOR SYSTEM.
12
13              ;       2. ANY COMBINATION OF DRIVES
14              ;       MAY BE TESTED BY THIS PROGRAM AT A SINGLE TIME.
15
16              ;7.0    PROGRAM DESCRIPTION/THEORY OF OPERATION:
17
18              ;       A. RELIABILITY TEST (SA 500)
19
20              ;       A RANDOM NUMBER GENERATOR IS USED TO SELECT A
21              ;       DISK DRIVE,CYLINDER,HEAD,BEGINNING SECTOR,
22              ;       AND NUMBER OF CONSECUTIVE SECTORS.  RANDOM
23              ;       DATA IS THEN GENERATED, WRITTEN, AND READ.
24              ;       THE SEQUENCE IS REPEATED INDEFINITELY.
25              ;       IF RUNNING MULTIPLE UNITS, OVER LAPPED SEEKS ARE
26              ;       EMPLOYED IF THE NEXT RANDOM UNIT IS DIFFERENT FROM
27              ;       THE CURRENT UNIT UNDER I/O EXECUTION.
28
29              ;       B. RELIABILITY TEST (SA 501) WITH OPTIONS
30
31              ;       SAME AS A, EXCEPT THAT OPERATOR IS GIVEN
32              ;       OPTIONS ON DATA PATTERNS (SEE 7D II)
33              ;       AND MAY CHOOSE A CONSTANT CYLINDER,HEAD,SECTOR
34              ;       OR # OF SECTORS.  ANY LETTER RESPONSE TO CYL,HEAD ETC.
35              ;       GETS RANDOM FUNCTION FOR THAT VARIABLE.  A CARRIAGE
36              ;       RETURN ONLY GETS THE RANDOM FUNCTION FOR ALL VARIABLES.
37
38              ;       THE OPERATOR IS ALSO ASKED TO RESPOND TO
39              ;       JITTER OPTION(YES/NO).  IF YES, A RANDOM DELAY(0-40,50MS)
40              ;       IS INSERTED INTO THE BACKGROUND LOOP TO CREATE
41              ;       A MORE ASYNCHRONOUS DISK I/O LOOP.
42
43              ;       C.  INCREMENTAL DISK ADDRESS TEST (SA 502)
44
45              ;       OPERATOR IS GIVEN OPTION ON DATA (SEE 7D II)
46              ;       REQUESTED DATA IS FIRST WRITTEN (SEE SWPAK10) OVER
47              ;       THE ENTIRE PACK.  THEN THE DATA IS READ FROM
48              ;       ALL SECTORS .  THIS INSURES THAT ALL DISK
49              ;       PACK BLOCKS ARE USEABLE AND ARE FORMATTED
50              ;       PROPERLY.  THE TEST IS THEN REPEATED FOR ALL
51              ;       READY DISCS, AND PASS IS PRINTED.  THE
52              ;       SEQUENCE IS REPEATED INDEFINITELY.
53
54
55              ;       #NOTE
56              ;       SWPAK7=1, PROGRAM WAITS AFTER WRITE WITH READ
57              ;       VERIFICATION ALLOWING OPERATOR TO CHANGE PACKS.
58              ;       SWPAK8=1, PUTS PROGRAM INTO READ ONLY MODE
59              ;       ## SA'S 501,502 ONLY.  IF SA 501-DATA MUST !NOT! BE
60              ;       RANDOM (SEE 7D II).
```

```
0003  DISKR
01
02              ;       ALL NUMBERS ENTERED ABOVE MUST BE IN OCTAL.
03              ;       ANY NON-OCTAL INPUT IS TREATED AS A LETTER.
04              ;       ANY LETTER INPUT FOR CYL, HEAD, SECTOR, OR # OF
05              ;       SECTORS GETS RANDOM FUNCTION IN THE RELIABILITY
06              ;       TEST WITH OPTIONS.
07
08
09              ;       D. COMMAND STRING INTERPRETER (SA 503)
10              ;       AS A TROUBLE SHOOTING AID THE SERVICE
11              ;       ENGINEER MAY TYPE IN HIS OWN TEST LOOP.
12              ;       AFTER STARTING AT 503, THREE ARGUMENTS
13              ;       MUST BE ENTERED IN RESPONSE TO THREE
14              ;       PROGRAM QUESTIONS; "UNIT", "DATA", AND
15              ;       "COMMAND STRING". ALL NUMBERS MUST ENTERED
16              ;       IN OCTAL.
17
18              ;       I.      UNIT:   TYPE UNIT # OR CARRIAGE TO
19              ;                       USE THE PREVIOUS ENTRY
20
21              ;       II.     DATA:   RAN=RANDOM
22
23              ;                       ALO=ALL ONES
24              ;                       ALZ=ALL ZEROS
25              ;                       PAT=155555 PATTERN
26              ;                       ROT=155555 PATTERN ROTATED ON
27              ;                       SUCCESSIVE PASSES.
28              ;                       ALT=52525 PATTERN
29              ;                       FLO=FLOATING ONE PATTERN
30              ;                       FLZ=FLOATING ZERO PATTERN
31              ;                       ADR=ALTERNATING CYLINDER AND
32              ;                       HEAD, SECTOR WORDS
33              ;                       VAR=EXISTING WORDS ENTERED PREVIOUSLY AS
34              ;                       DESCRIBED BELOW
35
36              ;                       ALTERNATIVELY ENTER A STRING OF UP TO 7
37              ;                       OCTAL 16 BIT WORDS TO BE
38              ;                       USED AS DATA.  THE WORDS
39              ;                       ENTERED ARE USED REPEATEDLY
40              ;                       TO MAKE UP A SECTOR BLOCK.
41              ;                       TYPE CARRIAGE TO USE THE
42              ;                       PREVIOUS ENTRY.
43
44              ;       III.    COMMAND STRING:
45
46              ;       OPTIONS 1.      READ HEAD, SECTOR, #SECTORS
47              ;               2.      WRITE SAME
48              ;               3.      SEEK CYLINDER
49              ;               4.      RECALIBRATE
50              ;               5.      LOOP (GO TO BEGINNING OR LR)
51              ;               6.      DELAY N (N= DELAY IN MS)
52              ;               7.      DISABLE (WRITE DISABLE)
53              ;               8.      TRESPASS
54              ;               9.      STOP DISK
55              ;               10.     RELEASE
56              ;               11.     OFF (OFFSET FORWARD)
57              ;               12.     OFR (OFFSET REVERSE)
58              ;               13.     LR  (BEGIN LOOP HERE)
59              ;               14.     VERIFY (WRITE)
60              ;               15.     MEMORY ADDR, DATA (WRITE) (CONTROLLER MEMORY COMMAND)
```

```
0004  DISKR
01                ;               16.     TYPE CARRIAGE RETURN TO USE THE
02                ;                       PREVIOUS COMMAND STRING.
03                ;               NOTE THAT EITHER SPACES OR A COMMA
04                ;               MAY BE USED AS AN ARGUMENT DELIMITER.
05                ;               EACH RESPONSE IS TERMINATED BY
06                ;               TYPING CARRIAGE RETURN.  IF MORE
07                ;               ROOM IS NEEDED ON A LINE, TYPE
08                ;               LINE FEED TO SPACE TO THE NEXT LINE.
09                ;               THE WORD "SAME" USED WITH READ, OR WRITE,
10                ;               WILL CAUSE THE PREVIOUS DISK
11                ;               ADDRESS PARAMETERS TO BE USED.
12                ;
13                ;               AN R TYPED WHILE A STRING IS BEING EXECUTED
14                ;               WILL CAUSE THE PROGRAM TO  RETURN TO THE
15                ;               COMMAND STRING START.  THE ESCAPE KEY WILL
16                ;               BYPASS THE UNIT AND DATA PROMPTS TO THE
17                ;               COMMAND STRING PROMPT.
18                ;
19                ;       THE FOLLOWING EXAMPLE WOULD CAUSE UNIT
20                ;       1 TO SEEK CYLINDER 50, THEN REPEATEDLY
21                ;       WRITE SECTORS 2 AND 3 OF HEAD 5,
22                ;       THEN READ IT BACK AND CHECK.  DATA IS SPECIFIED
23                ;       AS ALTERNATE WORDS OF ZEROS THEN ONES.
24                ;
25                ;       UNIT: 1
26                ;       DATA: 0,177777
27                ;       COMMAND STRING: SEEK 50 LR WRITE 5,2,2 READ SAME LOOP
28                ;
29                ;       THE FOLLOWING EXAMPLE WOULD WRITE ZERO TO
30                ;       CONTROLLER MEMORY LOCATION 1500 (OCTAL)
31                ;
32                ;       UNIT:   1
33                ;       DATA:   N/A
34                ;       COMMAND STRING: MEMORY 101500,0
35                ;       NOTE: UPPER MEMORY BIT = 1 DEFINES A WRITE
36                ;
37                ;       E.  QUICKIE FORMATTER (SA 504)
38                ;       FORMATS PACK AND HALTS.  THERE IS NO VERIFY,
39                ;       NO FLAGS ARE SET, AND NO ERROR CHECKING.
40                ;
41                ;       F  RUNALL (SA 505)
42                ;       PROGRAM ALTERNATES BETWEEN THE PROGRAMS DESCRIBED
43                ;       IN 7.B(4 DATA PATTERNS -PAT,RAN,FLZ,FLO) AND
44                ;       7.C(6 DATA PATTERNS -PAT,RAN,ADR,ALT1,ZEROES,ONES)
45                ;       AND 7.H, AND IN THAT ORDER.
46                ;
47                ;       G.  SEEK EXERCISER (SA 506)
48                ;       PROGRAM PROVIDES A SEEK SCAN SEQUENCE
49                ;       CONVERGING FROM THE EXTREME OUTERMOST TRACKS INTO THE
50                ;       ADJACENT TRACK IN THE CENTER, THEN DIVERGING AGAIN TO
51                ;       THE EXTREMES.
52                ;
53                ;       H.  RANDOM SEEK EXERCISER (SA 507)
54                ;       PROGRAM PROVIDES A RANDOM SEEK SEQUENCE
55                ;
56                ;       ###G,H ALL SEEKS IN G/H ARE FOLLOWED BY A 1 SECTOR READ
57                ;       BUT WITH NO DATA CHECK.  ALL SEEKS ARE TIMED
58                ;       WITH MAX, MIN, AND AVE.  TIMES BEING LOGGED IN MS.
59                ;       SEEK PATHS FOR MAX,MIN VALUES ARE ALSO LOGGED.
60                ;       ###CAUTION -ECC ERRORS WILL RESULT IN SA'S 506,507 IF
```

```
0005  DISKR
01              ;       PACK IS NOT 1ST WRITTEN AFTER FORMATTING.
02
03              ;       I.  ERROR COUNT/LOG RECOVERY (SA 510)
04              ;       IN THE EVENT A PROGRAM WAS STOPPED DURING A RUN, THE
05              ;       ERROR LOGS MAY BE RECOVERED AT THIS STARTING ADDRESS.
06              ;       ***MUST BE DONE BEFORE ANY PROGRAM RESTART AS PROGRAM
07              ;       INITIALIZATION ZEROES ALL LOGS.
08              ;
09
10              ;8.     SWITCH SETTINGS
11              ;       S?WPD  8
12              ;8.3    SWITCH OPTIONS
13              ;       DIFFERENT BITS AND THEIR INTERPRETATION AT LOCATION
14              ;       "SWREG" IS AS FOLLOWS:
15              ;
16              ;       BIT     OCTAL   BINARY  INTERPRETATION
17              ;               VALUE   VALUE
18              ;
19              ;       1               0       LOOP ON ERROR
20              ;               40000   1       SKIP LOOPING ON ERROR
21              ;
22              ;       2               0       PRINT TO CONSOLE
23              ;               20000   1       ABORT PRINT OUT TO CONSOLE
24              ;
25              ;       4               0       PRINT PASS
26              ;               04000   1       DO NOT PRINT PASS
27              ;
28              ;       5               0       DO NOT PRINT ON THE LINE PRINTER
29              ;               02000   1       PRINT ON THE LINE PRINTER
30              ;
31              ;       6               0       DO NOT EXIT TO ODT ON ERROR
32              ;               01000   1       EXIT TO ODT ON ERROR
33              ;
34              ;       7               0       **** N/A
35              ;               00400   1       BREAK FOR PACK INTERCHANGE
36
37              ;       8               0       **** N/A
38              ;               00200   1       FOR READ ONLY MODE (SA 501,502)
39              ;
40              ;       9               0       N/A
41              ;               00100   1       BYPASS DATA CHECK
42              ;
43              ;       10(A)           0       N/A
44              ;               00040   1       DO VERIFY AFTER WRITE (SA 502 ONLY AND
45              ;                               NOT RANDOM DATA)
46              ;
47              ;       11(B)           0       N/A
48              ;               00020   1       ENABLE BAD SECTOR PRINTOUTS
49
50              ;       12(C)           0       N/A
51              ;               00010   1       HALT ON DRIVE ERROR PRIOR TO
52              ;                               RECOVERY RECALIBRATE OPERATION
53              ;
54              ;       13(D)           0       NO TRACE
55              ;               00004   1       TRACE PRINTOUT ON ERROR
56
57              ;9.0    OPERATING PROCEEDURE/OPERATOR INPUT:
58
59              ;       A. VERIFY DRIVE (DRIVES) ARE READY ON-LINE
60              ;       B. LOAD PROGRAM USING BINARY LOADER
```

```
0006  DISKR
01              ;       C. TO RUN OTHER THAN TEST 505, ENTER CONTROL "O"
02              ;          AT 9.2, ENTER STARTING ADDRESS FOLLOWED BY AN "R"
03
04              ;       STARTING ADDRESS
05              ;       200     READ UNIT CHARACTERISTICS AND THEN RUN ALL TEST (505)
06              ;       500     RELIABILITY TEST, ALL CYLINDERS
07              ;       501     RELIABILITY TEST, (OPTIONS)
08              ;       502     INCREMENTAL DISK ADDRESS TEST
09              ;       503     COMMAND STRING INTERPRETER
10              ;       504     QUICKIE FORMATTER
11              ;       505     RUN ALL
12              ;       506     SEEK EXERCISER (CONVERGING,DIVERGING PATTERN)
13              ;       507     SEEK EXERCISER (RANDOM PATTERN)
14              ;       510     ERROR COUNT/LOG RECOVERY
15
16              ;9.1    OPERATOR IS REQUESTED TO ENTER DEVICE CODE OF
17              ;       CONTROLLER (DEFAULT IS 27)
18              ;9.2    STARTING ADDRESS IS DISPLAYED AND
19              ;       OPERATOR IS REQUESTED TO SET SWPAK FOLLOWED
20              ;       BY A CARRIAGE RETURN (SEE 8.3)
21              ;9.3    OPERATOR IS REQUESTED TO ENTER YES/NO TO
22              ;       EXERCISE MAPS, IF PRESENT
23              ;9.4    DATE -DAY, MONTH, YEAR (I.E. 77...), HOUR, & MINUTE (A [CR]
24              ;       RESPONSE WILL IGNORE THIS ROUTINE)
25              ;9.5    OPERATOR IS REQUESTED TO ENTER YES/NO IF ANY
26              ;       DUAL VOLUME DRIVES (CMD'S)
27              ;9.6    OPERATOR IS REQUESTED TO ENTER YES/NO TO CONTROLLER
28              ;       CORRECTION, IF IT IS ENABLED
29              ;9.7    UNIT NUMBERS, TYPES, AND THEIR CHARACTERISTICS
30              ;       ARE THEN DISPLAYED, "PLEASE VERIFY"
31              ;       OPERATOR IS THEN REQUESTED TO ENTER
32              ;       UNIT NUMBERS TO BE TESTED (0-3)
33              ;9.8    OPERATOR IS THEN REQUESTED TO ENTER
34              ;       TYPE OF DISK (USER DEFINED ENTER 10)
35              ;       A.      IF TYPE ENTERED IS 10, ENTER 0
36              ;               1 2 OR 3 TO RE-DEFINE A DISK TYPE
37              ;       B.      # OF HEADS FOR NEW TYPE (IN DECIMAL)
38              ;       C.      # OF CYLINDERS FOR NEW TYPE (IN DECIMAL)
39              ;       D.      # OF SECTORS FOR NEW TYPE (IN DECIMAL, CANNOT BE DOWNSIZED)
40              ;       E.      RETURN TO 9.7
41
42              ;       ## A [CR] ONLY RESPONSE TO UNIT NUMBERS, WILL LEAVE
43              ;       UNIT INFORMATION IN PREVIOUS STATE.
44
45              ;       ## A [CR] ONLY RESPONSE TO YES/NO WILL
46              ;       DEFAULT TO NO
47
48              ;       OPERATOR INPUT CONTROLLED PRINTOUTS ARE AS FOLLOWS:
49
50              ;       L       = FIRST 100. BAD SECTORS, DATA, OR ADDRESSES
51              ;       S       = SEEK TIMING STATISTICS (506,507 ONLY)
52              ;       W       = SECTORS W/R, ERROR COUNTS, AND ON BOARD ECC AND OFFSET CORRECTS
53              ;       **NOTE** ANY CHARACTER TYPED WILL END PRINTOUTS AT THE
54              ;       NEXT CHANGE OF DATA TYPE.
55
56              ;       D. OPERATING MODES
57
58              ;       1 OF 4 DIFFERENT MEMORY/INTERRUPT MODES MAY BE IN USE
59              ;       IN THIS PROGRAM AND ARE DESCRIBED AS FOLLOWS:
60
```

```
0007  DISKR
01              ;       1-BACKGROUND ONLY, WAIT ON INTERRUPT.
02              ;       MAX # OF SECTORS = ALL OF AVAILABLE CORE (IE NOT TAKEN
03              ;       BY PROGRAM) OR 32 SECTORS MAX. USED FOR SA'S 503,506,507
04
05              ;       2-BACKGROUND/FOREGROUND MODES, 2 BUFFERS USED FOR
06              ;       BOTH READ AND WRITE PURPOSES. MAX # OF SECTORS
07              ;       = 1/2 OF AVAILABLE CORE OR 32 SECTORS MAX. USED
08              ;       FOR CONSTANT DATA PATTERNS.
09
10              ;       3.-BACKGROUND/FOREGROUND MODES, 4 BUFFERS ( 2 FOR READ
11              ;       AND 2 FOR WRITE). MAX # OF SECTORS =1/3 OF AVAILABLE
12              ;       CORE OR 32. MAX. USED FOR VARIABLE DATA(EXPECT ADR).
13
14              ;       4. -IF THE ECLIPSE OR NOVA-3 MAPS ARE IN THE SYSTEM,
15              ;       AND MAPPING IS REQUESTED, ONE OF TWO MAPPING SCHEMES
16              ;       WILL BE IN EFFECT
17
18              ;       4.1 THE 1ST N PHYSICAL 1K BLOCKS CONTAINING THE PROGRAM
19              ;       WILL BE MAPPED TO THE 1ST N 1K LOGICAL BLOCKS IN BOTH
20              ;       THE A AND B USER MAPS. THIS MAPPING WILL REMAIN
21              ;       CONSTANT.  A 25. K PHYSICAL BLOCK WITH THE
22              ;       START 1K DESIGNATED BY THE PROGRAM VARIABLE MPB?N
23              ;       WILL BE ALLOCATED TO THE DISK I/O BUFFER AS FOLLOWS:
24
25              ;       THE 25K I/O BUFFER IS DIVIDED INTO 3 NON-CONTIGUOUS
26              ;       BUFFERS, 9K OF COMMON(TO BOTH THE A AND B I/O BLOCKS)
27              ;       WRITE BUFFER(WAB), 8K OF READ BUFFER ALLOCATED TO THE
28              ;       A-I/O BLOCK(RA) VIA THE A USER MAP, AND 8K
29              ;       OF READ BUFFER ALLOCATED TO THE B-I/O BLOCK(RB) VIA
30              ;       THE B USER MAP.  THE 1K BLOCKS OF THE 3 BUFFERS ARE
31              ;       INTERLEAVED IN THE PHYSICAL SPACE IN THE FOLLOWING
32                      MANNER:
33
34              ;       WAB1,RA1,RB1,WAB2,RA2,RB2,WAB3 ETC.
35
36              ;       4.2 THE 25K PHYSICAL I/O BUFFER IS MAPPED TO THE
37              ;       1ST 25K LOGICAL IN THE DCH MAP.  DISPLACEMENT VALUES
38              ;       H.DBW,2 AND H.DBR,2 ARE ADDED TO THE USER LOGICAL
39              ;       ADDRESSES WHEN LOADING THE DCH MEMORY ADDRESS REGISTER.
40
41
42              ;10.0   PROGRAM OUTPUT/ERROR DESCRIPTION:
43
44
45              ;       ALL ERRORS ARE IDENTIFIED, COUNTED, AND THE
46              ;       PROGRAM IS ROUTED VIA BASE TO A CALL TO CKSW
47              ;       ON THE BASIS OF SWITCH SETTINGS (SEE 8.2) THE
48              ;       PROGRAM WILL GO INTO A SCOPE LOOP, OR PROCEED,
49              ;       DEPENDING ON THE SWPAK SETTINGS.
50
51              ;       UPON LOSS OF READY AND A SINGLE DRIVE, THE PROGRAM
52              ;       WILL PRINT THE APPROPRIATE ERROR MESSAGE AND WILL NOT
53              ;       PROCEED UNTIL READY IS RETURNED.  IF MULTIPLE
54              ;       DRIVES EXIST, THE PROGRAM WILL CONTINUE WITH THE
55              ;       REMAINING DRIVES.  IF THE DOWN DRIVE IS PLACED BACK
56              ;       ONLINE, THE PROGRAM WILL RESUME TESTING OF
57              ;       THAT DRIVE.  THE ABOVE ALSO APPLIES TO THE LOSS
58              ;       OF WRITE ENABLE IF THE PROGRAM IS IN A WRITE MODE.
59
60              ;       RECALIBRATE - ANY UNUSUAL STATUS IS REPORTED
```

```
0008  DISKR
01              ;       IMMEDIATELY AND AN ERROR RETURN EXECUTED.
02
03              ;10.1   SEEK - POSITIONER FAULT STATUS INCREMENTS SEEK
04              ;       ERROR COUNTER.  ANY ERROR STATUS  RESULTS
05              ;       IN STATUS PRINTOUT AND ERROR RETURN.
06              ;       A RECALIBRATE WILL BE PERFORMED BY THE ERROR HANDLER.
07              ;       PROGRAM WILL LOG THE FIRST 20. CYLINDERS
08              ;       TO/FROM ON FINDING SEEK ERRORS
09
10              ;10.2   WRITE - FOLLOWING "DONE" ON A WRITE, ERRORS ARE
11              ,       CHECKED IN THE SEQUENCE SHOWN BELOW.  ERROR
12              ;       RECOVERY PROCEEDURE IS OUTLINED FOR EACH CASE.
13              ;       IF THE ERROR IS NOT PRESENT THE NEXT CHECK IS MADE.
14
15              ,       DRIVE STATUS (DIB) IS CHECKED 1ST FOR BOTH READ AND
16              ,       WRITE BEFORE ANY DIA CHECKS ARE MADE
17
18              ,       1. READ/WRITE TIMEOUTS, DATA LATE, ILLEGAL SECTOR,
19              ,       PARITY,DATA VERIFY, OR ANY DRIVE FAULTS- INCREMENT THE
20              ,       APPROPRIATE ERROR COUNT, PRINT THE ILLEGAL STATUS
21              ;       AND DO AN ERROR RETURN.  ANY DRIVE FAULT WILL CAUSE
22              ,       A RECALIBRATE TO BE PERFORMED BY THE ERROR HANDLER.
23
24              ,       2. ADDRESS ERROR- REPEAT THE WRITE, IF TEST PASSES
25              ,       THE SECOND TIME, INCREMENT THE SOFT ADDRESS ERROR
26              ;       COUNT AND DO A NORMAL RETURN; OTHERWISE INCREMENT
27              ,       THE HARD ADDRESS ERROR COUNT AND DO AN ERROR RETURN
28
29              ,       IF A HARD CYLINDER ADDRESS ERROR OCCURS, A READ
30              ;       ON AN ADJACENT HEAD WILL BE ATTEMPTED TO DETERMINE
31              ,       WHETHER THE FAULT SHOULD BE CLASSED AS A SEEK ERROR
32              ,       OR AN ADDRESS ERROR.THE FIRST 20  ADDRESS
33              ,       ERRORS WILL HAVE THEIR ADDRESSES LOGGED.
34
35              ,       3. BAD SECTOR- LOG THE DISK ADDRESS (1ST 100.) AND DO
36              ,       A NORMAL RETURN.  NO PRINTOUT WILL RESULT UNLESS SW11=1,
37              ,       ALTHOUGH THE I/O OPERATION WAS PREMATURELY TERMINATED.
38              ,       A "SOFT" ERROR WILL BE RECORDED IF THE SECTOR UNDER
39              ,       TEST PASSES AT LEAST 1 OF 4 RETRYS.  THE LOG DENOTES
40              ,       SOFT ERRORS BY A COUNT GREATER THAN 0, REPRESENTING
41              ;       THE ERROR COUNT TALLIED.
42              ,       ***SEE 10.3A.
43
44              ;       4. ENDING MEMORY ADDRESS - INCREMENT THE MEMORY ADDRESS
45              ;       ERROR COUNT, PRINT THE ERROR MESSAGE, CHECK FOR A
46              ;       DISK ADDRESS ERROR AND DO AN ERROR RETURN
47
48              ;       5. ENDING DISK ADDRESS - INCREMENT THE DISK ADDRESS
49              ;       ERROR COUNT, PRINT THE ERROR MESSAGE, AND
50              ;       DO AN ERROR RETURN
51
52
53              ,10.3   READ - ALL READ ERRORS WITH THE EXCEPTION OF DATA RELATED
54              ;       ERRORS ARE HANDLED THE SAME AS DESCRIBED FOR THE WRITE
55              ,       OPERATIONS
56
57              ;       DATA ERRORS - DATA IS REREAD 3 X (4X IF ECC UNDETECTED)
58              ;       IF PROGRAM IS IN WRITE/READ MODE AND DATA IS BAD ALL
59              ;       4 TRIES, A HARD ERROR COUNT IS INCREMENTED AND AN
60              ;       ERROR RETURN IS TAKEN.  IF DATA IS GOOD ON ANY OF FOUR
```

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0009 DISKR
01              ;       TRIES, A SOFT ERROR COUNT IS INCREMENTED AND A
02              ;       NORMAL RETURN IS TAKEN
03
04              ;       IF THE PROGRAM IS IN A READ ONLY MODE (IE. READ MODE
05              ;       FOR ANY 502 PROGRAM OR WHEN 505 IS RUNNING A 502
06              ;       PROGRAM), THE DATA WILL BE REREAD AN ADDITIONAL
07              ;       4 TIMES IN BOTH OFFSET FORWARD AND OFFSET REVERSE
08              ;       MODES BEFORE THE PROBLEM IS CLASSED AS A HARD ERROR
09
10              ;       THUS TOTAL RETRIES FOR A HARD ECC DETECTED ERROR IN
11              ;       A READ ONLY MODE IS 12 (13 FOR ECC UNDETECTED), AND
12              ;       4 IF IN A WRITE/READ MODE (5 IF ECC UNDETECTED).
13              ;       ***SEE 10.3A
14
15              ;       ANY SUCCESSFUL REREADS WHILE IN AN OFFSET MODE
16              ;       WILL BE PRINTED AND LOGGED.  THE DISK ADDRESSES
17              ;       OF ALL DATA PROBLEMS WILL BE PRINTED AND THE FIRST
18              ;       100. WILL BE LOGGED.  THE FIRST THREE GOOD/BAD
19              ;       WORD PAIRS AND RESPECTIVE ADDRESSES WILL BE PRINTED.
20
21              ;       IF SWPAK9=1 (BYPASS DATA CHECK) HARD OR SOFT DATA
22              ;       ERRORS WILL BE DETERMINED BY ECC STATUS.
23
24              ;10.3A  ECC (ERROR CORRECTION CODE) ANALYSIS
25
26              ;       ALL READ PASSES INCLUDING RETRIES WILL HAVE THE ECC
27              ;       RESULTS LOGGED AS PER THE FOLLOWING 4 CATEGORIES:
28
29              ;       1.  ECC CORRECTED -THE ECC DETECTED AND SUCCESSFULLY
30              ;       CORRECTED THE DATA ERROR.
31
32              ;       2.  NON-CORRECTABLE ECC -THE ECC DETECTED AND CORRECTLY
33              ;       DIAGNOSED THE ERROR PATTERN AS UNCORRECTABLE.
34
35              ;       3.  ECC UNDETECTED -THE ECC FAILED TO DETECT A DATA ERROR.
36              ;       THIS MAY BE A MALFUNCTION OF THE ECC LOGIC, BUT IT IS
37              ;       MORE LIKELY ONE OF THE FOLLOWING PROBLEMS:
38
39              ;       A FAILURE OF THE DRIVE TO WRITE A SECTOR.
40              ;       ***NOTE- A CHECK SHOULD BE MADE IN THE BAD SECTOR
41              ;       LOG TO SEE WHETHER A WRITE OPERATION MAY HAVE
42              ;       ENCOUNTERED A SOFT OR FAULTY BAD SECTOR INDICATION,
43              ;       WHICH WOULD HAVE TERMINATED THE WRITE.
44
45              ;       A FAILURE IN THE CONTROLLER DATA PATHS.
46              ;       4.  ECC FAILED -TWO CONDITIONS MAY FALL INTO THIS CATEGORY.
47
48              ;       4A.  AN ECC ERROR WAS DETECTED BUT WITH NO ACCOMPANYING
49              ;       DATA ERROR.  A CHECK IS MADE TO SEE WHETHER THE ECC
50              ;       WORDS POINT TO AN ERROR WITHIN THE TWO APPENDED
51              ;       WRITE ECC WORDS.  IF SUCH AN ERROR IS
52              ;       DETERMINED TO BE THE CASE, THE ERROR WILL BE LOGGED AS
53              ;       CORRECTABLE AND NO ECC FAILED MESSAGE WILL RESULT.
54              ;       THIS TYPE OF ERROR SHOULD REPRESENT ONLY A VERY SMALL
55              ;       PERCENTAGE OF THE DATA ERRORS (<1%- LARGE SAMPLE).  IF
56              ;       A SIGNIFICANTLY HIGHER PERCENTAGE OF THIS ERROR RESULTS,
57              ;       THEN AN ECC PROBLEM WOULD BE INDICATED.
58
59              ;       IF THE ECC DOES NOT POINT TO THE TWO APPENDED WRITE ECC
60              ;       WORDS, THEN AN ECC FAILED MESSAGE (1ST PASS ONLY) WILL
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```
01              ;       RESULT AND THE ACTUAL ECC WORDS READ FROM THE CONTROLLER
02              ;       WILL BE PRINTED.
03              ;
04              ;       48. AN ECC ERROR WAS DETECTED, BUT THE ECC EITHER FAILED
05              ;       TO CORRECT A CORRECTABLE ERROR, OR TRIED TO CORRECT AN
06              ;       UNCORRECTABLE ERROR.  THESE CONDITIONS (POSSIBLY CAUSED
07              ;       BY PROBLEMS OTHER THAN ECC) WILL RESULT IN A PRINTOUT
08              ;       (1ST PASS ONLY) OF THE SIMULATED WRITE AND SIMULATED
09              ;       READ ECC WORDS PLUS THE ACTUAL READ ECC WORDS AS READ
10              ;       FROM THE CONTROLLER.
11              ;
12              ;       THE SIMULATED WRITE ECC WORDS ARE THE RESULT OF A
13              ;       PROGRAM SIMULATION OF THE ECC LOGIC ON WHAT THE PROGRAM
14              ;       BELIEVES TO BE THE WRITE DATA (A WRITE ERROR WILL CAUSE
15              ;       THIS ASSUMPTION TO BE FALSE), AND REPRESENTS WHAT THE
16              ;       PROGRAM BELIEVES SHOULD HAVE BEEN WRITTEN AS THE ACTUAL
17              ;       TWO WRITE ECC WORDS ON THE DISK.
18              ;
19              ;       THE SIMULATED READ ECC WORDS ARE THE RESULT OF ANOTHER
20              ;       PROGRAM SIMULATION OF THE ECC LOGIC ON THE READ DATA
21              ;       IN MEMORY, AND REPRESENT WHAT THE PROGRAM BELIEVES
22              ;       SHOULD BE READ FROM THE CONTROLLER AS THE TWO ECC
23              ;       WORDS.  THE ACTUAL READ ECC WORDS ARE THOSE TWO WORDS
24              ;       AS READ FORM THE DISK CONTROLLER.
25              ;
26              ;10.4   ERRORS- ERROR STATUS IS PRINTED WHENEVER ENCOUNTERED
27              ;       AS FOLLOWS
28              ;
29              ;       'MODE'  UNIT:   'N'
30              ;       CYL-- 'N'       HEAD  'N'       SECT  'N'    #SECT  'N'
31              ;       DIA/DIB STATUS= 'N'     'DESCRIPTIVE MESSAGE'
32              ;
33              ;       WHERE CYL,HEAD,SECT REFER TO THE FINAL DISK ADDRESS AT
34              ;       THE POINT OF ERROR, AND #SECT REFERS TO THE NUMBER OF
35              ;       SECTORS ALREADY DONE IN THE MULTIPLE SECTOR TRANSFER.
36              ;
37              ;       WHEN DATA ERRORS ARE FOUND, ONLY THREE ARE PRINTED PER
38              ;       ENCOUNTER PLUS THE TOTAL NUMBER OF ERRORS. (SEE PARA 5)
39              ;       IF THE DATA ERROR IS ECC UNDETECTED AND THE SYSTEM IS
40              ;       MAPPED, THE MAP, PHYSICAL 1K ADDRESS, AND THE DCH
41              ;       LOGICAL ADDRESSES ARE ALSO PRINTED.
42              ;
43              ;       WHEN LOOPING IS INVOLVED (RETRIES OR FOR SCOPING)
44              ;       STATUS IS PRINTED ON THE 1ST PASS ONLY.
45              ;
46              ;10.5   STATISTICS - TYPE A W
47              ;       DURING RANDOM TESTING TO GET A REPORT OF THE
48              ;       NUMBER OF SECTORS WRITTEN(AND/OR)READ, PLUS
49              ;       ERROR COUNTS IN DECIMAL.  ALSO LISTED IS A
50              ;       COUNT FOR CONTROLLER CORRECTS/UNIT
51              ;       (ON BOARD ECC CORRECTION AND OFFSET CORRECTS)
52              ;
53              ;       TYPE L FOR FIRST 100. DISK ADDRESSES OF BAD SECTORS AND
54              ;       DATA ERRORS, AND FIRST 20. OF ADDRESS ERRORS AND
55              ;       SEEK ERRORS (SEEK PATH).  IF ERROR ADDRESSES ARE
56              ;       ENCOUNTERED MORE THAN ONCE (1ST PASS), A COUNT OF UP TO
57              ;       32. WILL BE RECORDED IN THE LOG.  ALSO A COUNT OF UP TO
58              ;       15. HARD ERRORS WILL BE RECORDED.  THIS COUNT WILL BE
59              ;       A SUBSET OF THE THE FIRST COUNT.
60
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0011  DISKR
01              ;       THE ADDRESS INFORMATION WILL BE IN OCTAL WHILE THE
02              ;       COUNTS WILL BE DECIMAL.
03
04              ;       TYPE S FOR SEEK TIMING STATISTICS IF RUNNING
05              ;       EITHER SEEK EXERCISER.
06
07              ;       **** NOTE ****
08              ;       THE PROGRAM WILL ACCOUNT FOR UP TO A MAX.
09              ;       OF 2**31 SECTORS WRITTEN OR READ.  SPECIAL
10              ;       TEST RUNS EXCEEDING THIS FACILITY WILL
11              ;       REQUIRE AN OPERATOR'S TEST LOG TO AUGMENT
12              ;       SOFTWARE ACCOUNTING.  2**31 SECTORS =
13              ;       APPROX.  5.5* 10**11 WORDS.
14              ;11.0   DEBUG HELP
15                      O?DTD   11
16
17              ;12.0   SPECIAL NOTES/SPECIAL FEATURES:
18
19              ;       1.  A CR ONLY RESPONSE TO UNIT NUMBERS, WILL LEAVE
20              ;       UNIT/CYLINDER INFORMATION IN PREVIOUS STATE.
21
22              ;       2.  THE PROGRAM USES A 10 WORD BUFFER.
23
24              ;       3.  THE PROGRAM WILL ACCOUNT FOR UP TO A MAX.
25              ;       OF 2**31 SECTORS WRITTEN OR READ.  SPECIAL
26              ;       TEST RUNS EXCEEDING THIS FACILITY WILL
27              ;       REQUIRE AN OPERATOR'S TEST LOG TO AUGMENT
28              ;       SOFTWARE ACCOUNTING.  2**31 SECTORS =
29              ;       APPROX.  5.5* 10**11 WORDS.
30
31              ;       4.  SWPAK7=1, PROGRAM HALTS AFTER WRITE WITH READ
32              ;       VERIFICATION ALLOWING OPERATOR TO CHANGE PACKS.
33              ;       SWPAK8=1, PUTS PROGRAM INTO READ ONLY MODE
34              ;       ## SA'S 501,502 ONLY.  IF SA 501-DATA MUST !NOT! BE
35              ;       VARIABLE.  START AT THE ABOVE SELECTED ADDRESS.
36
37              ;       5.  ALL NUMBERS ENTERED IN 7.0 MUST BE IN OCTAL.
38              ;       ANY NON-OCTAL INPUT IS TREATED AS A LETTER.
39              ;       ANY LETTER INPUT FOR CYL,HEAD,SECTOR, OR # OF
40              ;       SECTORS GETS RANDOM FUNCTION IN THE RELIABILITY
41              ;       TEST WITH OPTIONS.
42
43              ;       6.  AT TIMES THE ECC MAY ATTEMPT TO CORRECT A NON-CORRECTABLE
44              ;       DATA ERROR AND THE SIMULATED ECC AND ACTUAL ECC WILL
45              ;       MATCH EVEN THOUGH AN ECC FAILURE WILL HAVE BEEN PRINTED.
46              ;       THIS IS DUE TO A FAILURE OF THE ECC POLYNOMIAL ITSELF TO
47              ;       DISTINGUISH BETWEEN TWO DIFFERENT ERROR PATTERNS, ONE
48              ;       CORRECTABLE AND ONE UNCORRECTABLE.  THIS IS !NOT! A
49              ;       HARDWARE FAILURE.
50
51              ;13.0   PROGRAM RUNTIME:
52
53              ;       PROGRAM RUNTIMES ARE SUBSTANTIALLY REDUCED WITH
54              ;       MEMORIES OF 16K OR LARGER.  PROGRAM CAN USE UP TO
55              ;       24K USING 2 BUFFERS AND UP TO 32K USING 4 BUFFERS
56              ;       IN THE RANDOM RELIABILITY TESTS.  ## SEE 9D
57
58              ;       READ, WRITE AND SEEK OPERATIONS ARE TIMED
59              ;       BY SPECIAL ROUTINES.  WHEN THE PROGRAM IS
60              ;       FIRST STARTED, THE TIMING ROUTINE WILL TEST
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0012  DISKR
01              ;       FOR THE PRESENCE OF A REAL TIME CLOCK (RTC)
02              ;       TO DERIVE TIMING FROM IT.
03
```
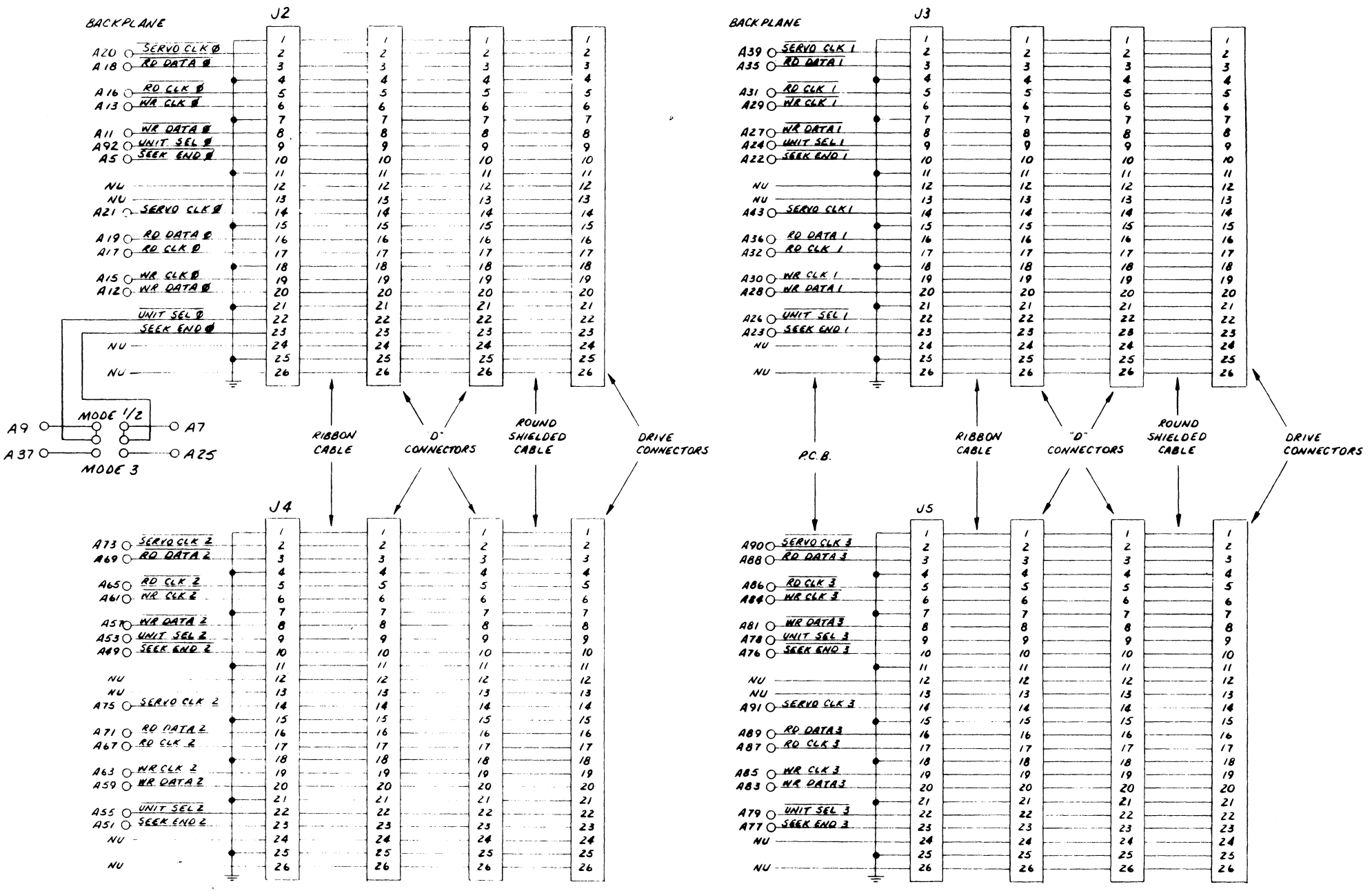



REVISION HISTORY		
ECO	DATE	DESCRIPTION

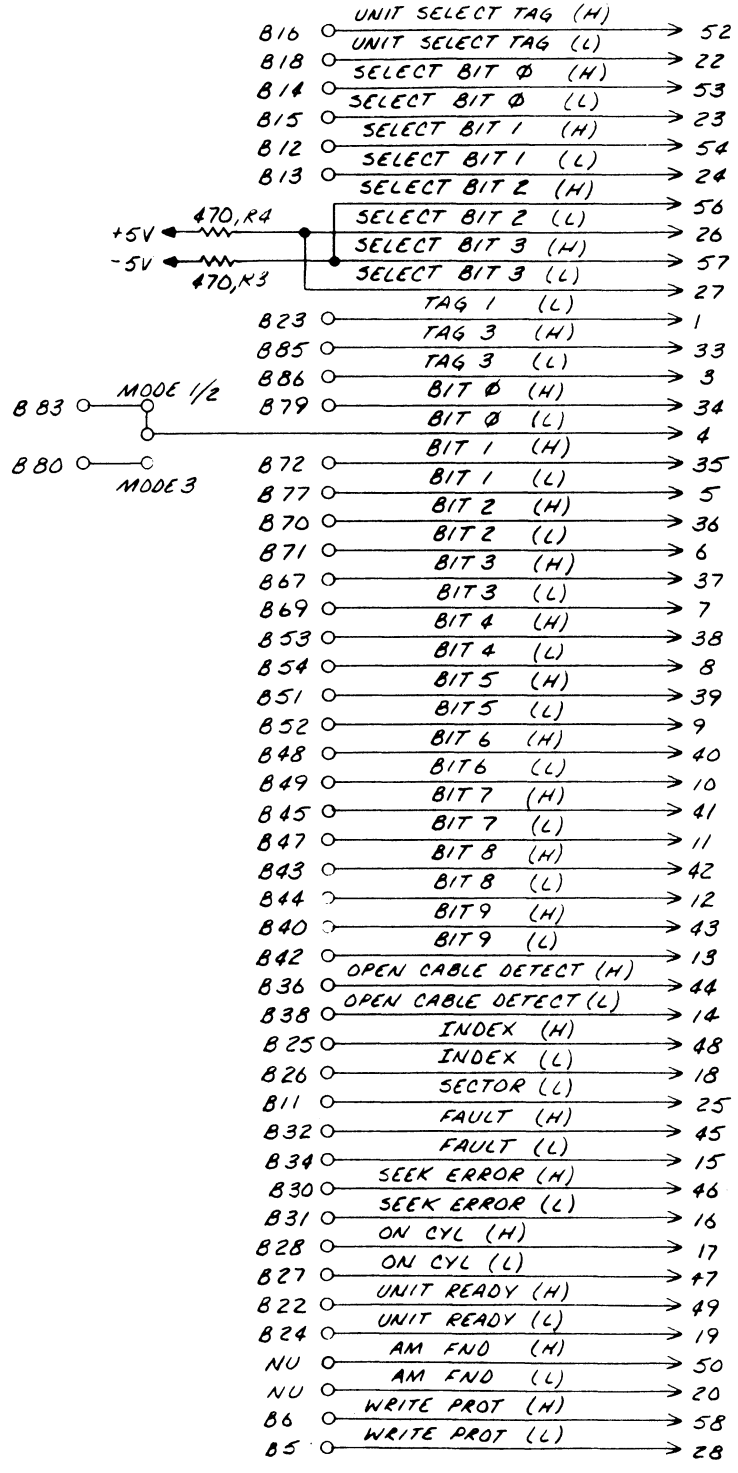
ZETACO

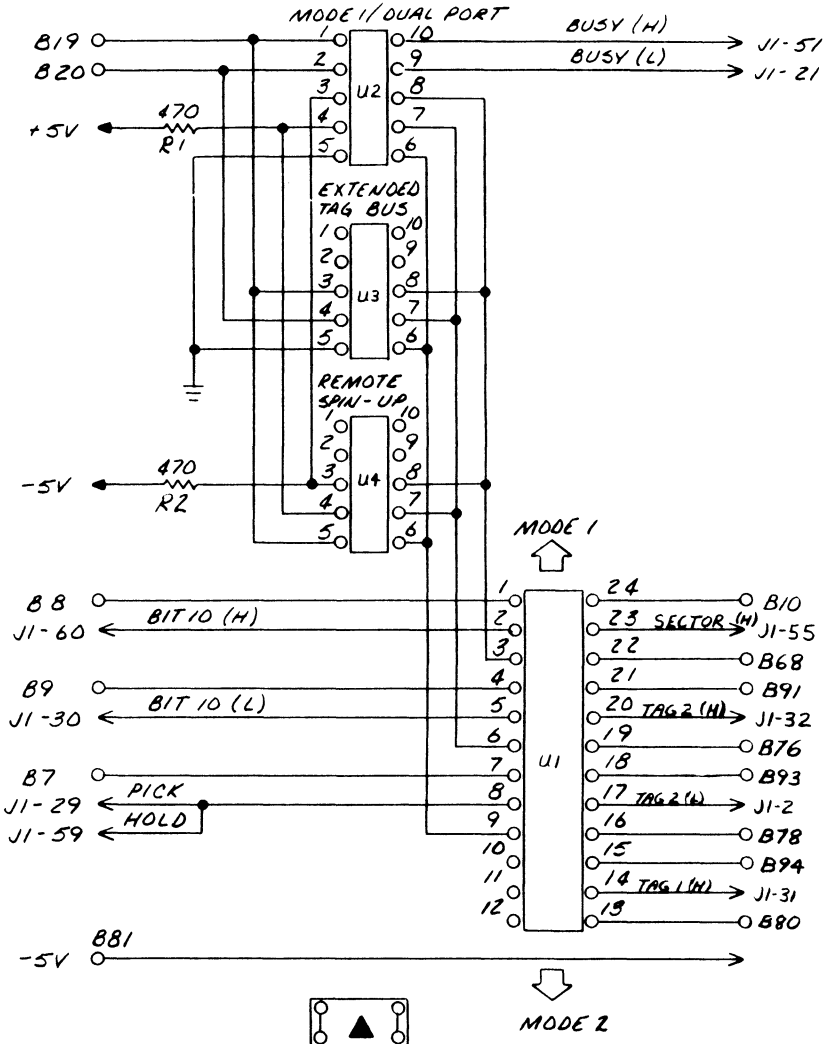
DRAWN BY	DATE	REVISIONS	ZONE
CJK	7-22-86		
CHECKED BY			
APPROVED BY			
SCALE: ~			

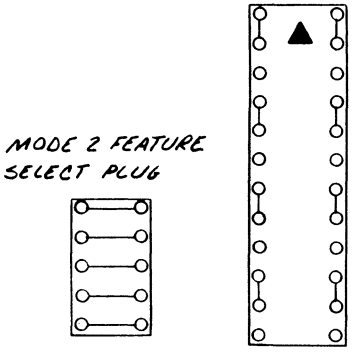
TITLE: BMX-1 "A" PADDLE BOARD

DRAWING NUMBER: 700276-000 A

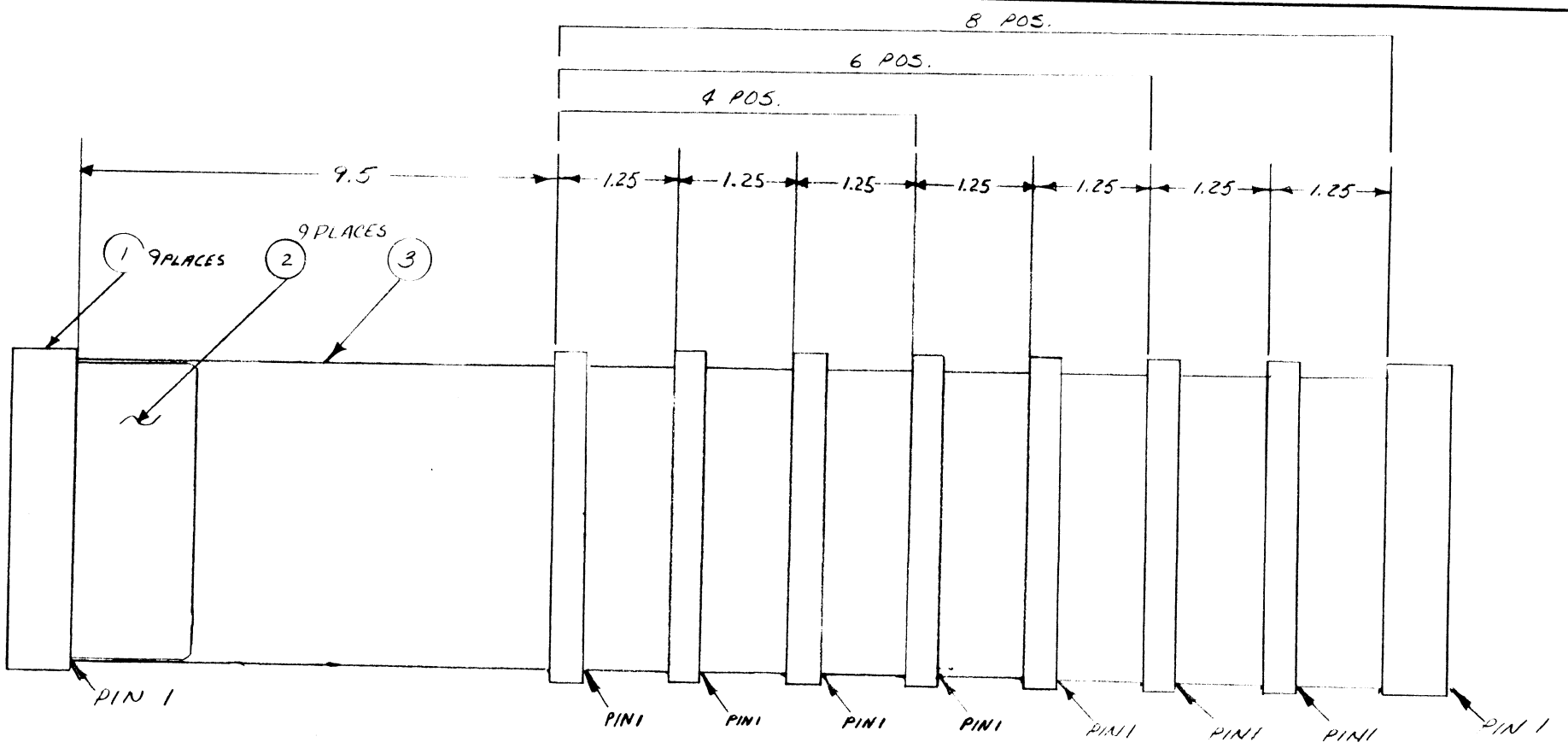
4021 TB







ZETACO			
DRAWN BY CJK	DATE 8-7-84	REVISIONS	DATE
CHECKED BY			
APPROVED BY			
SCALE: ~			
TITLE BMX-1 "B" PADDLE BOARD		DRAWING NUMBER 700276-000	A



(1) 3M 40 PIN CONNECTORS 3417-6040 (SEE OPTION TABLE)

(2) 3M PULL TABS 3490-4 (SEE OPTION TABLE)

(3) 3M 40 PIN FLAT CABLE 3302/40

4. STAMP CABLE WITH APPROPRIATE PART NUMBER & LENGTH

5 TOLERANCE ON CABLE LENGTH ±.50

REVISION HISTORY		
ECO #	DATE	DESCRIPTION
0446	7-21-85	PER ECO

OPTION TABLE	
OPTION	POSITION
001	4 POS.
002	6 POS.
003	8 POS.

ZETACO

DRAWN BY	DATE	REVISIONS	DATE
A Jamison	12-7-83		
CHECKED BY			
APPROVED BY			
SCALE:		276-C30-2E0-00	

TITLE: BMX BUS CABLE

DRAWING NUMBER: 100 110-000 8

PARTS LIST
ZETA30

ER: INTERNAL "A" CABLE ASSY

ASSEMBLY #: 300000-000
PREV. ASSEMBLY #: 286-014-2R0
REV. LEVEL: A
SCHEMATIC REV. LEVEL:

ITEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION	REFERENCE
1	1	100503-000	HEX SETS	D20418-2 MNT HARDWARE SET	
2	1	100528-000	CONN D-SUB	AMP 204508-3	
3	60	100639-000	CONN PIN	AMP 66717-5	
4	0	100705-000	LABEL	5552 AVERY	

WIRE LIST

NOTES	WIRE GAUGE	COLOR	ORIGIN	TERM. METHOD	DESTINATION	TERM. METHOD	REMARKS
WP	28	BRN	P1-1	MASS	P2-1	3	
		TAN			P2-2		
		RED			P2-3		
		TAN			P2-4		
		ORG			P2-5		
		TAN			P2-6		
		YEL			P2-7		
		TAN			P2-8		
		GRN			P2-9		
		TAN			P2-10		
		BLU			P2-11		
		TAN			P2-12		
		VIO			P2-13		
		TAN			P2-14		
		GRY			P2-15		
		TAN			P2-16		
		WHT			P2-17		
		TAN			P2-18		
		BLK			P2-19		
		TAN			P2-20		
		BRN			P2-21		
		TAN			P2-22		
		RED			P2-23		
		TAN			P2-24		
		ORG			P2-25		
		TAN			P2-26		
		YEL			P2-27		
		TAN			P2-28		
		GRN			P2-29		
WP	28	TAN	P1-45	MASS	P2-30	3	

ZETACO

TITLE
INTERNAL "A" CABLE ASSEMBLY

DOCUMENT NO. 300000-000 A

SHEET 2 **OF** 4 **REV.** 286-C14-2R0-00

WIRE LIST

NOTES	WIRE GAUGE	COLOR	ORIGIN	TERM. METHOD	DESTINATION	TERM. METHOD	REMARKS
TWP	28	BLU	P1-16	MASS	P2-31	3	
		TAN			P2-32		
		VIO			P2-33		
		TAN			P2-34		
		GRY			P2-35		
		TAN			P2-36		
		WHT			P2-37		
		TAN			P2-38		
		BLK			P2-39		
		TAN			P2-40		
		BRN			P2-41		
		TAN			P2-42		
		RED			P2-43		
		TAN			P2-44		
		ORG			P2-45		
		TAN			P2-46		
		YEL			P2-47		
		TAN			P2-48		
		GRN			P2-49		
		TAN			P2-50		
		BLU			P2-51		
		TAN			P2-52		
		VIO			P2-53		
		TAN			P2-54		
		GRY			P2-55		
		TAN			P2-56		
		WHT			P2-57		
		TAN			P2-58		
		BLK			P2-59		
TWP	28	TAN	P1-60	MASS	P2-60	3	

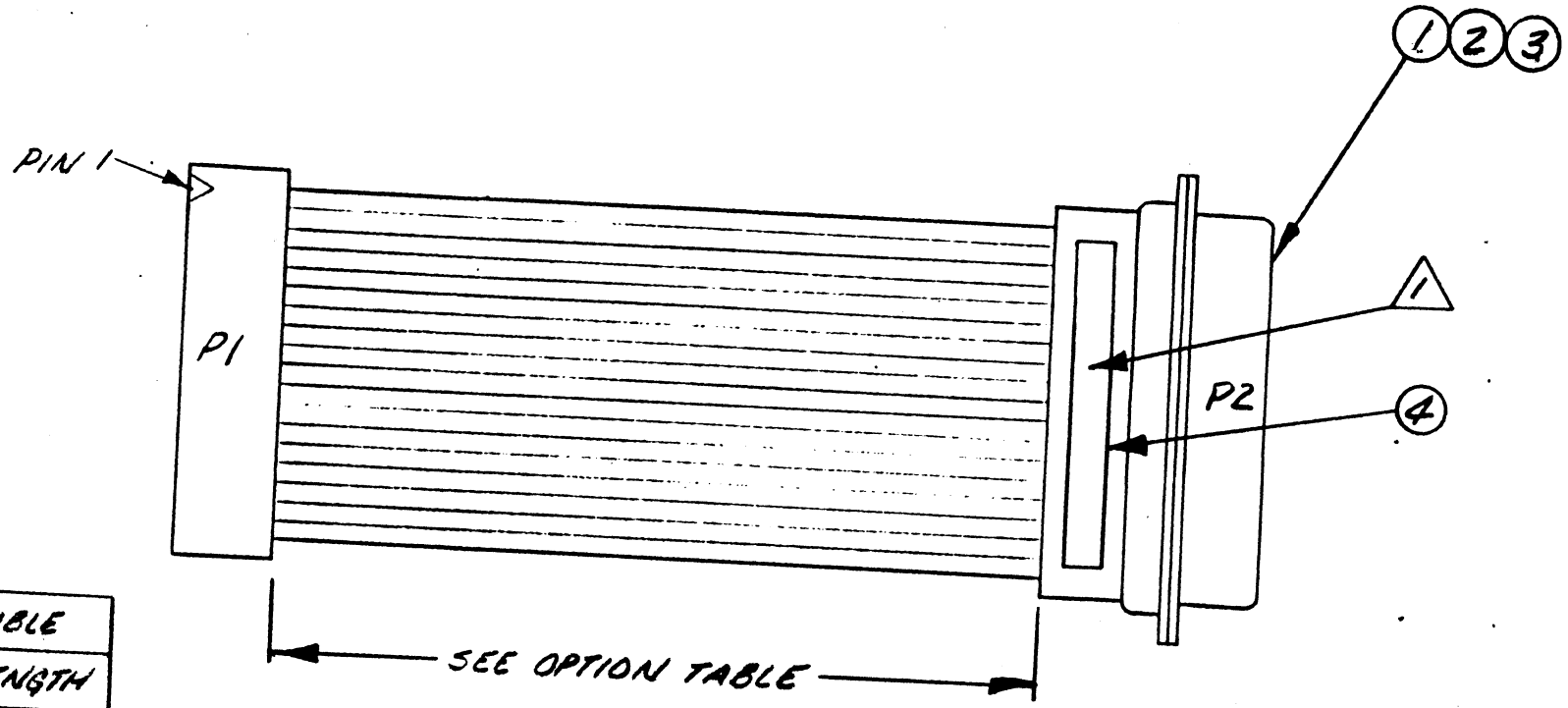
ZETACO

TITLE

INTERNAL "A" CABLE ASSEMBLY

DOCUMENT NO. 300000-000 A

SHEET 3 **OF** 4 **REV.** 286-C14-2R0-00



OPTION TABLE	
OPTION	LENGTH
001	18"
002	36"

REVISION HISTORY		
ECO	DATE	DESCRIPTION

⚠1 STAMP WITH APPROPRIATE PART NUMBER & LENGTH

ZETACO

DRAWN BY C RULSETH	DATE 9-28-83	REVISIONS	DATE
CHECKED BY			
APPROVED BY		286-C14-ZRO-00	
SCALE: ~		500376-000	
TITLE: INTERNAL "A" CABLE ASS'Y		DRAWING NUMBER 300000-000 A	

PARTS LIST
ZETACO

JR: INTERNAL "A" CABLE ASSY (18")

ASSEMBLY #: 300000-001
PREV. ASSEMBLY #:
REV. LEVEL: A
SCHEMATIC REV. LEVEL:

ITEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION		REFERENCE
1	18	100909-001	CABLE	INTERNAL "A" 18"	REV A	

PARTS LIST
ZETACO

JR: EXTERNAL "B" CABLE ASSEMBLY

ASSEMBLY #: 300011-000
PREV. ASSEMBLY #: 287-C07-2S2
REV. LEVEL: A
SCHEMATIC REV. LEVEL:

ITEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION	REFERENCE
1	1	100517-000	FERRULE IN	1-745129-7	
2	1	100519-000	FERRULE-O	1-745130-0	
3	1	100545-000	CONN D-SUB	AMP 50P PLUG 205212-3	
4	1	100555-000	CONN HDWE	AMP 205980-1	
5	1	100566-007	CONN HOOD	AMP 1-747098-7 30PR CABLE	
6	26	100638-000	CONN PIN	AMP 66507-2	
7	0	100705-000	LABEL	5552 AVERY	

WIRE LIST

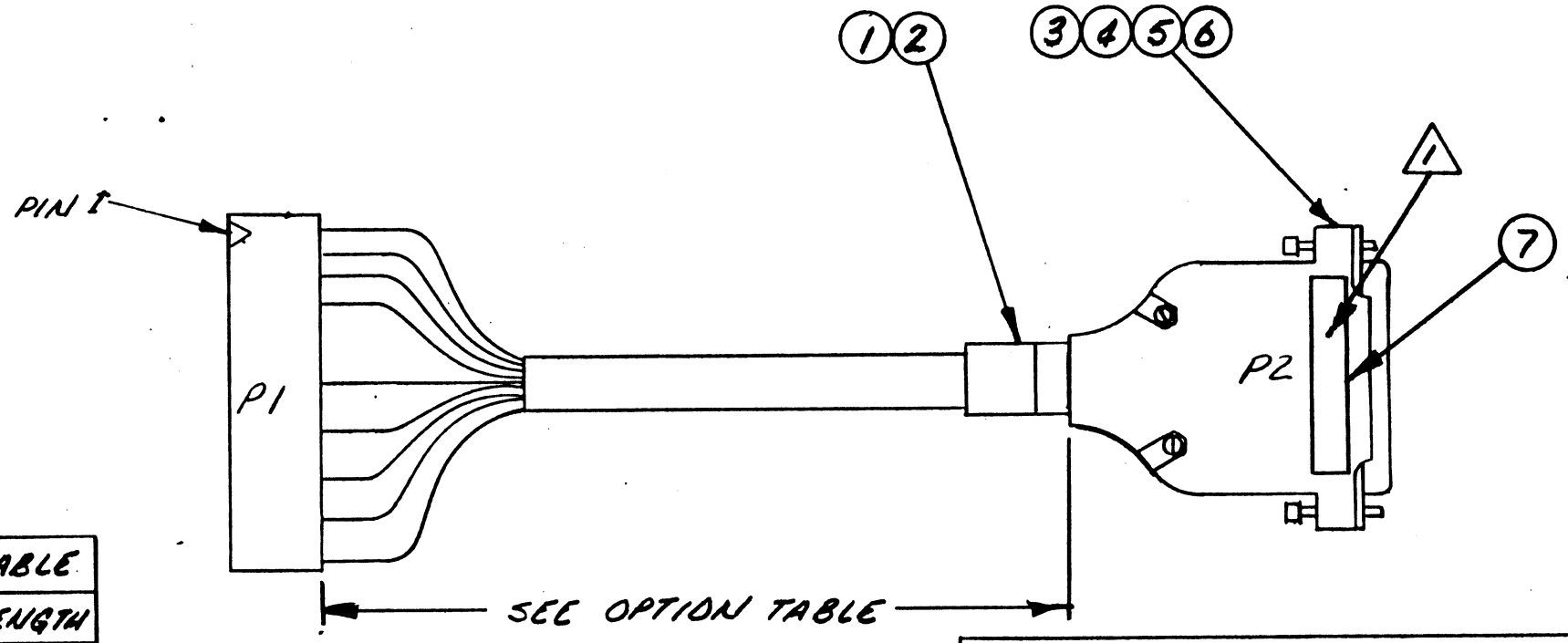
NOTES	WIRE GAUGE	COLOR	ORIGIN	TERM. METHOD	DESTINATION	TERM. METHOD	REMARKS
TWP		BRN	P1-1	MASS	P2-1	3	
		BLK			P2-2		
		RED			P2-3		
		BLK			P2-4		
		ORG			P2-5		
		BLK			P2-6		
		YEL			P2-7		
		BLK			P2-8		
		GRN			P2-9		
		BLK			P2-10		
		BLU			P2-11		
		BLK			P2-12		
		VIO			P2-13		
		BLK			P2-14		
		GRY			P2-15		
		BLK			P2-16		
		WHT			P2-17		
		BLK			P2-18		
		RED			P2-19		
		BRN			P2-20		
		ORG			P2-21		
		BRN			P2-22		
		YEL			P2-23		
		BRN			P2-24		
		GRN			P2-25		
TWP		BRN	P1-26	MASS	P2-26	3	

ZETACO

TITLE
EXTERNAL "B" CABLE ASSEMBLY

DOCUMENT NO. 300011-000 A

SHEET 2 **OF** 3 **REV.** 287-C07-2S2-00



OPTION TABLE	
OPTION	LENGTH
001	6'
002	16'

1 STAMP WITH APPROPRIATE PART NUMBER & LENGTH

REVISION HISTORY		
ECO	DATE	DESCRIPTION
0274	3/20/84	PER ECO
0286	4-12-84	PER ECO

ZETACO

DRAWN BY	DATE	REVISIONS	DATE
T RULSETH	9-28-83		
CHECKED BY			
APPROVED BY			
SCALE: ~		287-C07-252-00	

TITLE: EXTERNAL "B" CABLE ASS'Y

DRAWING NUMBER: 30001A-000 A

SHEET 3 OF 3

PARTS LIST
ZETACO

OR: EXTERNAL "B" CABLE ASSEMBLY (16')

ASSEMBLY #: 300011-002
PREV. ASSEMBLY #:
REV. LEVEL: A
SCHEMATIC REV. LEVEL:

ITEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION	REFERENCE
1	192	100917-002	CABLE	EXTERNAL "B" 16' REV A	

PARTS LIST
ZETACO

JR: EXTERNAL "A" CABLE ASSEMBLY

ASSEMBLY #: 300013-000
PREV. ASSEMBLY #: 286-C10-2R1
REV. LEVEL: A
SCHEMATIC REV. LEVEL:

TEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION	REFERENCE
1	1	100518-000	FERRULE IN	2-745129-3	
2	1	100520-000	FERRULE-O	1-745130-1	
3	1	100529-000	CONN D-SUB	AMP 204509-3	
4	1	100555-000	CONN HDWE	AMP 205980-1	
5	1	100566-007	CONN HOOD	AMP 1-747098-7 30PR CABLE	
6	60	100640-000	CONN PIN	AMP 66718-5	
7	0	100705-000	LABEL	5552 AVERY	

WIRE LIST

NOTES	WIRE GAUGE	COLOR	ORIGIN	TERM. METHOD	DESTINATION	TERM. METHOD	REMARKS
TWP		BRN	P1	MASS	P2-1	3	
		BLK			P2-2		
		RED			P2-3		
		BLK			P2-4		
		ORG			P2-5		
		BLK			P2-6		
		YEL			P2-7		
		BLK			P2-8		
		GRN			P2-9		
		BLK			P2-10		
		BLU			P2-11		
		BLK			P2-12		
		VIO			P2-13		
		BLK			P2-14		
		GRY			P2-15		
		BLK			P2-16		
		WHT			P2-17		
		BLK			P2-18		
		RED			P2-19		
		BRN			P2-20		
		ORG			P2-21		
		BRN			P2-22		
		YEL			P2-23		
		BRN			P2-24		
		GRN			P2-25		
		BRN			P2-26		
		BLU			P2-27		
		BRN			P2-28		
		VIO			P2-29		
TWP		BRN	P1	MASS	P2-30	3	

ZETACO

TITLE
EXTERNAL "A" CABLE ASSEMBLY

DOCUMENT NO. 300013-000 A

SHEET 2 **OF** 4 **REV.** 286-C10-2R1-00

WIRE LIST

NOTES	WIRE GAUGE	COLOR	ORIGIN	TERM. METHOD	DESTINATION	TERM. METHOD	REMARKS
TWP		GRY	P1	MASS	P2-31	3	
		BRN			P2-32		
		WHT			P2-33		
		BRN			P2-34		
		ORG			P2-35		
		RED			P2-36		
		YEL			P2-37		
		RED			P2-38		
		GRN			P2-39		
		RED			P2-40		
		BLU			P2-41		
		RED			P2-42		
		VIO			P2-43		
		RED			P2-44		
		GRY			P2-45		
		RED			P2-46		
		WHT			P2-47		
		RED			P2-48		
		YEL			P2-49		
		ORG			P2-50		
		GRN			P2-51		
		ORG			P2-52		
		BLU			P2-53		
		ORG			P2-54		
		VIO			P2-55		
		ORG			P2-56		
		GRY			P2-57		
		ORG			P2-58		
		WHT			P2-59		
TWP		ORG	P1	MASS	P2-60	3	

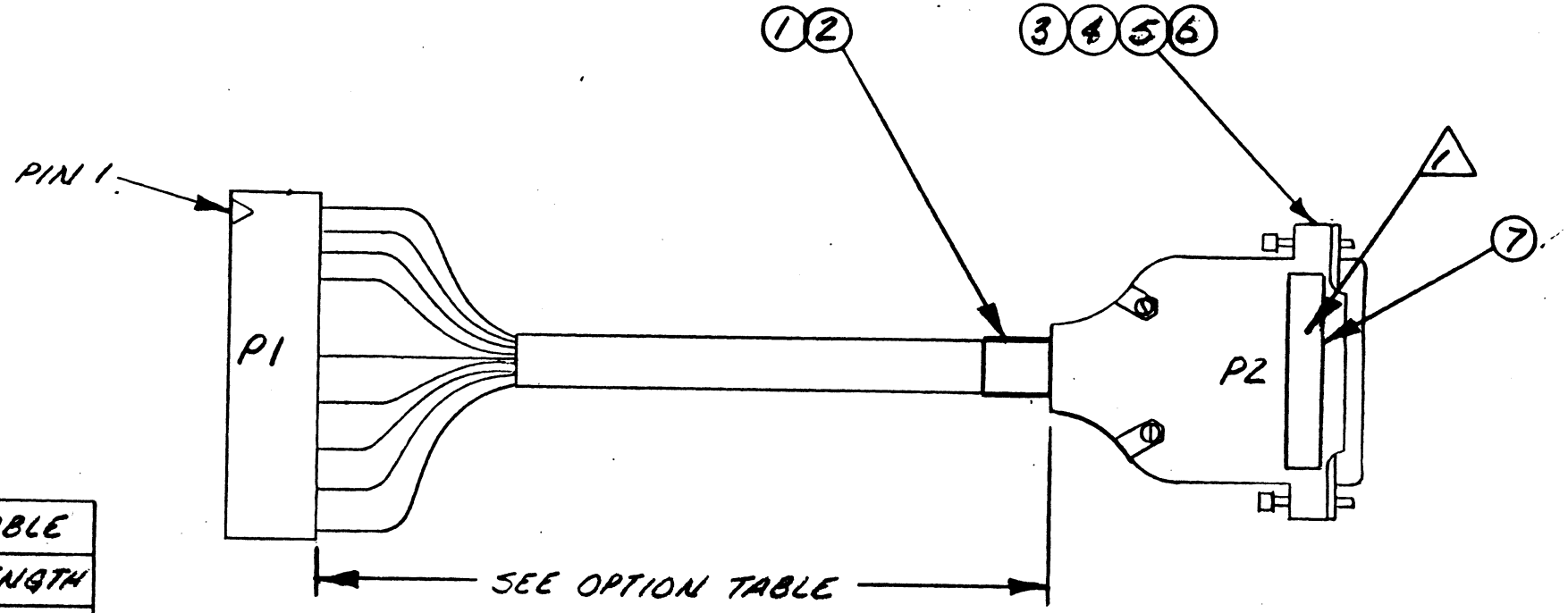
ZETACO

TITLE

EXTERNAL "A" CABLE ASSEMBLY

DOCUMENT NO. 300013-000 A

SHEET 3 **OF** 4 **REV.** 286-C10-2R1-00



OPTION TABLE	
OPTION	LENGTH
001	6'
002	16'

1 STAMP WITH APPROPRIATE PART NUMBER & LENGTH

REVISION HISTORY		
ECO	DATE	DESCRIPTION
0298	5-3-84	PER ECO

ZETACO

DRAWN BY	DATE	REVISIONS	DATE
CHULSETH	9-28-83		
CHECKED BY			
APPROVED BY			
SCALE: ~		286-C10-2R1-00	
TITLE: EXTERNAL "A" CABLE ASS'Y		DRAWING NUMBER: 300013-000 A	

PARTS LIST
ZETACO

OR: EXTERNAL "A" CABLE ASSEMBLY (16')

ASSEMBLY #: 300013-002
PREV. ASSEMBLY #:
REV. LEVEL: A
SCHEMATIC REV. LEVEL:

ITEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION	REFERENCE
1	192	100919-002	CABLE	EXTERNAL "A" 16' REV A	

PARTS LIST
ZETACO

OR: INTERNAL "B" CABLE ASSY

ASSEMBLY #: 300014-000
PREV. ASSEMBLY #: 287-012-250
REV. LEVEL: A
SCHEMATIC REV. LEVEL:

ITEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION	REFERENCE
1	1	100503-000	HEX SETS	D20418-2 MNT HARDWARE SET	
2	1	100546-000	CONN D-SUB	AMP 50P RECPT 205211-2	
3	26	100547-000	CONN PIN	AMP 66505-8	

WIRE LIST

NOTES	WIRE GAUGE	COLOR	ORIGIN	TERM. METHOD	DESTINATION	TERM. METHOD	REMARKS
	28		P1-1	MASS	P2-1	3	
	↑		P1-2	↑	P2-2	↑	
			P1-3		P2-3		
			P1-4		P2-4		
			P1-5		P2-5		
			P1-6		P2-6		
			P1-7		P2-7		
			P1-8		P2-8		
			P1-9		P2-9		
			P1-10		P2-10		
			P1-11		P2-11		
			P1-12		P2-12		
			P1-13		P2-13		
			P1-14		P2-14		
			P1-15		P2-15		
			P1-16		P2-16		
			P1-17		P2-17		
			P1-18		P2-18		
			P1-19		P2-19		
			P1-20		P2-20		
			P1-21		P2-21		
			P1-22		P2-22		
			P1-23		P2-23		
			P1-24		P2-24		
	↓		P1-25	↓	P2-25	↓	
	28		P1-26	MASS	P2-26	3	

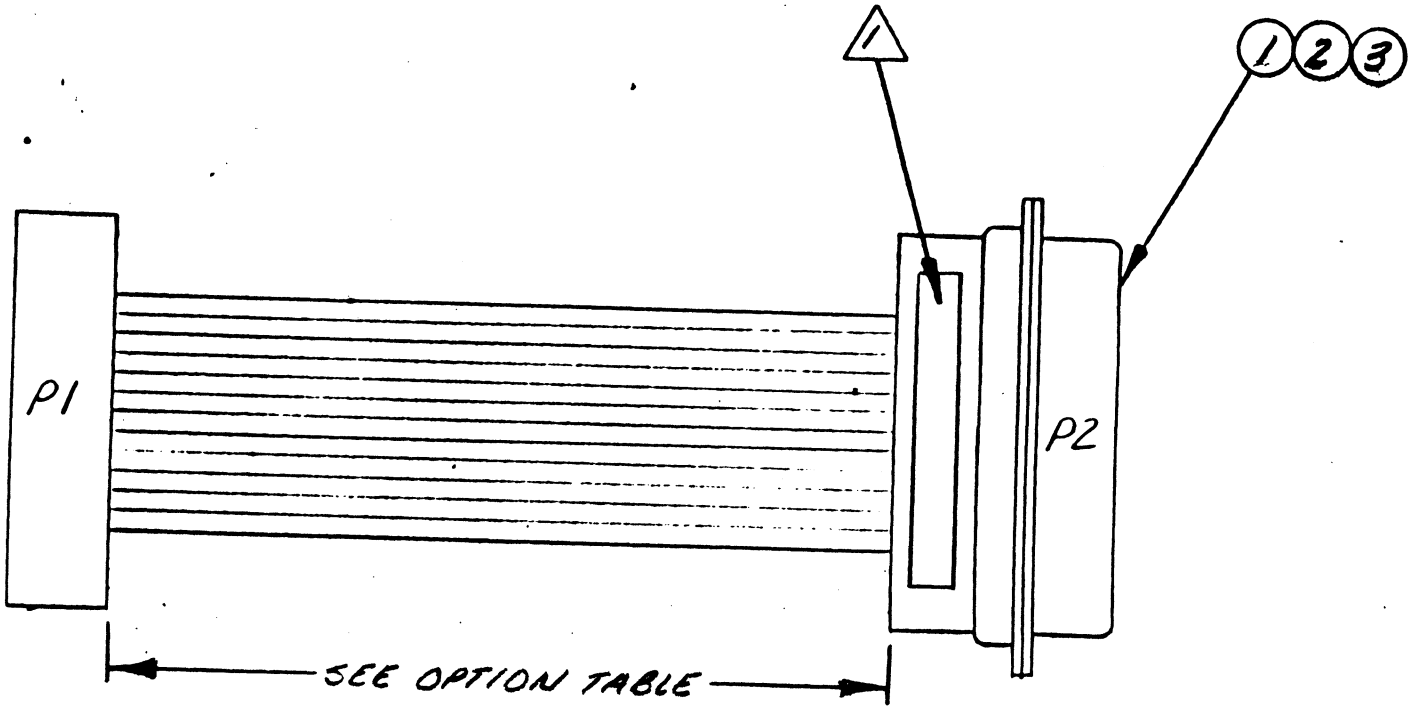
ZETACO

TITLE

INTERNAL "B" CABLE ASSEMBLY

DOCUMENT NO. 300014-000 A

SHEET 2 **OF** 3 **REV.** 287-C12-2S0-00



OPTION TABLE	
OPTION	LENGTH
001	18"
002	36"

1 STAMP WITH APPROPRIATE PART NUMBER & LENGTH

REVISION HISTORY		
ECO	DATE	DESCRIPTION

DRAWN BY C KULSETH	DATE 9-28-83	REVISIONS	DATE
CHECKED BY			
APPROVED BY		Z87-C12-250-00	
SCALE: ~		500377-000	
TITLE: INTERNAL "B" CABLE ASS'Y		DRAWING NUMBER 300014-000	A

PARTS LIST
ZETACO

JR: INTERNAL "B" CABLE ASSY (18")

ASSEMBLY #: 300014-001
PREV. ASSEMBLY #:
REV. LEVEL: A
SCHEMATIC REV. LEVEL:

TEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION	REFERENCE
1	18	100920-001	CABLE	INTERNAL "B" 18" REV A	

PARTS LIST
ZETACO

OR: BMX-1 "A" PADDLE BOARD ASSEMBLY

ASSEMBLY #: 300024-000
PREV. ASSEMBLY #: 276-B25-2E0
REV. LEVEL: A
SCHEMATIC REV. LEVEL:

TEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION	REFERENCE
1	4	100268-000	CONN HDR	26P 3M #3593-5002	
2	5	100542-000	CONN EDGE	CARD 20P CA-ES20-1/16	
3	0	100705-000	LABEL	5552 AVERY	PORT 0,PORT 1, PORT 2, PORT 3
4	1	100868-000	PCB PADDLE	276-A24 BMX-1 "A" REV 0	

PARTS LIST
ZETACO

JR: BMX-1 "B" PADDLE BOARD ASSEMBLY

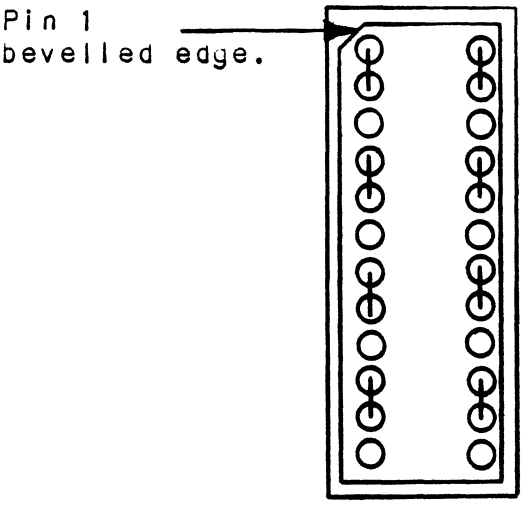
ASSEMBLY #: 300025-000
PREV. ASSEMBLY #: 276-B33-2E0
REV. LEVEL: B
SCHEMATIC REV. LEVEL:

TEM	QTY	PART #	GENERIC DESCRIP.	DESCRIPTION	REFERENCE
1	4	100200-471	RES 470	1/4W 5%	R1,R2,R3,R4
2	1	100262-000	CONN HDR	60P 3M #3372-5002	P1
3	5	100542-000	CONN EDGE	CARD 20P CA-ES20-1/16	
4	3	100573-000	SOC LO PRO	DILB 14P-108T	U2,U3,U4
5	1	100706-000	SOC SLIM	AUGAT 224-AG30D	U1
6	1	100820-000	HEADER	ARIES 10-680-191T	U2,U3,U4
7	1	100821-000	HDR COVER	ARIES 10-655-10	U2,U3,U4
8	1	100822-000	HEADER	ARIES 24-680-190T	U1
9	1	100823-000	HDR COVER	ARIES 24-655-10	U1
10	1	100869-000	PCB PADDLE	BMX-1 "B" REV A	

REWORK FOR BMX-1 "B" PADDLEBOARDS

REWORK FOR BOARD BLANKS #276-A31-2E0:

1. Add wire between the two feedthrus as shown.
2. Cut foil (solder side) at J1-17 and J1-47.
3. Add wire (solder side) from J1-17 to B28.
4. Add wire (solder side) from J1-47 to feedthru connected to B27.
5. Cut foil (component side) at J1-1.
6. Cut foil (solder side) at J1-31.
7. Add wire (component side) from J1-1 to B23.
8. Add wire (solder side) from J1-31 to U1-14 (Pin 1 is next to Mode 1 label).
9. Sockets U2, U3 and U4 are 14 pin sockets cut down to 10 pin. Cut the standoff tabs off the bottom of the sockets.
10. Glue on cover for U1 and add dot of white ink between pins 1 and 24 (center).
11. The above rework for Assembly 300025-000 A.



REWORK FOR BOARD BLANKS #100869-000 A:

1. Rework per Steps 9 and 10 above.
2. The above rework for Assembly 300025-000 B.